

AD-A078 392

BURROUGHS CORP PAOLI PA FEDERAL AND SPECIAL SYSTEMS GROUP F/6 9/2
SOFTWARE MAINTENANCE MANUAL FOR THE MODULAR SYSTEM CONTROL DEVE--ETC(U)
NOV 79

UNCLASSIFIED

66158

SBIE-AD-E100 314

DCA100-76-C-0083

NL

1 OF 4
ADA
078392



AD-E100314

66158

Book 2

LEVEL

November 1979

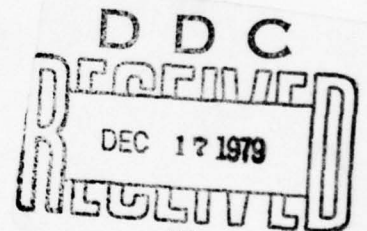
A078391

ADA 078392

SOFTWARE MAINTENANCE MANUAL
FOR THE
MODULAR SYSTEM CONTROL
DEVELOPMENT MODEL (MSCDM)

for

THE DEFENSE COMMUNICATIONS AGENCY
WASHINGTON, D.C. 20305



DDC FILE COPY

THIS DOCUMENT IS BEST QUALITY PRACTICABLE.
THE COPY FURNISHED TO DDC CONTAINED A
SIGNIFICANT NUMBER OF PAGES WHICH DO NOT
REPRODUCE LEGIBLY.

Burroughs Corporation

Federal and Special Systems Group

Paoli, Pa. 19301

This document has been approved
for public release and sale; its
distribution is unlimited.

UNCLASSIFIED

SECURITY CLASSIFICATION OF THIS PAGE (When Data Entered)

REPORT DOCUMENTATION PAGE		READ INSTRUCTIONS BEFORE COMPLETING FORM
1. REPORT NUMBER 66158 ✓	2. GOVT ACCESSION NO.	3. RECIPIENT'S CATALOG NUMBER
4. TITLE (and Subtitle) Software Maintenance Manual for the Modular System Control Development Model (MSCDM) Book 2		5. TYPE OF REPORT & PERIOD COVERED FINAL Sep 76 - Nov 79
		6. PERFORMING ORG. REPORT NUMBER
7. AUTHOR(s)	8. CONTRACT OR GRANT NUMBER(s) DCA100-76-C-0083 ✓	
9. PERFORMING ORGANIZATION NAME AND ADDRESS Burroughs Corporation Federal and Special Systems Group Paoli, PA 19301		10. PROGRAM ELEMENT, PROJECT, TASK AREA & WORK UNIT NUMBERS PE 33126 T&CCP / 3012 Task 15203
11. CONTROLLING OFFICE NAME AND ADDRESS Defense Communications Agency Defense Communications Engineering Center 1860 Wiehle, Ave., Reston, VA 22090		12. REPORT DATE November 1979
		13. NUMBER OF PAGES 284
14. MONITORING AGENCY NAME & ADDRESS (if different from Controlling Office)		15. SECURITY CLASS. (of this report) UNCLASSIFIED
		15a. DECLASSIFICATION/DOWNGRADING SCHEDULE
16. DISTRIBUTION STATEMENT (of this Report) Approved for Public Release; Distribution Unlimited		
17. DISTRIBUTION STATEMENT (of the abstract entered in Block 20, if different from Report)		
18. SUPPLEMENTARY NOTES		
19. KEY WORDS (Continue on reverse side if necessary and identify by block number) Program Listings Modular Architecture Loop Network DCS System Control Ring Network Distributed Computer System		
20. ABSTRACT (Continue on reverse side if necessary and identify by block number) Program descriptions and listings of the Modular System Control Development Model utility programs and diagnostics are presented. This includes the Bootstrap Load Facility for the Ring Architecture.		

9 22 5 039

18 SBIE

Book 2

19 AD-E-100 314

11

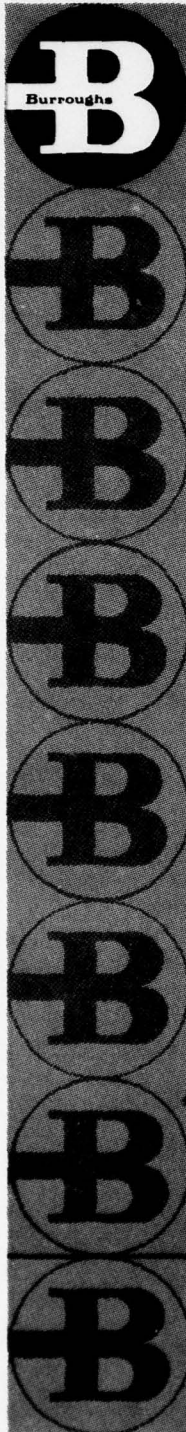
14
66158

11
November 1979

12
286

THIS DOCUMENT IS BEST QUALITY PRACTICABLE.
THE COPY FURNISHED TO DDC CONTAINED A
SIGNIFICANT NUMBER OF PAGES WHICH DO NOT
REPRODUCE LEGIBLY.

DDC
RECEIVED
DEC 17 1979
E



2

SOFTWARE MAINTENANCE MANUAL
FOR THE
MODULAR SYSTEM CONTROL
DEVELOPMENT MODEL (MSCDM).
Book 2.

15 DCH 100-76-C-0083

for

THE DEFENSE COMMUNICATIONS AGENCY
WASHINGTON, D.C. 20305

9 Final rept. Sep 76 - Nov 79.

Burroughs Corporation

Federal and Special Systems Group

Paoli, Pa. 19301

070 040

This document has been approved
for public release and sale; its
distribution is unlimited.

1/3

Foreword

This publication is the Software Maintenance Manual for the Modular System Control Development Model (MSCDM). This manual was prepared by the Burroughs Corporation and is submitted in accordance with the requirements of Contract DCA 100-76-C-0083.

Accession For	
NTAS GRA&I	<input checked="checked" type="checkbox"/>
DDC TAB	<input type="checkbox"/>
Unannounced	<input type="checkbox"/>
Justification	
By _____	
Distribution/	
Availability Codes	
Dist	Avail and/or special
A	

MSCDM SOFTWARE MAINTENANCE MANUAL

Book 1

PAGE

INTRODUCTION

1.0 MSCDM DMCP AND APPLICATION PROGRAMS

1.1 DESCRIPTION OF DMCP

1.2 SSCI NODE 21

1.3 VSQC NODE 22

1.4 DSQC NODE 23

1.5 DBMS NODE 24

1.6 OCRI NODE 25

1.7 BWBSA NODE 26

1.8 FIAC NODE 27

1.9 SDCA NODE 28

1.10 USER LANGUAGE

1.11 SIG / SDCA - 11/40 PROGRAM

Book 2

2.0 UTILITY PROGRAMS 3

2.1 PAGE PROGRAM 4

2.2 FORMAT PROGRAM 29

2.3 STATUS PROGRAM 32

2.4 FDMLDR PROGRAM 36

3.0 MACRO-11 PROGRAMS 56

3.1 DIAGNOSTICS LOOP 5 57

3.2 IEEE PROGRAMS 99

3.3 LOADER PROMS 115

MSCDM SOFTWARE MAINTENANCE MANUAL

Book 2

PAGE

4.0 MODIFICATION TO ESMD SOFTWARE 177

APPENDIX 1	Systems Drawings	232
APPENDIX 2	System Diskettes	235
APPENDIX 3	Startup Procedures	266
APPENDIX 4	Interface Data	272
APPENDIX 5	Acronyms Glossary	278

INTRODUCTION

The Modular System Control Development Model (MSCDM), consists of nine functional nodes: Station-to-Station Communications Interface (SSCI), Voice Service Quality Control (VSQC), Digital Service Quality Control (DSQC), Data Base Management Service (DBMS), Operator Control and Report Interface (OCRI), Baseband Signal Analysis and Wide Band Signal Analysis (BWBSA), Fault Isolation and Control Coordination (FIAC), Switch Data Collection and Analysis (SDCA), and Simulated Input Generator (SIG). Each of these nodes is implemented using microcomputer hardware and software, and node intercommunication is performed via a Burroughs loop architecture under control of a distributed master control program (DMCP).

Each node can communicate with any other node on the loop; however, the nodal software for the MSCDM application defines the flow of information in the system. For example, the OCRI terminal normally communicates with the DBMS node, which runs the User Language. The other ESM terminals communicate with the User Language via the loop 4-5 gateway Node 21 (SSCI).

A simulated input generator (SIG) generates inputs to the VSQC, DSQC and BWBSA, which communicate faults to the FIAC module. FIAC generates event reports to the OCRI and DBMS. The PDP 11/40 in loop 2 generates inputs to the SDCA which generates fault reports to the OCRI and DBMS. The DBMS, OCRI and FIAC communicate with the other loops via SSCI.

An LA36 DECWRITER is to be used as the OCRI hard-copy terminal attached to node 25. A VT52 DECSCOPE is to be used as a local CRT terminal connected to the Program Development Unit (PDU).

The Digital Equipment Corporation System's software for the PDP-11/V03 is contained on 21 - Floppy diskettes conforming to DEC's RX01 Floppy drive format.

The MSCDM applications software which runs on the PDP-11/V03 and the LSI-11/2 nodes is contained on 9 - floppy diskettes.

References for the FORTRAN and MACRO-11 languages and the PDP-11/V03 system used include documentation:

1. "RT-11 System Generation Manual"
2. "Introduction to RT11"
3. "RT-11 System User's Guide"
4. "RT-11 System Message Manual"
5. "PDP-11 MACRO Language Manual"
6. "PDP-11 FORTRAN Language Manual"
7. "Advanced Programmer's Guide"

2.0 UTILITY PROGRAM

	PAGE
2.1 PAGE PROGRAM (PGLOOP)	4
2.2 FORMAT PROGRAM (MSGCON)	29
2.3 STATUS PROGRAM (BDSTAT)	32
2.4 FDMLDR LOOP LOADING PROGRAM	36

2.1 PAGE PROGRAM DESCRIPTION

PGLOOP is a program that allows a user of MSCMD to get a hard copy print out of a source file stored on the PDP11/03's diskettes. Using the NODE 25's LA-36 printer this program is made up of three source files: PGLOOP, WRTLP AND PGMAC. This allows the LA-36 to act as a line printer by providing for FF, tab, and sequence numbers.

2.1.1 Using Page

1. Press Clear Switch on operator's panel
2. Insert Diskette #1 into Drive 0
3. Insert USER's diskette into Drive 1
4. Type .R PGLOOP
5. The CRT screen will then be formatted as follows

```
FILE [          ]  
SEQN [          ]
```
6. Cursor is placed just right bracket of file line
7. Type your file to cpied, e.g., "123456.123"
8. Press RETURN
9. Type "Y or N" for sequence numbers
10. Place LA36 printer head just below fan fold of paper
11. Press RETURN

12. File will then begin to print, when complete the utility will return to step 5, enabling a new file to be typed in.
13. To stop PGLOOP type "END" when it asks for a file name.

NOTE: on files with types of ".LST, .MAP" PGLOOP will not ask for sequence numbers

NOTE: after the first file is printed, step 11 need not be done

2.1.2 Program Descriptions

2.1.2.1 Program PGLOOP (FORTRAN)

This program is the driver for the PGLOOP program; it opens the selected files, formats screen and pages the file for all the control characters.

2.1.2.1 Subroutine HWRITE (FORTRAN)

This subroutine is passed a byte that is written to the VT52.

2.1.2.3 Subroutine HEADER (FORTRAN)

This subroutine builds the line of information at the top of each page (i.e., file name, date, time).

2.1.2.3 Subroutine SPACE (FORTRAN)

This subroutine is passed a count of how many spaces to write.

2.1.2.4 Subroutine LINE (FORTRAN)

This subroutine is passed a count of how many line feeds to write.

2.1.2.5 Subroutine SEQNUM (FORTRAN)

This subroutine appends a sequence number onto a line when SEQN OPTION is selected.

2.1.2.6 Subroutine ACKNAK (FORTRAN)

This subroutine checks the response coming from node 25 for ACKs or NAKs; on a NAK the packet is retransmitted.

2.1.2.7 Subroutine WRITE (FORTRAN)

This subroutine forms the data into packets and writes them to the loop.

2.1.2.8 Subroutine MTAB (MACRO)

This subroutine calculates where the next tab character should be.

2.1.2.9 Subroutine INIT (MACRO)

This subroutine initializes the LIU at node 24.

2.1.2.10 Subroutine LIO (MACRO)

This is the interrupt handler for PGLOOP. It reads packets coming from node 25.

2.1.2.11 Subroutine RAM (MACRO)

This subroutine writes the read address for the node, into the ACRAM on the LIU.

2.1.2.12 Subroutine DABLE (MACRO)

This subroutine disables interrupts from the LIU.

2.1.2.13 Subroutine WTLOOP (MACRO)

This subroutine writes packets to the loop.

PAGE 001

V02.1-1 Tue 27-Mar-79 20:23:55

FORTRAN IV

```

0001 PROGRAM PGLOOP
0002 BYTE CHAR,BUFFER(512),NAME(16),INPF,OUTBF
0003 INTEGER*4 NPAGE,NSEQ
0004 COMMON/BLK1/NAME
0005 COMMON/LOOP1/OUTBF(256),INBF(256)
0006 COMMON/LOOP2/IGOT,IACK,INAK,IILT
0007 COMMON/SAVE/IPNT,IPCNT
0008 CALL DABLE
0009 CALL INIT(1,12,13,14,15)
0010 IF(11.EQ.1) GOTO 60
0011 STOP'ERROR--> BAD INITIALIZE'
0012
0013 C
0014 100 OUTBF(1)=0
0015 OUTBF(2)=6
0016 OUTBF(3)=3
0017 OUTBF(4)=7
0018 CALL WTLOOP(4)
0019 NLINE=0
0020 IPCNT=0
0021 IPNT=3
0022 NSEQ=1
0023 ISW=0
0024 JSW=0
0025 NPAGE=1
0026 NCHAR=0
0027 TYPE 4,('033','007')
0028 TYPE 4,('033','110')
0029 TYPE 4,('033','112')
0030 FORMAT(1X,1A1,1A1)
0031 WRITE(7,11)
0032 11 FORMAT(/,1X,'FILE[
0033 DO 12 I=1,16
0034 NAME(I)=32
0035 DO 14 I=1,2
0036 CALL HWRITE('033)
0037 14 CALL HWRITE('111)
0038 DO 15 I=1,6
0039 CALL HWRITE('033)
0040 15 CALL HWRITE('103)
0041 READ(7,40) JCHAR,(NAME(I), I=1,JCHAR)
0042 40 FORMAT(Q,16A1)
0043 DO 43 I=1,16
0044 IF(NAME(I).NE.'') GOTO 43
0045 IF(NAME(I+1).EQ.'L'.OR.NAME(I+1).EQ.'M'.AND.
0046 1 NAME(I+3).EQ.'P') JSW=1
0047 43 CONTINUE
0048 IF(JSW.EQ.1) GOTO 28
0049 IF(NAME(1).EQ.'E'.AND.NAME(2).EQ.'N'.AND.NAME(3).EQ.'I')
0050 2 GOTO 6
0051 DO 41 I=1,5
0052 CALL HWRITE('033)
0053 41 CALL HWRITE('103)
0054 READ(7,42) IT
0055
0056

```

PAGE 002

FORTRAN IV V02.1-1 Tue 27-Mar-79 20:23:55

```

0057 42 FORMAT(1A1)
0058 C IF(IT.EQ.'Y') ISW=1

0060 28 CALL ASSIGN(6,NAME)
0061 DEFINE FILE 6(300,256,U,I,REC)
0062 IREC=1
0063 CALL WRITE("007")

C
0064 1 READ(6'I,REC,END=5,ERR=2) (BUFFER(I), I=1,512)
0065 FIND(6'I,REC)
0066 IF(I,REC.GT.2.OR.JSW.EQ.1) GOTO 9
0067 CALL HEADER(0,NPAGE)
0068 NPAGE=2
0069 DO 7 I=1,512
0070 IF(BUFFER(I).EQ."000.AND.BUFFER(I+1).EQ."000) GOTO 1
0071 IF(BUFFER(I).NE."014) GOTO 17
0072 NPAGE=NPAGE+1
0073 IF(JSW.NE.1) GOTO 30
0074 IL=66-NLINE
0075 CALL LINE(IL)
0076 NLINE=0
0077 GOTO 17
0078
0079 30 IL=57-NLINE
0080 CALL LINE(IL)
0081 NLINE=0
0082 GOTO 17
0083
0084 17 IF(BUFFER(I-1).EQ."012.AND.ISW.EQ.2) CALL SEQNUM(NSEQ)
0085 IF(ISW.EQ.1.AND.NSEQ.EQ.1) CALL SEQNUM(1)
0086 IF(ISW.EQ.1) ISW=2
0087 IF(BUFFER(I).NE."011) GOTO 8
0088 CALL MTAB(NCHAR,NSPACE)
0089 CALL SPACE(NSPACE-NCHAR)
0090 NCHAR=0
0091 GOTO 7
0092
0093 8 CALL WRITE(BUFFER(I))
0094 NCHAR=NCHAR+1
0095
0096 29 IF(BUFFER(I).NE."012) GOTO 7
0097 NSEQ=NSEQ+1
0098 NLINE=NLINE+1
0099 NCHAR=0
0100 IF(NLINE.LT.57.OR.JSW.EQ.1) GOTO 7
0101 CALL HEADER(1,NPAGE)
0102 NPAGE=NPAGE+1
0103 NLINE=0
0104 CONTINUE
0105 GOTO 1
0106
C
0107 5 IL=61-NLINE
0108 CALL LINE(IL)
0109 IF(JSW.EQ.1) CALL LINE(5)
0110 CALL CLOSE(6)
0111 IF(IPNT.EQ.3) GOTO 61
0112 LO 62 J=IPNT,130
0113 CALL WRITE(0)
0114
0115
0116
0117
0118
0119
0120
0121

```

PAGE 003

Tue 27-Mar-79 20:23:55

FORTRAN IV V02.1-1

```
0122 62 CONTINUE
0123 61 GOTO 100
0124 6 CALL DABLE
0125 STOP 'PAGE'
0126 2 WRITE(7,3)
0127 3 FORMAT(/,1X,'ERROR IN FILE NAME-RETYPE')
0128 GOTO 100
0129 END
C*****
```


PAGE 001

```
FORTRAN IV      V02.1-1      Tue 27-Mar-79 20:24:18
0001      SUBROUTINE HWRITE(NBYTE)
0002      CALL IPOKEB(177566,NBYTE)
0003      1  IF(IPEEK(177564).NE. 200) GOTO 1
0005      RETURN
0006      END
C*****
```

FORTRAN IV V02.1-1 Tue 27-Mar-79 20:24:30 PAGE 001

```

0001 SUBROUTINE HEADER(ITYPE,IPAGE)
0002 BYTE JDATE(9),JTIME(8),TITLE(22),JPAGE(4)
0003 INTEGER*4 IPAGE
0004 REAL*8 TI(3),PI
0005 EQUIVALENCE (TITLE,TI),(PAGE,PI)
0006 COMMON/BLK1/NAME
0007 BYTE NAME(16),PAGE(5)
0008 DATA TI(1),TI(2),TI(3) / 'DEWRITE', 'R' PR, 'OGRAM: ' /
0009 DATA PI, 'PAGE' /
0010 CALL DATE(JDATE)
0011 CALL TIME(JTIME)
C
0012 IF(ITYPE .EQ. 0) CALL LINE(2)
0014 IF(ITYPE .EQ. 1) CALL LINE(6)
C
0016 CALL SPACE(5)
0017 DO 40 I=1,22
0018 IF(TITLE(I) .LE. 90 .AND. TITLE(I) .GE. 65) GOTO 44
0019 CALL WRITE(TITLE(I))
0020 GOTO 40
0021 CALL WRITE(TITLE(I)+32)
0022 44 CONTINUE
0023 CALL SPACE(4)
0024 DO 45 I=1,16
0025 CALL WRITE(NAME(I))
0026 CONTINUE
0027 CALL SPACE(15)
0028 DO 50 I=1,9
0029 CALL WRITE(JDATE(I))
0030 CONTINUE
0031 CALL SPACE(10)
0032 DO 60 I=1,8
0033 CALL WRITE(JTIME(I))
0034 CONTINUE
0035 CALL SPACE(10)
0036 DO 80 I=1,5
0037 CALL WRITE(PAGE(I))
0038 CONTINUE
0039 ENCODE(4,22,JPAGE) IPAGE
0040 FORMAT(4I)
0041 DO 23 I=2,4
0042 IF(JPAGE(I) .LE. 57 .AND. JPAGE(I) .GE. 48) GOTO 25
0043 JPAGE(I)=48
0044 CALL WRITE(JPAGE(I))
0045 CONTINUE
0046 23 CALL LINE(3)
0047 C
0048 RETURN
0049 END
0050 C*****

```

PAGE 001

V02.1-1 Tue 27-Mar-79 20:24:46

FORTRAN IV

```
0001 SUBROUTINE SPACE(ICOUNT)
0002 DO 20 I=1,ICOUNT
0003   CALL WRITE(32)
0004 20 CONTINUE
0005 RETURN
0006 END
C*****
```

PAGE 001

V02.1-1 Tue 27-Mar-79 20:24:58

FORTRAN IV

```
0001 SUBROUTINE LINE(JCOUNT)
0002 CALL WRITE(13)
0003 DO 30 I=1,JCOUNT
0004   CALL WRITE(10)
0005   30 CONTINUE
0006   RETURN
0007 END
C*****
```


PAGE 001

V02.1-1 Tue 27-Mar-79 20:25:09

FORTRAN IV

```
0001 SUBROUTINE SEQNUM(INUM)
0002 INTEGER*4 INUM
0003 BYTE JNUM(4)
0004 ENCODE(4,2,JNUM) INUM
0005 FORMAT(4I)
0006 DO 3 I=1,4
0007 IF(JNUM(I) .LE. 57 .AND. JNUM(I) .GE. 48) GOTO 4
0008 JNUM(I)=32
0009 CALL WRITE(JNUM(I))
0010 4 CALL CONTINUE
0011 3 CALL SPACE(4)
0012 RETURN
0013 END
0014
```

PAGE 001

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:00:39

```
0001 SUBROUTINE ACKNAK(IT)
0002 COMMON/LOOP2/IGOT,IACK,INAK,IFLT
      C
0003 DO 1,I=1,40
0004   DO 4,J=1,1000
0005     IF(IGOT.EQ.1) GOTO 2
0006     4 CONTINUE
0007     1 CONTINUE
0008     2 IF(IACK.NE.1) GOTO 3
0009     IT=1
0010     GOTO 999
0011   3 IT=-1
0012   IGOT=0
0013   IACK=0
0014   INAK=0
0015   IFLT=0
0016   RETURN
0017   ENL
0018
0019
```

PAGE 001

MFORTRAN IV V02.1-11 Mon 21-Oct-79 12:00:36

```

0001 SUBROUTINE WRITE(1BYTE)
0002   BYTE INB,OUTB,1BYTE
0003   COMMON/LOOP1/OUTB(256),INB(256)
0004   COMMON/SAVE/IPNT,IPCNT
0005   OUTB(IPNT)=1BYTE
0006   IPNT=IPNT+1
0007   IF(IPNT.NE.131) GOTO 999
0008   IPCNT=IPCNT+1
0009   IF(IPCNT.EQ.100) IPCNT=1
0010   OUTB(1)=IPCNT
0011   OUTB(2)=7
0012   OUTB(131)=003
0013   OUTB(132)=007
0014   IECNT=1
0015   1 CALL WTLOOP(132)
0016   CALL ACKNAK(1ST)
0017   IF(1ST.EQ.1) GOTO 900
0018   IECNT=IECNT+1
0019   IF(IECNT.LT.3) GOTO 1
0020   CALL DABLE
0021   STOP ERROR--> NO RESPONSE FROM NCLE
0022   900 IPNT=3
0023   999 RETURN
0024   END
0025   C*****
0026
0027
0028

```

PRINTER MACRO'S MACRO V03.01 27-MAR-79 19:51:50 PAGE 1

```

1  .TITLE PRINTER MACRO'S
2  .IDENT /V1.0/
3  .GLOBL WTLOOP,INIT,RAM,DABLE,MTAB
4  .PSECT
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40

```

```

172410 BAR=
172412 WCR=
172414 CSR=
172416 IOBUF=
172416 OPREG=
172416 ARG1=
172416 ARG2=
172416 ARG3=
172416 ARG4=
172416 ARG5=
172416 BIT15=
172416 BIT14=
172416 BIT08=
172416 BIT07=
172416 BIT06=
172416 BIT05=
172416 BIT04=
172416 BIT03=
172416 BIT02=
172416 BIT01=
172416 BIT00=

```

```

172410
172412
172414
172416
172416
000002
000004
000006
000010
000012
100000
040000
000400
000200
000100
000040
000020
000010
000004
000002
000001

```

MTAB: MOV (R5)+, R0

```

CLR R1
MOVB Q(R5)+, R1
INCB R1
BITB #7,R1
BNE ADD
MOV R1,Q(R5)+
RTS PC

```

ADD:

000007

```

012500
005001
113501
105201
132701
001374
010135
000207

```


PRINTER MACRO'S

MACRO V03.01 27-MAR-79 19:51:50 PAGE 2

```

1
2
3
4
5 000022 012737 010420 172416 INIT: MOV #4368., G#OPREG
6 000030 012737 004414 172416 MOV #4414, G#OPREG
7 000036 105737 172414 TSTB G#CSR
8 000042 100375 BPL .-4
9 000044 105037 172414 CLRB G#CSR
10 000050 012737 004540 172416 MOV #4540, G#OPREG
11 000056 105737 172414 TSTB G#CSR
12 000062 100375 BPL .-4
13 000064 105037 172414 CLRB G#CSR
14 000070 012700 000124 MOV #124, R0
15 000074 012720 000764 MOV #L10, (R0)+
16 000100 012710 000340 MOV #340, (R0)
17 000104 005003 CLR R3
18 000106 005067 CLRB ERAM
19 000112 005067 CLR EBUF
20 000116 005067 CLR ESTAT
21
22
23
24 000122 005004 LIMIT: CLR R4
25 000124 052737 BIS #BIT00, G#CSR
26 000132 000240 NOP
27 000134 000240 NOP
28 000136 042737 BIC #BIT00, G#CSR
29 000144 012737 MOV #4354., G#OPREG
30 000152 012737 004400 172416 MOV #2304., G#OPREG
31 000160 105737 172414 TSTB G#CSR
32 000164 100375 BPL .-4
33 000166 105037 172414 CLRB G#CSR
34 000172 012737 010401 172416 MOV #4353., G#OPREG
35 000200 012700 000400 MOV #256., R0
36 000204 012737 004407 172416 MOV #2311., G#OPREG
37 000212 105737 172414 TSTB G#CSR
38 000216 100375 BPL .-4
39 000220 105037 172414 CLRB G#CSR
40 000224 077011 SOB R0, 1$
41 000226 012700 000400 MOV #256., R0
42 000232 012737 001400 172416 MOV #768., G#OPREG
43 000240 105737 172414 TSTB G#CSR
44 000244 100375 BPL .-4
45 000246 105037 172414 CLRB G#CSR
46 000252 013701 172416 MOV #IOBUF, R1
47 000256 042701 177760 BIC #177760, R1
48 000262 022701 000007 CMP #7, R1
49 000266 001403 BEQ 3$
50 000270 005204 INC R4
51 000272 005267 INC ERAM
52 000276 077023 SOB R0, 2$
53
54
55
56 000300 012701 010410 INBUF0: MOV #10410, R1
57 000304 012700 010440 MOV #10440, R0

```

```

;MOD S AT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL
;RESET SWITCHES
;GOOD WD
;LOOP UNTIL
;INTERRUPT VECTOR
;ADDRESS OF LIO
;PRI =340
;CLEAR ERROR COUNT

;CLEAR LOOP COUNT
;INIT LIU

;CLEAR LIU
;LDADR
;ADDRESS=0
;GOOD WD
;NO RETRY

;SEL ACRAM
;COUNTER
;WRITE A NULL
;GOOD WD
;NO LOOP UNTIL READY

;LOOP UNTIL ZERO
;COUNTER
;RD
;GOOD RD
;NO RETRY

;FETCH DATA
;CLEAR BITS
;DATA=NULL
;YES
;NO REPORT IT
;RAM ERROR
;LOOP UNTIL ZERO

;RDBUFADR CMD
;SEL INBUFO

```

PRINTER MACRO'S

MACRO V03.01 27-MAR-79 19:51:50 PAGE 2-1

```

58 000310 004767 000106 PC, ZEROBP
59 000314 012737 001400 #1400, Q#OPREG
60 000322 105737 172414 Q#CSR
61 000326 105037 172414 BPL -4
62 000330 105037 172414 CLRB
63 000334 012701 010610 INBUF1: MOV #10610, R1
64 000340 012700 010640 MOV #10640, R0
65 000344 004767 000052 PC, ZEROBP
66 000350 012737 001400 Q#OPREG
67 000356 105737 172414 TSTB
68 000362 100375 172414 BPL -4
69 000364 105037 172414 CLRB
70 000370 012701 010510 OTEBUF0: MOV #10510, R1
71 000374 012700 010540 MOV #10540, R0
72 000400 004767 000016 PC, ZEROBP
73 000404 012701 010710 OTEBUF1: MOV #10710, R1
74 000410 012700 010740 MOV #10740, R0
75 000414 004767 000002 PC, ZEROBP
76 000420 000464 BR STAT
77 000422 010137 172416 ZEROBP: MOV R1, Q#OPREG
78 000426 012737 001400 MOV #1400, Q#OPREG
79 000434 105737 172414 TSTB
80 000440 100375 172414 BPL -4
81 000442 105037 172414 CLRB
82 000446 013702 172416 MOV #10800, R2
83 000452 042702 174000 BIC #177400, R2
84 000456 010037 172416 MOV R0, Q#OPREG
85 000462 022702 000000 4$: CMP R0, R2
86 000466 001412 BEQ 5$
87 000470 012737 001400 MOV #1400, Q#OPREG
88 000476 105737 172414 TSTB
89 000502 100375 172414 BPL -4
90 000504 105037 172414 CLRB
91 000510 005302 DEC R2
92 000512 000763 BR 4$
93 000514 010137 172416 MOV R1, Q#OPREG
94 000520 012737 001400 MOV #1400, Q#OPREG
95 000526 105737 172414 TSTB
96 000532 100375 172414 BPL -4
97 000534 105037 172414 CLRB
98 000540 013702 172416 MOV #10800, R2
99 000544 042702 174000 BIC #177400, R2
100 000550 022702 000000 CMP #0, R2
101 000554 001403 BEQ 6$
102 000556 005204 INC R4
103 000560 005267 INC EBUF
104 000564 010037 172416 MOV R0, Q#OPREG
105 000570 000207 RTS PC
106
107
108 INITIALIZE STATUS
109 000572 012737 010400 MOV #4352, Q#OPREG
110 000600 012737 002400 MOV #1280, Q#OPREG
111 000606 012737 002400 MOV #1280, Q#OPREG
112 000614 013700 172416 MOV Q#OPREG, R0
113 000620 042700 177400 BIC #177400, R0
114 000624 022700 000000 CMP #0, R0

```

```

;SET POINTER=0
;FALSE READ DATA
;GOOD READ
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL INBUF1 COMMAND
;POINTER=0
;FALSE READ DATA
;GOOD RD
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;ZERO BUFFER POINTER
;GO CLEAR STATUS
;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH POINTER
;CLEAR MST BYTE
;SEL BUFFER
;POINTER=0
;NO FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;POINTER-1
;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH NEW POINTER
;CLEAR MST BYTE
;POINTER=0
;REPORT ERROR
;BUFFER ERROR
;SEL BUFFER
;RETURN
;WCR:R
;RS(FALSE)
;RS
;FETCH STATUS
;CLEAR MST BYTE
;STATUS=0 ?

```

MACRO V03.01 27-MAR-79 19:51:50 PAGE 2-2

[illegible]


```

1
2
3
4 000764 01004E
5 000766 01014E
6 000770 01024E
7 000772 01034E
8 000774 01044E
9 000776 01054E
10 001000 005067 000726
11 001004 005737 172414
12 001010 100077
13 001012 042737 040000 172414
14 001020 012737 010400 172416
15 001026 012737 002400 172416
16 001034 013701 172416
17 001040 042701 177400
18
19
20
21 001044 132701 000004
22 001050 001406
23 001052 012702 010410
24 001056 012703 010440
25 001062 004767 000146
26 001066 132701 000010
27 001072 001406
28 001074 012702 010610
29 001100 012703 010640
30 001104 004767 000124
31
32
33
34 001110 132701 000001
35 001114 001415
36 001116 012702 010410
37 001122 012703 010440
38 001126 012704 000001
39 001132 004767 000076
40 001136 005767 000570
41 001142 100402
42 001144 004767 000264
43 001150 132701 000002
44 001154 001415
45 001156 012702 010610
46 001162 012703 010640
47 001166 012704 000002
48 001172 004767 000036
49 001176 005767 000530
50 001202 100402
51 001204 004767 000224
52
53 001210 012605
54 001212 012604
55 001214 012603
56 001216 012602
57 001220 012601

;LIU HANDLER
L10: MOV R0, -(SP)
      MOV R1, -(SP)
      MOV R2, -(SP)
      MOV R3, -(SP)
      MOV R4, -(SP)
      MOV R5, -(SP)
      DATA
      CLR G#CSR
      TST RTI$
      BPL #BIT14, G#CSR
      BIC #4352., G#OPREG
      MOV #1280., G#OPREG
      MOV G#IOBUF, R1
      BIC #177400, R1

;CLEAR VAR.
;INT
;NO RETURN
;DISABLE
;ACR : RS(0)
;RD
;CLEAR UNUSED BITS

;SAVE REGISTERS
;OV-FL
;COM=SEL INBUF 0
;COM=RDBUFPNT
;EMPTY BUFFER
;OV-FL
;COM=SEL INBUF 1
;COM=RDBUFPNT
;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUFADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO EMPTY AND LOAD
;GO CHECK PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUFADR
;COM=SEL INBUF1
;WHICH CRC BIT
;EMPTY AND LOAD
;GO CHECK PACKET
;RESTORE REGISTERS

```


PRINTER MACRO'S

MACRO V03.01 27-MAR-79 19:51:50 PAGE 3-1

```

58 001222 012600      MOV      (SP)+, R0
59 001224 052737      BIS      #BIT14, G#CSR
60 001232 000002      RTI
61
62      ; EMPTY BUFFERS
63
64 001234 012737      EMBF:    MOV      #4480., G#OPREG
65 001242 012737      MOV      #1280., G#OPREG
66 001250 013705      MOV      G#IOBUF, R5
67 001254 130405      BITB     R4, R5
68 001256 001003      BNE      5$
69 001260 012767      MOV      #1., DATA
70 001266 012737      MOV      #INFF, G#BAR
71 001274 010237      MOV      R2, G#OPREG
72 001300 012737      MOV      #768., G#OPREG
73 001306 105737      TSTB     G#CSR
74 001312 100375      BPL      .-4
75 001314 105037      CLRB     G#CSR
76 001320 013704      MOV      G#IOBUF, R4
77 001324 042704      BIC      #177400, R4
78 001330 005404      NEG      R4
79 001332 010437      MOV      R4, G#WCR
80 001336 010337      MOV      R3, G#OPREG
81 001342 012737      MOV      #768., G#OPREG
82 001350 105737      TSTB     G#CSR
83 001354 100375      BPL      .-4
84 001356 105037      CLRB     G#CSR
85 001362 012737      MOV      #8704., G#OPREG
86 001370 000240      NOP
87 001372 105737      TSTB     G#CSR
88 001376 100403      BMI      7$
89 001400 012767      MOV      #1., DATA
90 001406 105037      CLRB     G#CSR
91 001412 012737      MOV      #2304., G#OPREG
92 001420 105737      TSTB     G#CSR
93 001424 100375      BPL      .-4
94 001426 105037      CLRB     G#CSR
95 001432 000207      RTS      PC
96      .ENABLEF LSB
97
98 001434 012702      PARSE:   MOV      #INFF, R2
99 001440 116203      MOV      1(R2), R3
100 001444 122703      CMPB     #2, R3
101 001450 001007      BNE      2$
102 001452 012767      MOV      #1., IGOT
103 001460 000207      MOV      #1., IACK
104 001466 000207      RTS      PC
105 001470 122703      CMPB     #3, R3
106 001474 001007      BNE      3$
107 001476 012767      MOV      #1., IGOT
108 001504 012767      MOV      #1., INAK
109 001512 000207      RTS      PC
110 001514 012767      MOV      #1., IFLT
111 001522 012767      MOV      #1., IGOT
112 001530 000207      RTS      PC
113

```

```

;RS (CRC)
;RS CMD
;FETCH STATUS
;CRC OK
;YES CONTINUE
;FLAG BAD CRC
;ADDRESS TO WRITE
;RTBUFADR
;READ POINTER
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;READ POINTER
;CLEAR UNUSED BITS
;2'S COMP
;BYTE COUNT
;SEL BUFFER
;FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;FIRE DMA RD
;DELAY
;GOOD DMA
;YES
;BAD DMA XFER
;CLEAR CSR
;RESET POINTER TO 255
;GOOD WD
;NO RETRY
;CLEAR DONE BIT
;ADDRESS OF PACKET
;FETCH COMMAND
;=ACK
;YES REPORT
;RETURN
;=NAK
;YES REPORT
;FLAG

```

PRINTER MACRO'S

MACRO V03.01 27-MAR-79 19:51:50 PAGE 4

```

1
2
3
4 001532 017500 000002 RAM: MOV GARG1(R5), R0
5 001536 017501 000004 MOV GARG2(R5), R1
6 001542 012737 010402 172416 MOV #4354., G#OPREG
7 001550 062700 004400 ADD #2304., R0
8 001554 010037 172416 MOV R0, G#OPREG
9 001560 105737 172414 TSTB G#CSR
10 001564 100375 -4 BPL -4
11 001566 105037 172414 CLRB G#CSR
12 001572 012737 010401 MOV #4353., G#OPREG
13 001600 062701 004400 ADI #2304., R1
14 001604 010137 172416 MOV R1, G#OPREG
15 001610 105737 172414 TSTB G#CSR
16 001614 100375 -4 BPL -4
17 001616 105037 172414 CLRB G#CSR
18 001622 000207 RTS PC
19
20
21
22 001624 042737 040000 172414 DABLE: BIC #BIT14, G#CSR
23 001632 000207 RTS PC
24
25
26
27 001634 017502 000002 WTLOOP: MOV GARG1(R5), R2
28 001640 012737 000000 NEG R2
29 001646 005402 MOV R2, G#WCR
30 001650 010237 172412 MOV #4448., G#OPREG
31 001654 012737 010540 172416 MOV #10240., G#OPREG
32 001662 012737 024000 172416 NOP
33 001670 000240 TSTB G#CSR
34 001672 105737 172414 BMI CMD
35 001676 100401 HALT
36 001700 000000 MOV #4368., G#OPREG
37 001702 012737 010420 172416 MOV #4421., G#OPREG
38 001710 012737 004421 172416 TSTB G#CSR
39 001716 105737 172414 BPL -4
40 001722 100375 CLRB G#CSR
41 001724 105037 172414 RTS PC
42 001730 000207
43
44
45 001732 .PSECT
46
47 001732 000000 DATA: .WORD 0
48 001734 .BLKW ERAM: .BLKW
49 001736 .BLKW EBUF: .BLKW
50 001740 .BLKW ESTAT: .BLKW
51
52 000000 .PSECT LOOP1,RW,D,GBL,REL,OVR
53 000000 .BLKB 256.
54 000400 .BLKB 256.
55
56 000000 .PSECT LOOP2,RW,D,GBL,REL,OVR
57 000000 .BLKW

```

```

;ADDRESS
;WRITE DATA
;SEL LDACR
;WD/DATA (ADDR)
;WRITE DATA
;VALID WRITE
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;SEL ACRAM
;WD/DATA (CMD)
;WRITE
;VALID WRITE
;NO LOOP UNTIL READY
;CLEAR DONE BIT

```

;DIABLE LIU

```

;BYTE COUNT
;BUS ADDRESS
;2'S COMP COUNT
;COUNT
;OB0 COMMAND
;DMA GO
;INTERFACE TIME
;DMA OK
;YES

```

```

;MODSTAT COMMAND
;BUFFERS FULL
;GOOD WRITE
;NO LOOP UNTIL
;CLEAR DONE BIT

```

PRINTER MACRO'S

MACRO V03.01 27-MAR-79 19:51:50 PAGE 4-1

58 000002
59 000004
60 000006
61
62
63

IACK: .BLKW
INAK: .BLKW
IFLT: .BLKW

000001

.END

MACRO V03.01 27-MAR-79 19:51:50 PAGE 4-2

PRINTER MACRO'S
SYMBOL TABLE

ADD	= 000006R	INAK	000004R	003	OTBUF0	000370R	002
ARG1	= 000002	INBF	000400R	002	OTBUF1	000404R	
ARG2	= 000004	INBUF0	000300R		OUTBF	000000R	
ARG3	= 000006	INBUF1	000334R		PARSE	001434R	
ARG4	= 000010	INIT	000022RG		RAM	001532RG	
ARG5	= 000012	IOBUF	= 172416		RTI\$	001210R	
BAR	= 172410	LINIT	000122R		STAT	000572R	
BIT00	= 000001	LIO	000764R		WCR	= 172412	
BIT01	= 000002	MTAB	000000RG	003	WTLOOP	001634RG	
BIT02	= 000004	OPREG	= 172416	003	ZEROBP	000422R	
BIT03	= 000010			003			

. ABS. 000000 000
 LOOP1 001742 001
 LOOP2 001000 002
 LOOP3 000010 003
 ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 300 WORDS (2 PAGES)
 DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
 .DK:PGMAC=DK:PGMAC

decipherer program: FOLDOF.COH 01-JUL-79 12:00:00 PAGE 001

ASS DX0: DK:
K LINE
DX1:FOLDOF=DX1:FOLDOF/C
DX1:MKTF/C
DX1:FGMAC/

ASS DX1: DK:

2.2 FORMAT PROGRAM

2.2.1 Using the MSGCON Program

This program is found on diskette #3A, and is run by typing "RUN MSGCON" after the PDU power is up as per the USER MANUAL. At the prompt "ENTER INPUT FILE*", type the desired input source file (must be contained on DX1:, diskette #3A). At prompt "ENTER OUTPUT FILE*", type the name of the file to be formed with 80 byte records. This formats the file and stops.

2.2.2 Program Description

The program reads an input file name and an output file name from the user and opens the input file as read only. The program reads blocks of the file and writes 80 byte records to the output file for each line of text of the input file.

IFORTTRAN IV V02.1-11 Mon 01-Oct-79 12:00:44

```

0001      PROGRAM MSGCON
0002      BYTE IBUF,TRUF,COUNT
0003      INTEGER STARTS,ENDS,TSTART,TEND,NREC,MREC,RECNUM
0004      DIMENSION IBUF(512),TRUF(S0)
0005      WRITE(7,2)
0006      FORMAT(5,'ENTER INPUT FILE ')
0007      CALL ASSIGN(8,-1)
0008      WRITE(7,4)
0009      FORMAT(5,'ENTER OUTPUT FILE ')
0010      CALL ASSIGN(9,-1)
0011      DEFINE FILE 8(200,256,U,NREC)
0012      DEFINE FILE 9(500,40,U,MREC)
0013      NREC=1
0014      MREC=1
0015      STARTS=1
0016      READ (8,NREC,END=500)(IBUF(I),I=1,512)
0017      I=1
0018      COUNT=0
0019      CONTINUE
0020      IF (IBUF(I) .EQ. "15") GOTO 30
0021      I=I+1
0022      GOTO 20
0023      COUNT=COUNT+1
0024      IF (COUNT .EQ. 500) GOTO 40
0025      GOTO 20
0026      ENDS=I-1
0027      WRITE (9,MREC,END=500)(IBUF(J),J=STARTS,ENDS)
0028      STARTS=ENDS+3
0029      I=I+1
0030      GOTO 15
0031      I=0
0032      DO 44 J=1,80
0033      TRUF(J)=0
0034      CONTINUE
0035      DO 50 J=STARTS,512
0036      I=I+1
0037      TRUF(I)=IBUF(J)
0038      TSTART=I+1
0039      READ(8,NREC,END=500)(IBUF(I),I=1,512)
0040      I=1
0041      IF (IBUF(I) .EQ. "15") GOTO 70
0042      I=I+1
0043      GOTO 60
0044      K=0
0045      TEND=TSTART+I-2
0046      DO 80 J=TSTART,TEND
0047

```

PAGE 002

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:00:44

```

0048      K=K+1
0049      TBUF(J)=TBUF(K)
0050      WRITE (9,MREC,END=500)(TBUF(J),J=1,TEND)
0051      STARTB=I+2
0052      I=I+1
0053      GOTO 15
0054      CONTINUE
0055      DO 550 J=1,80
0056      TBUF(J)=0
0057      WRITE(9,MREC,END=600)(TBUF(J),J=1,80)
0058      GOTO 560
0059      CALL CLOSE(8)
0060      CALL CLOSE(9)
0061      STOP 'CONVERT'
0062      END

```


2.3 STATUS PROGRAM

2.3.1 Description and Use of Status Program (BDSTAT):

The program is run by first power-up the PDU as per Section 1 and typing "RUN BDSTAT", the program opens a file called "STATUS. MAS" and writes 1015 records contain green channel and link data, status records, later to be renamed to "STATUS. DAT" which is used by MSCDM user language. This program is only used when the STATUS. MAS is corrupted or deleted.

PAGE 001

```

AFORTRAN IV      V02.1-11      Mon 01-Oct-79 12:00:55
0001      C      PROGRAM BDSTAT
0002      BUILD STATUS FILE FOR MSCDM
0003      BYTE BD4,OUT(20)
0004      INTEGER*4 ID4
0005      REAL*8 I1,I2,I3
0006      REAL*4 I4
0007      DATA I1,I2,I3/'AAAAAAA','BBBBBBB',' 11110'/
0008      DATA I4/'XXXX'/
0009      COMMON/IM/BD4(4)
0010      CALL ASSIGN(1,'STATUS.MAS')
0011      DEFINE FILE 1(1014,I0,U,I,REC)
0012      IREC=1
0013      DO 100 J=1,500
0014      JJ=J
0015      IS=JICVT(JJ,ID4)
0016      CALL IMAGE(ID4)
0017      ENCODE(8,101,OUT(1)) I1
0018      101 FORMAT(A8)
0019      DO 102 I=1,4
0020      OUT(I+4)=BD4(I)
0021      DO 103 I=1,4
0022      OUT(I+9)=BD4(I)
0023      OUT(14)=3
0024      OUT(15)=3
0025      DO 104 I=1,20
0026      OUT(I)=3
0027      WRITE(1,J)(OUT(I),I=1,20)
0028      WRITE(7,105)(OUT(I),I=1,20)
0029      105 FORMAT(1X,20A1)
0030      100 CONTINUE
0031      C
0032      DO 200 J=501,1000
0033      JJ=J
0034      IS=JICVT(JJ,ID4)
0035      CALL IMAGE(ID4)
0036      ENCODE(8,201,OUT(1)) I2
0037      201 FORMAT(A8)
0038      DO 202 I=1,4
0039      OUT(I+4)=BD4(I)
0040      DO 203 I=1,4
0041      OUT(I+9)=BD4(I)
0042      OUT(14)=3
0043      OUT(15)=3
0044      DO 204 I=1,20
0045      OUT(I)=3
0046      WRITE(1,J)(OUT(I),I=1,20)
0047      WRITE(7,205)(OUT(I),I=1,20)
0048      205 FORMAT(1X,20A1)
0049      200 CONTINUE
0050      C
0051      DO 300 J=1001,1003
0052      JJ=J
0053      IS=JICVT(JJ,ID4)
0054      CALL IMAGE(ID4)
0055      ENCODE(8,301,OUT(1)) I3
0056      301 FORMAT(A8)
0057      DO 302 I=1,4
0058      OUT(I+4)=BD4(I)
0059      DO 303 I=1,4
0060      OUT(I+9)=BD4(I)
0061      OUT(14)=3
0062      OUT(15)=3
0063      DO 304 I=1,20
0064      OUT(I)=3
0065      WRITE(1,J)(OUT(I),I=1,20)
0066      WRITE(7,305)(OUT(I),I=1,20)
0067      305 FORMAT(1X,20A1)
0068      300 CONTINUE
0069      C
0070      DO 400 J=1004,1006
0071      JJ=J
0072      IS=JICVT(JJ,ID4)
0073      CALL IMAGE(ID4)
0074      ENCODE(8,401,OUT(1)) I4
0075      401 FORMAT(A8)
0076      DO 402 I=1,4
0077      OUT(I+4)=BD4(I)
0078      DO 403 I=1,4
0079      OUT(I+9)=BD4(I)
0080      OUT(14)=3
0081      OUT(15)=3
0082      DO 404 I=1,20
0083      OUT(I)=3
0084      WRITE(1,J)(OUT(I),I=1,20)
0085      WRITE(7,405)(OUT(I),I=1,20)
0086      405 FORMAT(1X,20A1)
0087      400 CONTINUE
0088      C
0089      DO 500 J=1007,1009
0090      JJ=J
0091      IS=JICVT(JJ,ID4)
0092      CALL IMAGE(ID4)
0093      ENCODE(8,501,OUT(1)) I5
0094      501 FORMAT(A8)
0095      DO 502 I=1,4
0096      OUT(I+4)=BD4(I)
0097      DO 503 I=1,4
0098      OUT(I+9)=BD4(I)
0099      OUT(14)=3
0100      OUT(15)=3
0101      DO 504 I=1,20
0102      OUT(I)=3
0103      WRITE(1,J)(OUT(I),I=1,20)
0104      WRITE(7,505)(OUT(I),I=1,20)
0105      505 FORMAT(1X,20A1)
0106      500 CONTINUE
0107      C
0108      DO 600 J=1010,1012
0109      JJ=J
0110      IS=JICVT(JJ,ID4)
0111      CALL IMAGE(ID4)
0112      ENCODE(8,601,OUT(1)) I6
0113      601 FORMAT(A8)
0114      DO 602 I=1,4
0115      OUT(I+4)=BD4(I)
0116      DO 603 I=1,4
0117      OUT(I+9)=BD4(I)
0118      OUT(14)=3
0119      OUT(15)=3
0120      DO 604 I=1,20
0121      OUT(I)=3
0122      WRITE(1,J)(OUT(I),I=1,20)
0123      WRITE(7,605)(OUT(I),I=1,20)
0124      605 FORMAT(1X,20A1)
0125      600 CONTINUE
0126      C
0127      DO 700 J=1013,1015
0128      JJ=J
0129      IS=JICVT(JJ,ID4)
0130      CALL IMAGE(ID4)
0131      ENCODE(8,701,OUT(1)) I7
0132      701 FORMAT(A8)
0133      DO 702 I=1,4
0134      OUT(I+4)=BD4(I)
0135      DO 703 I=1,4
0136      OUT(I+9)=BD4(I)
0137      OUT(14)=3
0138      OUT(15)=3
0139      DO 704 I=1,20
0140      OUT(I)=3
0141      WRITE(1,J)(OUT(I),I=1,20)
0142      WRITE(7,705)(OUT(I),I=1,20)
0143      705 FORMAT(1X,20A1)
0144      700 CONTINUE
0145      C
0146      DO 800 J=1016,1018
0147      JJ=J
0148      IS=JICVT(JJ,ID4)
0149      CALL IMAGE(ID4)
0150      ENCODE(8,801,OUT(1)) I8
0151      801 FORMAT(A8)
0152      DO 802 I=1,4
0153      OUT(I+4)=BD4(I)
0154      DO 803 I=1,4
0155      OUT(I+9)=BD4(I)
0156      OUT(14)=3
0157      OUT(15)=3
0158      DO 804 I=1,20
0159      OUT(I)=3
0160      WRITE(1,J)(OUT(I),I=1,20)
0161      WRITE(7,805)(OUT(I),I=1,20)
0162      805 FORMAT(1X,20A1)
0163      800 CONTINUE
0164      C
0165      DO 900 J=1019,1021
0166      JJ=J
0167      IS=JICVT(JJ,ID4)
0168      CALL IMAGE(ID4)
0169      ENCODE(8,901,OUT(1)) I9
0170      901 FORMAT(A8)
0171      DO 902 I=1,4
0172      OUT(I+4)=BD4(I)
0173      DO 903 I=1,4
0174      OUT(I+9)=BD4(I)
0175      OUT(14)=3
0176      OUT(15)=3
0177      DO 904 I=1,20
0178      OUT(I)=3
0179      WRITE(1,J)(OUT(I),I=1,20)
0180      WRITE(7,905)(OUT(I),I=1,20)
0181      905 FORMAT(1X,20A1)
0182      900 CONTINUE
0183      C
0184      DO 1000 J=1022,1024
0185      JJ=J
0186      IS=JICVT(JJ,ID4)
0187      CALL IMAGE(ID4)
0188      ENCODE(8,1001,OUT(1)) I10
0189      1001 FORMAT(A8)
0190      DO 1002 I=1,4
0191      OUT(I+4)=BD4(I)
0192      DO 1003 I=1,4
0193      OUT(I+9)=BD4(I)
0194      OUT(14)=3
0195      OUT(15)=3
0196      DO 1004 I=1,20
0197      OUT(I)=3
0198      WRITE(1,J)(OUT(I),I=1,20)
0199      WRITE(7,1005)(OUT(I),I=1,20)
0200      1005 FORMAT(1X,20A1)
0201      1000 CONTINUE
0202      C
0203      DO 1100 J=1025,1027
0204      JJ=J
0205      IS=JICVT(JJ,ID4)
0206      CALL IMAGE(ID4)
0207      ENCODE(8,1101,OUT(1)) I11
0208      1101 FORMAT(A8)
0209      DO 1102 I=1,4
0210      OUT(I+4)=BD4(I)
0211      DO 1103 I=1,4
0212      OUT(I+9)=BD4(I)
0213      OUT(14)=3
0214      OUT(15)=3
0215      DO 1104 I=1,20
0216      OUT(I)=3
0217      WRITE(1,J)(OUT(I),I=1,20)
0218      WRITE(7,1105)(OUT(I),I=1,20)
0219      1105 FORMAT(1X,20A1)
0220      1100 CONTINUE
0221      C
0222      DO 1200 J=1028,1030
0223      JJ=J
0224      IS=JICVT(JJ,ID4)
0225      CALL IMAGE(ID4)
0226      ENCODE(8,1201,OUT(1)) I12
0227      1201 FORMAT(A8)
0228      DO 1202 I=1,4
0229      OUT(I+4)=BD4(I)
0230      DO 1203 I=1,4
0231      OUT(I+9)=BD4(I)
0232      OUT(14)=3
0233      OUT(15)=3
0234      DO 1204 I=1,20
0235      OUT(I)=3
0236      WRITE(1,J)(OUT(I),I=1,20)
0237      WRITE(7,1205)(OUT(I),I=1,20)
0238      1205 FORMAT(1X,20A1)
0239      1200 CONTINUE
0240      C
0241      DO 1300 J=1031,1033
0242      JJ=J
0243      IS=JICVT(JJ,ID4)
0244      CALL IMAGE(ID4)
0245      ENCODE(8,1301,OUT(1)) I13
0246      1301 FORMAT(A8)
0247      DO 1302 I=1,4
0248      OUT(I+4)=BD4(I)
0249      DO 1303 I=1,4
0250      OUT(I+9)=BD4(I)
0251      OUT(14)=3
0252      OUT(15)=3
0253      DO 1304 I=1,20
0254      OUT(I)=3
0255      WRITE(1,J)(OUT(I),I=1,20)
0256      WRITE(7,1305)(OUT(I),I=1,20)
0257      1305 FORMAT(1X,20A1)
0258      1300 CONTINUE
0259      C
0260      DO 1400 J=1034,1036
0261      JJ=J
0262      IS=JICVT(JJ,ID4)
0263      CALL IMAGE(ID4)
0264      ENCODE(8,1401,OUT(1)) I14
0265      1401 FORMAT(A8)
0266      DO 1402 I=1,4
0267      OUT(I+4)=BD4(I)
0268      DO 1403 I=1,4
0269      OUT(I+9)=BD4(I)
0270      OUT(14)=3
0271      OUT(15)=3
0272      DO 1404 I=1,20
0273      OUT(I)=3
0274      WRITE(1,J)(OUT(I),I=1,20)
0275      WRITE(7,1405)(OUT(I),I=1,20)
0276      1405 FORMAT(1X,20A1)
0277      1400 CONTINUE
0278      C
0279      DO 1500 J=1037,1039
0280      JJ=J
0281      IS=JICVT(JJ,ID4)
0282      CALL IMAGE(ID4)
0283      ENCODE(8,1501,OUT(1)) I15
0284      1501 FORMAT(A8)
0285      DO 1502 I=1,4
0286      OUT(I+4)=BD4(I)
0287      DO 1503 I=1,4
0288      OUT(I+9)=BD4(I)
0289      OUT(14)=3
0290      OUT(15)=3
0291      DO 1504 I=1,20
0292      OUT(I)=3
0293      WRITE(1,J)(OUT(I),I=1,20)
0294      WRITE(7,1505)(OUT(I),I=1,20)
0295      1505 FORMAT(1X,20A1)
0296      1500 CONTINUE
0297      C
0298      DO 1600 J=1040,1042
0299      JJ=J
0300      IS=JICVT(JJ,ID4)
0301      CALL IMAGE(ID4)
0302      ENCODE(8,1601,OUT(1)) I16
0303      1601 FORMAT(A8)
0304      DO 1602 I=1,4
0305      OUT(I+4)=BD4(I)
0306      DO 1603 I=1,4
0307      OUT(I+9)=BD4(I)
0308      OUT(14)=3
0309      OUT(15)=3
0310      DO 1604 I=1,20
0311      OUT(I)=3
0312      WRITE(1,J)(OUT(I),I=1,20)
0313      WRITE(7,1605)(OUT(I),I=1,20)
0314      1605 FORMAT(1X,20A1)
0315      1600 CONTINUE
0316      C
0317      DO 1700 J=1043,1045
0318      JJ=J
0319      IS=JICVT(JJ,ID4)
0320      CALL IMAGE(ID4)
0321      ENCODE(8,1701,OUT(1)) I17
0322      1701 FORMAT(A8)
0323      DO 1702 I=1,4
0324      OUT(I+4)=BD4(I)
0325      DO 1703 I=1,4
0326      OUT(I+9)=BD4(I)
0327      OUT(14)=3
0328      OUT(15)=3
0329      DO 1704 I=1,20
0330      OUT(I)=3
0331      WRITE(1,J)(OUT(I),I=1,20)
0332      WRITE(7,1705)(OUT(I),I=1,20)
0333      1705 FORMAT(1X,20A1)
0334      1700 CONTINUE
0335      C
0336      DO 1800 J=1046,1048
0337      JJ=J
0338      IS=JICVT(JJ,ID4)
0339      CALL IMAGE(ID4)
0340      ENCODE(8,1801,OUT(1)) I18
0341      1801 FORMAT(A8)
0342      DO 1802 I=1,4
0343      OUT(I+4)=BD4(I)
0344      DO 1803 I=1,4
0345      OUT(I+9)=BD4(I)
0346      OUT(14)=3
0347      OUT(15)=3
0348      DO 1804 I=1,20
0349      OUT(I)=3
0350      WRITE(1,J)(OUT(I),I=1,20)
0351      WRITE(7,1805)(OUT(I),I=1,20)
0352      1805 FORMAT(1X,20A1)
0353      1800 CONTINUE
0354      C
0355      DO 1900 J=1049,1051
0356      JJ=J
0357      IS=JICVT(JJ,ID4)
0358      CALL IMAGE(ID4)
0359      ENCODE(8,1901,OUT(1)) I19
0360      1901 FORMAT(A8)
0361      DO 1902 I=1,4
0362      OUT(I+4)=BD4(I)
0363      DO 1903 I=1,4
0364      OUT(I+9)=BD4(I)
0365      OUT(14)=3
0366      OUT(15)=3
0367      DO 1904 I=1,20
0368      OUT(I)=3
0369      WRITE(1,J)(OUT(I),I=1,20)
0370      WRITE(7,1905)(OUT(I),I=1,20)
0371      1905 FORMAT(1X,20A1)
0372      1900 CONTINUE
0373      C
0374      DO 2000 J=1052,1054
0375      JJ=J
0376      IS=JICVT(JJ,ID4)
0377      CALL IMAGE(ID4)
0378      ENCODE(8,2001,OUT(1)) I20
0379      2001 FORMAT(A8)
0380      DO 2002 I=1,4
0381      OUT(I+4)=BD4(I)
0382      DO 2003 I=1,4
0383      OUT(I+9)=BD4(I)
0384      OUT(14)=3
0385      OUT(15)=3
0386      DO 2004 I=1,20
0387      OUT(I)=3
0388      WRITE(1,J)(OUT(I),I=1,20)
0389      WRITE(7,2005)(OUT(I),I=1,20)
0390      2005 FORMAT(1X,20A1)
0391      2000 CONTINUE
0392      C
0393      DO 2100 J=1055,1057
0394      JJ=J
0395      IS=JICVT(JJ,ID4)
0396      CALL IMAGE(ID4)
0397      ENCODE(8,2101,OUT(1)) I21
0398      2101 FORMAT(A8)
0399      DO 2102 I=1,4
0400      OUT(I+4)=BD4(I)
0401      DO 2103 I=1,4
0402      OUT(I+9)=BD4(I)
0403      OUT(14)=3
0404      OUT(15)=3
0405      DO 2104 I=1,20
0406      OUT(I)=3
0407      WRITE(1,J)(OUT(I),I=1,20)
0408      WRITE(7,2105)(OUT(I),I=1,20)
0409      2105 FORMAT(1X,20A1)
0410      2100 CONTINUE
0411      C
0412      DO 2200 J=1058,1060
0413      JJ=J
0414      IS=JICVT(JJ,ID4)
0415      CALL IMAGE(ID4)
0416      ENCODE(8,2201,OUT(1)) I22
0417      2201 FORMAT(A8)
0418      DO 2202 I=1,4
0419      OUT(I+4)=BD4(I)
0420      DO 2203 I=1,4
0421      OUT(I+9)=BD4(I)
0422      OUT(14)=3
0423      OUT(15)=3
0424      DO 2204 I=1,20
0425      OUT(I)=3
0426      WRITE(1,J)(OUT(I),I=1,20)
0427      WRITE(7,2205)(OUT(I),I=1,20)
0428      2205 FORMAT(1X,20A1)
0429      2200 CONTINUE
0430      C
0431      DO 2300 J=1061,1063
0432      JJ=J
0433      IS=JICVT(JJ,ID4)
0434      CALL IMAGE(ID4)
0435      ENCODE(8,2301,OUT(1)) I23
0436      2301 FORMAT(A8)
0437      DO 2302 I=1,4
0438      OUT(I+4)=BD4(I)
0439      DO 2303 I=1,4
0440      OUT(I+9)=BD4(I)
0441      OUT(14)=3
0442      OUT(15)=3
0443      DO 2304 I=1,20
0444      OUT(I)=3
0445      WRITE(1,J)(OUT(I),I=1,20)
0446      WRITE(7,2305)(OUT(I),I=1,20)
0447      2305 FORMAT(1X,20A1)
0448      2300 CONTINUE
0449      C
0450      DO 2400 J=1064,1066
0451      JJ=J
0452      IS=JICVT(JJ,ID4)
0453      CALL IMAGE(ID4)
0454      ENCODE(8,2401,OUT(1)) I24
0455      2401 FORMAT(A8)
0456      DO 2402 I=1,4
0457      OUT(I+4)=BD4(I)
0458      DO 2403 I=1,4
0459      OUT(I+9)=BD4(I)
0460      OUT(14)=3
0461      OUT(15)=3
0462      DO 2404 I=1,20
0463      OUT(I)=3
0464      WRITE(1,J)(OUT(I),I=1,20)
0465      WRITE(7,2405)(OUT(I),I=1,20)
0466      2405 FORMAT(1X,20A1)
0467      2400 CONTINUE
0468      C
0469      DO 2500 J=1067,1069
0470      JJ=J
0471      IS=JICVT(JJ,ID4)
0472      CALL IMAGE(ID4)
0473      ENCODE(8,2501,OUT(1)) I25
0474      2501 FORMAT(A8)
0475      DO 2502 I=1,4
0476      OUT(I+4)=BD4(I)
0477      DO 2503 I=1,4
0478      OUT(I+9)=BD4(I)
0479      OUT(14)=3
0480      OUT(15)=3
0481      DO 2504 I=1,20
0482      OUT(I)=3
0483      WRITE(1,J)(OUT(I),I=1,20)
0484      WRITE(7,2505)(OUT(I),I=1,20)
0485      2505 FORMAT(1X,20A1)
0486      2500 CONTINUE
0487      C
0488      DO 2600 J=1070,1072
0489      JJ=J
0490      IS=JICVT(JJ,ID4)
0491      CALL IMAGE(ID4)
0492      ENCODE(8,2601,OUT(1)) I26
0493      2601 FORMAT(A8)
0494      DO 2602 I=1,4
0495      OUT(I+4)=BD4(I)
0496      DO 2603 I=1,4
0497      OUT(I+9)=BD4(I)
0498      OUT(14)=3
0499      OUT(15)=3
0500      DO 2604 I=1,20
0501      OUT(I)=3
0502      WRITE(1,J)(OUT(I),I=1,20)
0503      WRITE(7,2605)(OUT(I),I=1,20)
0504      2605 FORMAT(1X,20A1)
0505      2600 CONTINUE
0506      C
0507      DO 2700 J=1073,1075
0508      JJ=J
0509      IS=JICVT(JJ,ID4)
0510      CALL IMAGE(ID4)
0511      ENCODE(8,2701,OUT(1)) I27
0512      2701 FORMAT(A8)
0513      DO 2702 I=1,4
0514      OUT(I+4)=BD4(I)
0515      DO 2703 I=1,4
0516      OUT(I+9)=BD4(I)
0517      OUT(14)=3
0518      OUT(15)=3
0519      DO 2704 I=1,20
0520      OUT(I)=3
0521      WRITE(1,J)(OUT(I),I=1,20)
0522      WRITE(7,2705)(OUT(I),I=1,20)
0523      2705 FORMAT(1X,20A1)
0524      2700 CONTINUE
0525      C
0526      DO 2800 J=1076,1078
0527      JJ=J
0528      IS=JICVT(JJ,ID4)
0529      CALL IMAGE(ID4)
0530      ENCODE(8,2801,OUT(1)) I28
0531      2801 FORMAT(A8)
0532      DO 2802 I=1,4
0533      OUT(I+4)=BD4(I)
0534      DO 2803 I=1,4
0535      OUT(I+9)=BD4(I)
0536      OUT(14)=3
0537      OUT(15)=3
0538      DO 2804 I=1,20
0539      OUT(I)=3
0540      WRITE(1,J)(OUT(I),I=1,20)
0541      WRITE(7,2805)(OUT(I),I=1,20)
0542      2805 FORMAT(1X,20A1)
0543      2800 CONTINUE
0544      C
0545      DO 2900 J=1079,1081
0546      JJ=J
0547      IS=JICVT(JJ,ID4)
0548      CALL IMAGE(ID4)
0549      ENCODE(8,2901,OUT(1)) I29
0550      2901 FORMAT(A8)
0551      DO 2902 I=1,4
0552      OUT(I+4)=BD4(I)
0553      DO 2903 I=1,4
0554      OUT(I+9)=BD4(I)
0555      OUT(14)=3
0556      OUT(15)=3
0557      DO 2904 I=1,20
0558      OUT(I)=3
0559      WRITE(1,J)(OUT(I),I=1,20)
0560      WRITE(7,2905)(OUT(I),I=1,20)
0561      2905 FORMAT(1X,20A1)
0562      2900 CONTINUE
0563      C
0564      DO 3000 J=1082,1084
0565      JJ=J
0566      IS=JICVT(JJ,ID4)
0567      CALL IMAGE(ID4)
0568      ENCODE(8,3001,OUT(1)) I30
0569      3001 FORMAT(A8)
0570      DO 3002 I=1,4
0571      OUT(I+4)=BD4(I)
0572      DO 3003 I=1,4
0573      OUT(I+9)=BD4(I)
0574      OUT(14)=3
0575      OUT(15)=3
0576      DO 3004 I=1,20
0577      OUT(I)=3
0578      WRITE(1,J)(OUT(I),I=1,20)
0579      WRITE(7,3005)(OUT(I),I=1,20)
0580      3005 FORMAT(1X,20A1)
0581      3000 CONTINUE
0582      C
0583      DO 3100 J=1085,1087
0584      JJ=J
0585      IS=JICVT(JJ,ID4)
0586      CALL IMAGE(ID4)
0587      ENCODE(8,3101,OUT(1)) I31
0588      3101 FORMAT(A8)
0589      DO 3102 I=1,4
0590      OUT(I+4)=BD4(I)
0591      DO 3103 I=1,4
0592      OUT(I+9)=BD4(I)
0593      OUT(14)=3
0594      OUT(15)=3
0595      DO 3104 I=1,20
0596      OUT(I)=3
0597      WRITE(1,J)(OUT(I),I=1,20)
0598      WRITE(7,3105)(OUT(I),I=1,20)
0599      3105 FORMAT(1X,20A1)
0600      3100 CONTINUE
0601      C
0602      DO 3200 J=1088,1090
0603      JJ=J
0604      IS=JICVT(JJ,ID4)
0605      CALL IMAGE(ID4)
0606      ENCODE(8,3201,OUT(1)) I32
0607      3201 FORMAT(A8)
0608      DO 3202 I=1,4
0609      OUT(I+4)=BD4(I)
0610      DO 3203 I=1,4
0611      OUT(I+9)=BD4(I)
0612      OUT(14)=3
0613      OUT(15)=3
0614      DO 3204 I=1,20
0615      OUT(I)=3
0616      WRITE(1,J)(OUT(I),I=1,20)
0617      WRITE(7,3205)(OUT(I),I=1,20)
0618      3205 FORMAT(1X,20A1)
0619      3200 CONTINUE
0620      C
0621      DO 3300 J=1091,1093
0622      JJ=J
0623      IS=JICVT(JJ,ID4)
0624      CALL IMAGE(ID4)
0625      ENCODE(8,3301,OUT(1)) I33
0626      3301 FORMAT(A8)
0627      DO 3302 I=1,4
0628      OUT(I+4)=BD4(I)
0629      DO 3303 I=1,4
0630      OUT(I+9)=BD4(I)
0631      OUT(14)=3
0632      OUT(15)=3
0633      DO 3304 I=1,20
0634      OUT(I)=3
0635      WRITE(1,J)(OUT(I),I=1,20)
0636      WRITE(7,3305)(OUT(I),I=1,20)
0637      3305 FORMAT(1X,20A1)
0638      3300 CONTINUE
0639      C
0640      DO 3400 J=1094,1096
0641      JJ=J
0642      IS=JICVT(JJ,ID4)
0643      CALL IMAGE(ID4)
0644      ENCODE(8,3401,OUT(1)) I34
0645      3401 FORMAT(A8)
0646      DO 3402 I=1,4
0647      OUT(I+4)=BD4(I)
0648      DO 3403 I=1,4
0649      OUT(I+9)=BD4(I)
0650      OUT(14)=3
0651      OUT(15)=3
0652      DO 3404 I=1,20
0653      OUT(I)=3
0654      WRITE(1,J)(OUT(I),I=1,20)
0655      WRITE(7,3405)(OUT(I),I=1,20)
0656      3405 FORMAT(1X,20A1)
0657      3400 CONTINUE
0658      C
0659      DO 3500 J=1097,1099
0660      JJ=J
0661      IS=JICVT(JJ,ID4)
0662      CALL IMAGE(ID4)
0663      ENCODE(8,3501,OUT(1)) I35
0664      3501 FORMAT(A8)
0665      DO 3502 I=1,4
0666      OUT(I+4)=BD4(I)
0667      DO 3503 I=1,4
0668      OUT(I+9)=BD4(I)
0669      OUT(14)=3
0670      OUT(15)=3
0671      DO 3504 I=1,20
0672      OUT(I)=3
0673      WRITE(1,J)(OUT(I),I=1,20)
0674      WRITE(7,3505)(OUT(I),I=1,20)
0675      3505 FORMAT(1X,20A1)
0676      3500 CONTINUE
0677      C
0678      DO 3600 J=1100,1102
0679      JJ=J
0680      IS=JICVT(JJ,ID4)
0681      CALL IMAGE(ID4)
0682      ENCODE(8,3601,OUT(1)) I36
0683      3601 FORMAT(A8)
0684      DO 3602 I=1,4
0685      OUT(I+4)=BD4(I)
0686      DO 3603 I=1,4
0687      OUT(I+9)=BD4(I)
0688      OUT(14)=3
0689      OUT(15)=3
0690      DO 3604 I=1,20
0691      OUT(I)=3
0692      WRITE(1,J)(OUT(I),I=1,20)
0693      WRITE(7,3605)(OUT(I),I=1,20)
0694      3605 FORMAT(1X,20A1)
0695      3600 CONTINUE
0696      C
0697      DO 3700 J=1103,1105
0698      JJ=J
0699      IS=JICVT(JJ,ID4)
0700      CALL IMAGE(ID4)
0701      ENCODE(8,3701,OUT(1)) I37
0702      3701 FORMAT(A8)
0703      DO 3702 I=1,4
0704      OUT(I+4)=BD4(I)
0705      DO 3703 I=1,4
0706      OUT(I+9)=BD4(I)
0707      OUT(14)=3
0708      OUT(15)=3
0709      DO 3704 I=1,20
0710      OUT(I)=3
0711      WRITE(1,J)(OUT(I),I=1,20)
0712      WRITE(7,3705)(OUT(I),I=1,20)
0713      3705 FORMAT(1X,20A1)
0714      3700 CONTINUE
0715      C
0716      DO 3800 J=1106,1108
0717      JJ=J
0718      IS=JICVT(JJ,ID4)
0719      CALL IMAGE(ID4)
0720      ENCODE(8,3801,OUT(1)) I38
0721      3801 FORMAT(A8)
0722      DO 3802 I=1,4
0723      OUT(I+4)=BD4(I)
0724      DO 3803 I=1,4
0725      OUT(I+9)=BD4(I)
0726      OUT(14)=3
0727      OUT(15)=3
0728      DO 3804 I=1,20
0729      OUT(I)=3
0730      WRITE(1,J)(OUT(I),I=1,20)
0731      WRITE(7,3805)(OUT(I),I=1,20)
0732      3805 FORMAT(1X,20A1)
0733      3800 CONTINUE
0734      C
0735      DO 3900 J=1109,1111
0736      JJ=J
0737      IS=JICVT(JJ,ID4)
0738      CALL IMAGE(ID4)
0739      ENCODE(8,3901,OUT(1)) I39
0740      3901 FORMAT(A8)
0741      DO 3902 I=1,4
0742      OUT(I+4)=BD4(I)
0743      DO 3903 I=1,4
0744      OUT(I+9)=BD4(I)
0745      OUT(14)=3
0746      OUT(15)=3
0747      DO 3904 I=1,20
0748      OUT(I)=3
0749      WRITE(1,J)(OUT(I),I=1,20)
0750      WRITE(7,3905)(OUT(I),I=1,20)
0751      3905 FORMAT(1X,20A1)
0752      3900 CONTINUE
0753      C
0754      DO 4000 J=1112,1114
0755      JJ=J
0756      IS=JICVT(JJ,ID4)
0757      CALL IMAGE(ID4)
0758      ENCODE(8,4001,OUT(1)) I40
0759      4001 FORMAT(A8)
0760      DO 4002 I=1,4
0761      OUT(I+4)=BD4(I)
0762      DO 4003 I=1,4
0763      OUT(I+9)=BD4(I)
0764      OUT(14)=3
0765      OUT(15)=3
0766      DO 4004 I=1,20
0767      OUT(I)=3
0768      WRITE(1,J)(OUT(I),I=1,20)
0769      WRITE(7,4005)(OUT(I),I=1,20)
0770      4005 FORMAT(1X,20A1)
0771      4000 CONTINUE
0772      C
0773      DO 4100 J=1115,1117
0774      JJ=J
0775      IS=JICVT(JJ,ID4)
0776      CALL IMAGE(ID4)
0777      ENCODE(8,4101,OUT(1)) I41
0778      4101 FORMAT(A8)
0779      DO 4102 I=1,4
0780      OUT(I+4)=BD4(I)
0781      DO 4103 I=1,4
0782      OUT(I+9)=BD4(I)
0783      OUT(14)=
```

PAGE 002

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:00:55

```

0049 JJ=J
0050 IS=JICVT(JJ, ID4)
0051 CALL IMAGE(ID4)
0052 ENCODE(8,301,OUT(1)) I3
0053 301 FORMAT(A8)
0054 OUT(8)=BD4(4)
0055 OUT(9)=
0056 DO 302 I=1,4
0057 302 OUT(I+9)=BD4(I)
0058 D
0059 OUT(14)=
0060 OUT(15)=3
0061 DO 303 I=16,20
0062 303 OUT(I)=
0063 WRITE(1,J)(OUT(I),I=1,20)
0064 WRITE(7,304)(OUT(I),I=1,20)
0065 304 FORMAT(IX,20A1)
0066 C
0067 J1=1
0068 J2=1
0069 IREC=1006
0070 DO 400 JR=1,9,3
0071 ENCODE(4,401,OUT(1)) I4
0072 401 FORMAT(A4)
0073 OUT(5)=
0074 IS=JICVT(J1, ID4)
0075 CALL IMAGE(ID4)
0076 DO 402 J=1,4
0077 402 OUT(J+5)=BD4(J)
0078 OUT(10)=
0079 DO 403 J=1,3
0080 IS=JICVT(J2, ID4)
0081 CALL IMAGE(ID4)
0082 DO 404 I=1,3
0083 404 OUT(I+10)=BD4(I+1)
0084 OUT(14)=
0085 OUT(15)=3
0086 DO 405 I=16,20
0087 405 OUT(I)=
0088 J2=J2+1
0089 WRITE(1,IREC)(OUT(I),I=1,20)
0090 WRITE(7,304)(OUT(I),I=1,20)
0091 J2=1
0092 J1=J1+1
0093 CONTINUE
0094 DO 500 J=1,2
0095 500 J1=1,4
0096 OUT(J1)=
0097 OUT(5)=040
0098 DO 502 J1=1,4
0099 502 OUT(J1+5)=0
0100 DO 503 J1=10,20

```

PAGE 003

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:00:55

```

0099 503 OUT(J1)='040
0100 OUT(15)='3'
0101 WRITE(1,'1003+J')(OUT(1),I=1,20)
0102 500 CONTINUE
0103 OUT(1)='P'
0104 OUT(2)='R'
0105 OUT(3)='E'
0106 OUT(4)='S'
0107 OUT(5)='S'
0108 OUT(6)='R'
0109 OUT(7)='R'
0110 OUT(8)='E'
0111 OUT(9)='T'
0112 OUT(10)='U'
0113 OUT(11)='R'
0114 OUT(12)='N'
0115 DO 600 J=13,20
0116 OUT(J)=' '
0117 OUT(15)='1'
0118 WRITE(1,'1015')(OUT(J),J=1,20)
0119 OUT(15)='2'
0120 WRITE(1,'1016')(OUT(J),J=1,20)
0121 CALL CLOSE(1)
0122 STOP 'BUILD COMPLETED'
0123 END

```

PAGE 001

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:01:07

```

0001 SUBROUTINE IMAGE(I14)
0002 BYTE BD4
0003 INTEGER*4 I14
0004 COMMON/IM/BD4(4)
0005 ENCODE(4,1,BD4) I14
0006 1 FORMAT(4I)
0007 DO 2 I=1,4
0008 IF(BD4(I) .LE. 57 .AND. BD4(I) .GE. 48) GOTO 2
0009 BD4(I)='0'
0010 2 CONTINUE
0011 RETURN
0012 END
0013

```


2.4 FDMLDR LOADER PROGRAM

FDMLDR program is the Fortran loading program which is run on host processor (PDP-11/V03) which enables the user of the MSCDM to down-line load object files (.SAV) to the LSI-11 nodes, start loaded program or request status of the nodes.

2.4.1 Program Description

2.4.1.1 Program FDMLDR (FORTRAN).

This program is the driver for FDMLDR and controls the format of the CRT, opening and closing files, and the user's interactions.

2.4.1.2 Subroutine Loader (FORTRAN):

This subroutine is called by FDMLDR to load a specific node with the object file assigned to logical unit 9.

2.4.1.3 Subroutine ACKNAK (FORTRAN):

This subroutine checks the response coming back to determine whether the current packet was ACKed or NAKed.

2.4.1.4 Subroutine ISET (FORTRAN):

This subroutine is called to place the cursor of the VT52 to the line displaying the current node being loaded.

2.4.1.5 Subroutine LDRMAC (MACRO):

This subroutine contains the MACROS for the VT52 (MVTP), LIU handlers (INIT, LIO, WTLOOP).

2.4.2 Use of FDMLDR

Refer to MSCDM User's Manual for operating procedures.

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:01:46

FORTRAN IV

```
0001 PROGRAM EDMLDR  
0002 BYTE OUTBF,RTIM(8),INBF  
0003 INTEGER%2 ILID,ILNF,IACK,INAK,IFLT,IGOT,FCNT  
0004 REAL$ NRFILE(16) FILE  
0005 COMMON/LOOP1/OUTBF(256),INBF(256),FILE(2)  
0006 COMMON/LOOP2/ILID(S),ILND(S),IGOT,IACK,INAK,IFLT  
0007 DATA NRFILE/'SIGGEN.L','DA' ,,'NODE21.S','AV'  
      ,,'NODE23.S','AV'  
      ,,'NODE25.S','AV'  
      ,,'NODE26.S','AV'  
      ,,'NODE27.S','AV'  
  
0008 ILID(1)="004"  
0009 ILID(2)="003"  
0010 ILID(3)="006"  
0011 ILID(4)="007"  
0012 ILID(5)="010"  
0013 ILID(6)="011"  
0014 ILID(7)="002"  
0015 ILID(8)="010"  
0016 ILND(1)=21  
0017 ILND(2)=22  
0018 ILND(3)=23  
0019 ILND(4)=25  
0020 ILND(5)=26  
0021 ILND(6)=27  
0022 ILND(7)=28  
0023 ILND(8)=20  
0024 ISLMD=0  
0025 ISLT=0  
  
C*****  
1 CALL DABLE "012","012"  
WRITE(7,2) "033","110"  
WRITE(7,2) "033","112"  
WRITE(7,2) "012","012"  
WRITE(7,2) "033","110"  
FORMAT(IX,IALLA1)  
CALL INIT(ICND,IARG1,IARG2,IARG3,IARG4)  
IF(ICND.EQ.1) GOTO 4  
WRITE(7,3)  
3 FORMAT(/,/,IX,"ERROR DEVELOPED-INITIALIZING NODE24"/,/,IX,  
    "/LOADING PROCEDURE APCRTERD!!!.")  
1 WRITE(7,17) IARG1,IARG2,IARG3,IARG4  
17 FORMAT(/,/,IX,"ACRAM ERRORS:",I4,/,,IX,"BUFFER ERRORS:",I4,  
    /,/,IX,"STATUS ERRORS:",I4,2X,.OB)  
CALL EXIT  
4 CALL MVTP("040","040")  
WRITE(7,5)  
5 FORMAT(IX,MSCDM (LOOP 5) LOADER/RUNNING ON NODE 24:')  
CALL MVTP("057","040")  
WRIT(7,6)  
6 FORMAT(IX,NODE 21:',/,IX,NODE 22:',/,IA,NODE 23:',/  
    IX,NODE 25:',/,IX,NODE 26:',/,IA,NODE 27:',/  
    IX,NODE 28:',/,IX,SIG 20:',/  
    CALL MVTP("044","040")
```

PAGE 002

```

FORTRAN IV      V02.1-11   Mon 01-Oct-79 12:01:46

0048      WRITE(7,8)
0049      8      FORMAT(6X, '1. NORMAL LOAD',/,6X
          4      '2. INDIVIDUAL LOAD',/,6X
          5      '3. START NODE(S)',/,6X
          6      '4. REPORT STATUS',/,6X
          7      '5. TERMINATE')
          C*****
0050      16      CALL RAM('005,4)
0051      DO 15 I=1,8
0052      ITRY=1
0053      IFLG=1
0054      IF(I.EQ.8) IFLG=8
0055      9      OUTBF(1)=0
0056      OUTBF(2)=IFLG
0057      OUTBF(3)=3
0058      OUTBF(4)=ILID(I)
0059      DO 11 ICI=1,4
0060      111      INBF(ICI)=0
0061      CALL WTLOOP(4)
0062      DO 70 ID=1,500
0063      70      CONTINUE
0064      CALL ACKNAK(IST)
0065      DO 89 JD=1,1000
0066      89      CONTINUE
0067      IF(IST.EQ.1) GOTO 91
0068      ITRY=ITRY+1
0069      IF(ITRY.GT.3) GOTO 11
0070      GOTO 90
0071      91      CALL ISET(I)
0072      WRITE(7,10)
0073      10      FORMAT('5', 'ON -LINE READY MODE',30X)
0074      GOTO 15
0075      11      IF(IST.NE.2) GOTO 13
0076      CALL ISET(I)
0077      WRITE(7,12)
0078      12      FORMAT('5', 'ON -LINE ERROR DETECTED',30X)
0079      GOTO 15
0080      13      IF(IST.NE.-1) GOTO 9
0081      CALL ISET(I)
0082      WRITE(7,14)
0083      14      FORMAT('5', 'OFF-LINE NO RESPONSE',30X)
0084      GOTO 15
0085      15      CONTINUE
0086      C*****
0087      100      CALL ISET(9)
0088      WRITE(7,106)
0089      106      FORMAT(20X,/,20X)
0090      INT=1
0091      ILCMD=ISLT
0092      CALL MVTP('042','040)
0093      WRITE(7,101)
0094      101      FORMAT(1X, 'SELECTION ? ',10X)
0095      CALL MVTP('042','054)
0096      READ(7,102) ISLT
0097      102      FORMAT(I)
0098
0099
0100

```


PAGE 003

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:01:46

```

0101 CALL MVTP('041','040)
0102 WRITE(7,107)
0103 107 FORMAT(20X)
0104 IF(ISLT.EQ. 4) GOTO 16
0106 IF(ISLT.NE. 1) GOTO 200
0108 FCNT=0
0109 DO 501 K=1,2
0110 501 FILE(K)=0
0111 DO 502 J=1,2
0112 502 FILE(J)=NBFILE(J+FCNT)
0113 CALL LOADER(8)
0114 FCNT=2
0115 DO 105 I=1,7
0116 DO 103 K=1,2
0117 FILE(K)=0
0118 CONTINUE
0119 DO 104 J=1,2
0120 FILE(J)=NBFILE(J+FCNT)
0121 CONTINUE
0122 CALL LOADER(1)
0123 FCNT=FCNT+2
0124 105 CONTINUE
0125 INT=0
0126 OUTBF(1)=0
0127 OUTBF(2)=8
0128 OUTBF(3)=3
0129 OUTBF(4)=LLID(8)
0130 CALL WLOOP(4)
0131 GOTO 350
C*****
0132 200 IF(ISLT.NE. 2) GOTO 300
0134 201 CALL ISET(9)
0135 WRITE(7,202)
0136 202 FORMAT(1X, 'NODE[ ]', 10X, '/', 1X, 'FILE[ ]')
0137 DO 203 I=1,2
0138 FILE(I)=0
0139 CALL MVTP('052','045)
0140 READ(7,206) INT
0141 206 FORMAT(I)
0142 IF(INT.EQ. 99) GOTO 100
0144 CALL MVTP('054','045)
0145 READ(7,215) FILE
0146 215 FORMAT(2A8)
0147 DO 208 I=1,8
0148 IF(I.EQ. 8.AND. INT.EQ. 0) GOTO 208
0150 IF(INT.EQ. 0) GOTO 209
0152 IF(INT.EQ. ILND(I)) GOTO 209
0154 GOTO 208
0155 209 CALL LOADER(1)
0156 CONTINUE
0157 IF(INT.NE. 0) GOTO 201
0159 GOTO 100
C*****
0160 300 IF(ISLT.NE. 3) GOTO 400

```

PAGE 004

```

FORTRAN IV      V02.1-11      Mon 01-Oct-79 12:01:46
0162      301 CALL ISET(9)
0163      WRITE(7,302)
0164      302 FORMAT(1X,'START NODE[ ]',/,25X)
0165      CALL MVIP(052,053)
0166      READ(7,304) INT
0167      304 FORMAT(I)
0168      IF(INT.EQ.99) GOTO 100
0170      350 DO 305 I=1,8
0171      IF(INT.EQ.0) GOTO 306
0173      IF(INT.EQ.ILND(I)) GOTO 306
0175      GOTO 305
0176      306 IF(I.NE.8) GOTO 308
0178      CALL ISET(8)
0179      WRITE(7,309)
0180      309 FORMAT('5',AUTO-START AFTER LOAD',20X)
0181      GOTO 305
0182      308 CALL ISET(I)
0183      WRITE(7,307)
0184      307 FORMAT('5',ATTEMPTING TO START',30X)
0185      OUTBF(1)=0
0186      OUTBF(2)=5
0187      OUTBF(3)=3
0188      OUTBF(4)=ILID(I)
0189      CALL WTLOOP(4)
0190      DO 340 JI=1,5000
0191      340 CONTINUE
0192      305 CONTINUE
0193      IF(ISLT.EQ.1) GOTO 450
0195      IF(INT.EQ.0) GOTO 100
0197      GOTO 301
C*****
0198      400 IF(ISLT.NE.5) GOTO 100
0200      CALL DABLE
0201      CALL EXIT
0202      450 WRITE(7,451) 033,110
0203      451 FORMAT(1X,1A1,1A1)
0204      WRITE(7,451) 033,112
0205      WRITE(7,451) 033,110
0206      CALL TIME(RTIM)
0207      WRITE(7,452) (RTIM(I),I=1,8)
0208      452 FORMAT(//,1X,MSCPM LOOP S LOAD: AT : ',0A1)
0209      CALL DABLE
0210      CALL EXIT
0211      END
C$$$

```

```

FORTRAN IV      V02.1-11      Mon 01-Oct-79 12:02:01      PAGE 001

0001      SUBROUTINE LOADER(INODE)
0002      BYTE OUTBF,INBF,BLOCK(4,128),RTIM(8)
0003      INTEGER*2 ILID,ILND,IGOT,IACK,INAK,IFLT
0004      REAL*8 FILE
0005      COMMON/LOOP1/OUTBF(256),INBF(256),FILE(2)
0006      COMMON/LOOP2/ILID(8),ILND(8),IGOT,IACK,INAK,IFLT

C
0007      302 CALL ASSIGN(9,FILE)
0008      DEFINE FILE 9(300,256,U,INEC)
0009      CALL ISET(INODE)
0010      WRITE(7,3) FILE
0011      3 FORMAT('$',LOADING- ',2A8,25X)
0012      CALL ISET(INODE)
0013      IRLCT=1
0014      IFLG=6
0015      IF(INODE.EQ. 8) IFLG=9
0017      4 OUTBF(1)=0
0018      OUTBF(2)=IFLG
0019      OUTBF(3)=3
0020      OUTBF(4)=ILID(INODE)
0021      CALL WTLOOP(4)
0022      IREC=1
0023      IPC=0
0024      IECNT=0

C
0025      100 READ(9,IREC,END=200,ERR=300) ((BLOCK(I,J),J=1,128),I=1,4)
0026      FIND(9,IREC)
0027      IDIR=0
0028      DO 101 I=1,4
0029      IPC=IPC+1
0030      IF(IPC.EQ. 100) IPC=1
0032      IECNT=0
0033      DO 102 J=1,128
0034      OUTBF(J+2)=BLOCK(I,J)
0035      102 CONTINUE
0036      IFLG=0
0037      IF(INODE.EQ. 8) IFLG=7
0039      OUTBF(1)=IPC
0040      OUTBF(2)=IFLG
0041      OUTBF(131)=003
0042      OUTBF(132)=ILID(INODE)
0043      DO 110 IC=1,4
0044      INBF(IC)=0
0045      103 CALL WTLOOP(132)
0046      IF(INODE.NE. 8) GOTO 104
0048      DO 121 ID1=1,9000
0049      121 CONTINUE
0050      104 CALL ACKNAK(ISTAT)
0051      105 IF(ISTAT.EQ. 1) GOTO 101
0053      WRITE(7,107) IPC,ISTAT
0054      107 FORMAT('$',RESENDING PACKET ',14,2X,'ERROR: ',12,25X)
0055      CALL ISET(INODE)
0056      IECNT=IECNT+1
0057      IF(IECNT.LT. 4) GOTO 103

```

PAGE 002

```

FORTRAN IV      V02.1-11   Mon 01-Oct-79 12:02:01
0059      WRITE(7,100) IRLCT
0060      FORMAT(' ',RELOADING ATTEMPT# ',12,29X)
0061      CALL ISET(INODE)
0062      IRLCT=IRLCT+1
0063      IF(IRLCT.LT. 4) GOTO 4
0065      WRITE(7,100)
0066      FORMAT(' ',NODE FAILED TO LOAD!!!',29X)
0067      CALL ISET(INODE)
0068      CALL CLOSE(9)
0069      RETURN
C
0070      101 CONTINUE
0071      GOTO 100
0072      200 CALL TIME(RTIM)
0073      CALL ISET(INODE)
0074      WRITE(7,201) (RTIM(J),J=1,8),FILE
0075      201 FORMAT(' ',LOADING COMPLETED ',8A1,' ',2A8,4X)
0076      CALL CLOSE(9)
0077      RETURN
0078      300 IF(IDIR.EQ. 1) GOTO 301
0080      CALL CLOSE(9)
0081      IDIR=1
0082      CALL ISET(INODE)
0083      WRITE(7,304) FILE
0084      304 FORMAT(' ',INSERT DISK WITH --> ',2A8,12X)
0085      READ(7,305) IDUM
0086      FORMAT(A)
0087      CALL ASSIGN(1,'DX0:DUMMY.MAC')
0088      CALL CLOSE(1)
0089      GOTO 302
0090      301 WRITE(7,306) FILE
0091      306 FORMAT(' ',ERROR FILE NOT FOUND- ',2A8,12X)
0092      CALL CLOSE(9)
0093      RETURN
0094      END
C$$$

```



```

0001 SUBROUTINE ACKNAK(IRTN)
0002 INTEGER*2 ILID,ILND,IGOT,IACK,INAK,IFLT,ICNT
0003 COMMON/LOOP2/ILID(8),ILND(8),IGOT,IACK,INAK,IFLT
C
0004 ICNT=0
0005 DO 1,I=1,600
0006 IF(IGOT.EQ. 1) GOTO 2
0008 1 CONTINUE
0009 ICNT=ICNT+1
0010 IF(IACK.NE. 1) GOTO 3
0012 IRTN=1
0013 GOTO 999
0014 IF(INAK.NE. 1) GOTO 4
0016 IRTN=2
0017 GOTO 999
0018 IF(IFLT.NE. 1) GOTO 5
0020 IRTN=-2
0021 GOTO 999
0022 IF(ICNT.LT. 5) GOTO 100
0024 IRTN=-1
0025 GOTO 999
0026 IACK=0
0027 INAK=0
0028 IFLT=0
0029 IGOT=0
0030 RETURN
0031 END
C$$$

```

PAGE 001

FORTRAN IV V02.1-11 Mon 01-Oct-79 12:02:11

```

0001 SUBROUTINE ISET(ILN)
0002 IF(ILN.NE.1) GOTO 2
0004 WRITE(7,100) "033,"110
0005 CALL MVTP(056,052)
0006 GOTO 999
0007 IF(ILN.NE.2) GOTO 3
0009 WRITE(7,100) "033,"110
0010 CALL MVTP(057,052)
0011 GOTO 999
0012 IF(ILN.NE.3) GOTO 4
0014 WRITE(7,100) "033,"110
0015 CALL MVTP(060,052)
0016 GOTO 999
0017 IF(ILN.NE.4) GOTO 5
0019 WRITE(7,100) "033,"110
0020 CALL MVTP(061,052)
0021 GOTO 999
0022 IF(ILN.NE.5) GOTO 6
0024 WRITE(7,100) "033,"110
0025 CALL MVTP(062,052)
0026 GOTO 999
0027 IF(ILN.NE.6) GOTO 7
0029 WRITE(7,100) "033,"110
0030 CALL MVTP(063,052)
0031 GOTO 999
0032 IF(ILN.NE.7) GOTO 8
0034 WRITE(7,100) "033,"110
0035 CALL MVTP(064,052)
0036 GOTO 999
0037 IF(ILN.NE.8) GOTO 9
0039 WRITE(7,100) "033,"110
0040 CALL MVTP(065,052)
0041 GOTO 999
0042 IF(ILN.NE.9) GOTO 999
0044 WRITE(7,100) "033,"110
0045 CALL MVTP(052,040)
0046 GOTO 999
0047 100 FORMAT(1X,1A1,1A1)
0048 999 RETURN
0049 END

```

```

LDRMAC.MACRO      MACRO V03.01 27-MAR-79 19:48:51 PAGE 1
1
2
3
4 000000
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49

```

```

.TITLE LDRMAC.MACRO

```

```

.IDENT /V1.0/
.GLOBL MVTP,WTLOOP,INIT,RAM,DABLE
.PSECT

```

```

172410 BAR=
172412 WCR=
172414 CSR=
172416 IOBUF=
172418 OPREG=
172560 RCSR=
172562 RBUF=
172564 XCSR=
172566 XBUF=
172568 ARG1=
172570 ARG2=
172572 ARG3=
172574 ARG4=
172576 ARG5=
172578 BIT15=
172580 BIT14=
172582 BIT08=
172584 BIT07=
172586 BIT06=
172588 BIT05=
172590 BIT04=
172592 BIT03=
172594 BIT02=
172596 BIT01=
172598 BIT00=

```

```

;VT52 HANDLER

```

```

MVTP:
105737 177564 TSTB Q#XCSR
100375 BPL -4
012737 000033 MOV #033, Q#XBUF
105737 177564 TSTB Q#XCSR
100375 BPL -4
012737 000131 MOV #1, Q#XBUF
105737 177564 TSTB Q#XCSR
100375 BPL -4
017537 000002 MOV QARG1(R5), Q#XBUF
105737 177564 TSTB Q#XCSR
100375 BPL -4
017537 000004 MOV QARG2(R5), Q#XBUF
017537 000240 NOP
000207 RTS

```

```

1
2
3
4
5 000064 012737 172416 INIT: MOV #4368., G#OPREG
6 000072 012737 172416 MOV #4414, G#OPREG
7 000100 105737 172414 TSTB G#CSR
8 000104 100375 BPL -4
9 000106 105037 CLRB G#CSR
10 000112 012737 172414 MOV #4540, G#OPREG
11 000120 105737 172414 TSTB G#CSR
12 000124 100375 BPL -4
13 000126 105037 CLRB G#CSR
14 000132 012700 000124 MOV #124, R0
15 000136 012720 001026 MOV #L10, (R0)+
16 000142 012710 000340 MOV #340, (R0)
17 000146 005003 CLR R3
18 000150 005067 CLR BRAM
19 000154 005067 CLR EBUF
20 000160 005067 CLR ESTAT
21
22
23
24 000164 005004 LIMIT: CLR R4
25 000166 052737 172414 BIC #BIT00, G#CSR
26 000174 000240 NOP
27 000176 000240 NOP
28 000200 042737 172414 BIC #BIT00, G#CSR
29 000206 012737 172416 MOV #4354., G#OPREG
30 000214 012737 172416 MOV #2304., G#OPREG
31 000222 105737 172414 TSTB G#CSR
32 000226 100375 BPL -4
33 000230 105037 CLRB G#CSR
34 000234 012737 172416 MOV #4353., G#OPREG
35 000242 012700 000400 MOV #256., R0
36 000246 012737 172416 1$: MOV #2311., G#OPREG
37 000254 105737 172414 TSTB G#CSR
38 000260 100375 BPL -4
39 000262 105037 CLRB G#CSR
40 000266 077011 SOB R0, 1$
41 000270 012700 000400 MOV #256., R0
42 000274 012737 172416 2$: MOV #768., G#OPREG
43 000302 105737 172414 TSTB G#CSR
44 000306 100375 BPL -4
45 000310 105037 CLRB G#CSR
46 000314 013701 172416 MOV G#IOBUF, R1
47 000320 042701 177760 BIC #177760, R1
48 000324 022701 000007 CMP #7, R1
49 000330 001403 BEQ 3$
50 000332 005204 INC R4
51 000334 005267 INC BRAM
52 000340 077023 3$: SOB R0, 2$
53
54
55
56 000342 012701 010410 INBUF0: MOV #10410, R1
57 000346 012700 010440 MOV #10440, R0

```



```

58 000352 004767 000106 172416 JSR PC, ZEROBP
59 000356 012737 001400 Q#OPREG
60 000364 105737 172414 TSTB #1400,
61 000370 100375 BPL #4, Q#CSR
62 000372 105037 172414 CLR B #4, Q#CSR
63 000376 012701 010610 R1 #10610, R1
64 000402 012700 010640 R0 #10640, R0
65 000406 004767 000052 PC, ZEROBP
66 000412 012737 000106 INBUF1: MOV PC, ZEROBP
67 000420 105737 172414 TSTB #1400, Q#OPREG
68 000424 100375 BPL #4, Q#CSR
69 000426 105037 172414 CLR B #4, Q#CSR
70 000432 012701 010510 R1 #10510, R1
71 000436 012700 010540 R0 #10540, R0
72 000442 004767 000016 PC, ZEROBP
73 000446 012701 010710 R1 #10710, R1
74 000452 012700 010740 R0 #10740, R0
75 000456 004767 000002 PC, ZEROBP
76 000462 000464 BR STAT, ZEROBP
77 000464 010137 172416 ZEROBP: MOV R1, Q#OPREG
78 000470 012737 001400 Q#OPREG
79 000476 105737 172414 TSTB #1400, Q#CSR
80 000502 100375 BPL #4, Q#CSR
81 000504 105037 172414 CLR B #4, Q#CSR
82 000510 013702 172416 Q#IOBUF, R2
83 000514 042702 177400 BIC #177400, R2
84 000520 010037 172416 MOV R0, Q#OPREG
85 000524 022702 000000 CMP #0, R2
86 000530 001412 BEQ 5$, R2
87 000532 012737 001400 MOV #1400, Q#OPREG
88 000540 105737 172414 TSTB #1400, Q#CSR
89 000544 100375 BPL #4, Q#CSR
90 000546 105037 172414 DEC R2
91 000552 005302 BR 4$, Q#OPREG
92 000554 000763 BR 4$, Q#OPREG
93 000556 010137 172416 MOV R1, Q#OPREG
94 000562 012737 001400 Q#OPREG
95 000570 105737 172414 TSTB #1400, Q#CSR
96 000574 100375 BPL #4, Q#CSR
97 000576 105037 172414 CLR B #4, Q#CSR
98 000602 013702 172416 Q#IOBUF, R2
99 000606 042702 177400 BIC #177400, R2
100 000612 022702 000000 CMP #0, R2
101 000616 001403 BEQ 6$, R2
102 000620 005204 INC R4
103 000622 005267 001152 INC EBUF
104 000626 010037 172416 MOV R0, Q#OPREG
105 000632 000207 RTS PC
106 000634 012737 001400 STAT: MOV #4352, Q#OPREG
107 000636 012737 002400 MOV #1280, Q#OPREG
108 000638 012737 002400 MOV #1280, Q#OPREG
109 000642 012737 002400 MOV #1280, Q#OPREG
110 000646 012737 002400 MOV #1280, Q#OPREG
111 000650 013700 172416 BIC #177400, R0
112 000654 042700 177400 BIC #177400, R0
113 000658 022700 000000 CMP #0, R0
114 000662 022700 000000

```

```

;SET POINTER=0
;FALSE READ DATA
;GOOD READ
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL INBUF1 COMMAND
;POINTER=0
;FALSE READ DATA
;GOOD RD
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;ZERO BUFFER POINTER
;GO CLEAR STATUS
;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH POINTER
;CLEAR MST BYTE
;SEL BUFFER
;POINTER=0
;NO FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;POINTER-1
;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH NEW POINTER
;CLEAR MST BYTE
;POINTER=0
;REPORT ERROR
;BUFFER ERROR
;SEL BUFFER
;RETURN
;WCR:RS
;RS(FALSE)
;RS
;FETCH STATUS
;CLEAR MST BYTE
;STATUS=0 ?

```

LDRMAC.MACRO MACRO V03.01 27-MAR-79 19:48:51 PAGE 2-2

```

115 000672 001403
116 000674 005204
117 000676 005267 001100
118 000702 022704 000000
119 000706 001406
120 000710 005203
121 000712 022703 000005
122 000716 001421
123 000720 000167 177240
124 000724 012775 000001
125 000732 005075 000004
126 000736 005075 000006
127 000742 005075 000010
128 000746 005075 000012
129 000752 052737 040000 172414
130 000760 000207
131 000762 012775 177777 000002
132 000770 016775 001002 000004
133 000776 016775 000776 000006
134 001004 016775 000772 000010
135 001012 010075 000012
136 001016 042737 040000 172414
137 001024 000207
138

;STATUS BAD REPORT
;STATUS ERROR
;WERE THERE ERRORS
;NO GOOD INIT
;ERROR +1
;ERROR LIMIT YET
;YES RETURN A 1(ERROR)
;NO RESTART VINIT
;GOOD INIT

;ENABLE INTERRUPTS
;BAD INIT

;DISABLE LIU

```

```

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57

;LIU HANDLER
L10: MOV R0, -(SP)
      MOV R1, -(SP)
      MOV R2, -(SP)
      MOV R3, -(SP)
      MOV R4, -(SP)
      MOV R5, -(SP)
      CLR DATA
      TST G#CSR
      BPL RTI$
      BIC #BIT14, G#CSR
      MOV #4352, G#OPREG
      MOV #1280, G#OPREG
      MOV G#IOBUF, R1
      BIC #177400, R1

;SAVE REGISTERS
;INT
;NO RETURN
;DISABLE
;WCR : RS(0)
;RD
;CLEAR VAR.
;CLEAR UNUSED BITS

;OV-FL
;COM=SEL INBUF 0
;COM=RDBUFFNT
;EMPTY BUFFER
;OV-FL
;COM=SEL INBUF 1
;COM=RDBUFFNT

;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUFADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO EMPTY AND LOAD

;GO CHECK PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUFADR
;COM=SEL INBUF1
;WHICH CRC BIT
;EMPTY AND LOAD

;GO CHECK PACKET
;RESTORE REGISTERS

;BUFFER OVERFLOW
B2$: BITB #BIT02, R1
      BEQ B3$
      MOV #4360, R2
      MOV #4384, R3
      JSR PC, EMBF
      BITB #BIT03, R1
      BEQ B3$
      MOV #4488, R2
      MOV #4512, R3
      JSR PC, EMBF

;BUFFERS FULL
B0$: BITB #BIT00, R1
      BEQ B1$
      MOV #4360, R2
      MOV #4384, R3
      MOV #BIT00, R4
      JSR PC, EMBF
      TST DATA
      BMI B1$
      JSR PC, PARSE
      BITB #BIT01, R1
      BEQ B1$
      MOV #4488, R2
      MOV #4512, R3
      MOV #BIT01, R4
      JSR PC, EMBF
      TST DATA
      BMI B1$
      JSR PC, PARSE

RTI$: MOV (SP)+, R5
      MOV (SP)+, R4
      MOV (SP)+, R3
      MOV (SP)+, R2
      MOV (SP)+, R1

```



```

58 001264 012602 (SP)+, R0
59 001266 052737 #BIT14, G#CSR
60 001274 000002 RTI
61
62
63
64 001276 012737 EMBF: MOV #4480., G#OPREG
65 001304 012737 MOV #1280., G#OPREG
66 001312 013705 MOV G#IOBUF, R5
67 001316 130405 BITB R4, R5
68 001320 001003 BNE 5$
69 001322 012767 MOV #1., DATA
70 001330 012737 MOV #INBF, G#BAR
71 001336 010237 MOV R2, G#OPREG
72 001342 012737 MOV #768., G#OPREG
73 001350 105737 TSTB G#CSR
74 001354 100375 BPL -4
75 001356 105037 CLRB G#CSR
76 001362 013704 MOV G#IOBUF, R4
77 001366 042704 BIC #177400, R4
78 001372 005407 NEG R4
79 001374 010437 MOV R4, G#CSR
80 001400 010337 MOV R3, G#OPREG
81 001404 012737 MOV #768., G#OPREG
82 001412 105737 TSTB G#CSR
83 001416 100375 BPL -4
84 001420 105037 CLRB G#CSR
85 001424 012737 MOV #8704., G#OPREG
86 001432 000240 NOP
87 001434 105737 TSTB G#CSR
88 001440 100403 BMI 7$
89 001442 012767 MOV #1., DATA
90 001450 105037 CLRB G#CSR
91 001454 012737 MOV #2304., G#OPREG
92 001462 105737 TSTB G#CSR
93 001466 100375 BPL -4
94 001470 105037 CLRB G#CSR
95 001474 000207 RTS PC
96
97
98 001476 012702 PARSE: MOV #INBF, R2
99 001502 116203 MOV 1(R2), R3
100 001506 122703 CMPB #2, R3
101 001512 001007 BNE 2$
102 001514 012767 MOV #1, IGOT
103 001522 012767 MOV #1, IACK
104 001530 000207 RTS PC
105 001532 122703 CMPB #3, R3
106 001536 001007 BNE 3$
107 001540 012767 MOV #1, IGOT
108 001546 012767 MOV #1, INAK
109 001554 000207 RTS PC
110 001556 012767 MOV #1, IFLT
111 001564 012767 MOV #1, IGOT
112 001572 000207 RTS PC
113

```


LDRMAC.MACRO MACRO V03.01 27-MAR-79 19:48:51 PAGE 4

```

1 2
3 4 001574 017500 000002 ;WRITE RAM
5 001600 017501 000004 RAM: MOV GARG1(R5), R0
6 001604 012737 010402 172416 MOV GARG2(R5), R1
7 001612 062700 004400 MOV #4354., G#OPREG
8 001616 012737 012416 ADD #2304., R0
9 001622 105737 172414 MOV #2304., G#OPREG
10 001626 100375 BPL G#CSR
11 001630 105037 172414 BPL -4
12 001634 012737 010401 CLR G#CSR
13 001642 062701 004400 MOV #4354., G#OPREG
14 001646 010137 172416 ADL #2304., R1
15 001652 105737 172414 MOV R1, G#OPREG
16 001656 100375 BPL G#CSR
17 001660 105037 172414 BPL -4
18 001664 000207 CLR G#CSR
19 RTS PC
20
21 ;DISABLE INTERRUPTS
22 001666 042737 040000 BIC #BIT14, G#CSR
23 001674 000207 RTS PC
24
25 ;WRITE LOOP
26
27 001676 017502 000002 WTLOOP: MOV GARG1(R5), R2
28 001702 012737 000000 NEG #OUTBF, G#EAR
29 001710 005402 172412 R2, G#WCR
30 001712 010237 172412 MOV R2, G#WCR
31 001716 012737 010540 MOV #4448., G#OPREG
32 001724 012737 024000 MOV #10240., G#OPREG
33 001732 000240 NOP
34 001734 105737 172414 TSTB G#CSR
35 001740 100401 BMI CMD
36 001742 000000 HALT
37 001744 012737 010420 CMD: MOV #4358., G#OPREG
38 001752 012737 004421 MOV #4421, G#OPREG
39 001760 105737 172414 TSTB G#CSR
40 001764 100375 BPL -4
41 001766 105037 172414 CLR G#CSR
42 001772 000207 RTS PC
43
44 .PSECT
45 001774
46
47 DATA: .WORD 0
48 ERAM: .BLKW
49 EBUF: .BLKW
50 ESTAT: .BLKW
51
52 .PSECT LOOP1,RW,D,GBL,REL,OV
53 .BLKB 256.
54 INBF: .BLKB 256.
55 FILE: .BLKW 4.
56
57 .PSECT LOOP2,RW,D,GBL,REL,OV

```

```

;ADDRESS
;WRITE DATA
;SEL LDATA
;WD/DATA (ADDR)
;WRITE DATA
;VALID WRITE
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;SEL ACRAM
;WD/DATA (CMD)
;WRITE
;VALID WRITE
;NO LOOP UNTIL READY
;CLEAR DONE BIT

```

;DIABLE LIU

```

;BYTE COUNT
;BUS ADDRESS
; 2'S COMP COUNT
;COUNT
;OEO COMMAND
;DMA GO
;INTERFACE TIME
;DMA OK
;YES

```

```

;MODSTAT COMMAND
;BUFFERS FULL
;GOOD WRITE
;NO LOOP UNTIL
;CLEAR DONE BIT

```

LDRMAC.MACRO MACRO V03.01 27-MAR-79 19:48:51 PAGE 4-1

```

58 000000
59 000020
60 000040
61 000042
62 000044
63 000046
64
65
66

```

```

ILID:
ILND:
IGOT:
IACK:
INAK:
IFLT:

```

```

.BLKW 8.
.BLKW 8.

```

000001

.END

LDRMAC-MACRO SYMBOL TABLE MACRO V03.01 27-MAR-79 19:48:51 PAGE 4-2

```

ARG1 = 000002
ARG2 = 000004
ARG3 = 000006
ARG4 = 000010
ARG5 = 000012
BAR = 172410
BIT00 = 000001
BIT01 = 000002
BIT02 = 000004
BIT03 = 000010
BIT04 = 000020
BIT05 = 000040
. ABS. 000000
. ABS. 002004
LOOP1 001010
LOOP2 000050
ERRORS DETECTED: 0

BIT06 = 000100
BIT07 = 000200
BIT08 = 000400
BIT14 = 040000
BIT15 = 100000
B0$ 001152R
B1$ 001212R
B2$ 001106R
B3$ 001130R
CMD 001744R
CSR = 172414
DABLE 001666RG

DATA
EBUF
EMBF
GRAM
ESTAT
FILE
IACK
IFLT
IGOT
ILID
ILND
INAK

001774R
002000R
001276R
001776R
002002R
001000R
000042R
000046R
000040R
000000R
000020R
000044R

INBP
INBUF0
INBUF1
INIT
IOBUF = 172416
002 LINIT
003 LIO
003 MTP
003 OPREG = 172416
003 OTRUF0
003 OTRUF1
003 OUTBF

000400R
000342R
000376R
000064RG
172416
000164R
001026R
000000RG
172416
000432R
000466R
000000R

002 PARSE
RAM
RBUF = 177562
RCSR = 177560
RT1$ 001252R
STAT = 000034R
WCR = 172412
WLOOP = 001676RG
XBUF = 177566
XCSR = 177564
ZEROP 000464R
002

```

VIRTUAL MEMORY USED: 300 WORDS (2 PAGES)
 DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
 .DK:LDRMAC-DK:LDRMAC

PAGE 001

12:00:00

01-JUL-79

FIML DK.COM

PROGRAM:

DECRYPTER

ASS DX0: DK
 R LINK
 DX1:FIML DK=DX1:FIML DK/C
 DX1:LINKAC//
 ASS DX1: DK

	<u>PAGE</u>
3.0 <u>MACRO-11 PROGRAMS</u>	
3.1 DIAGNOSTICS LOOP 5	57
3.2 IEEE PROGRAMS	99
3.3 LOADER PROMS	115

3.1 DIAGNOSTICS**PAGE**

3.1.1	CPU	58
3.1.2	MEMORY	66
3.1.3	LIU INTERFACE	72
3.1.4	LIU ACRAM	78
3.1.5	LIU BUFFER	85
3.1.6	RTC	93
3.1.7	OPERATING INSTRUCTION	97

3.1.1 LSI-11 CPU TEST

Source File: LSICPU.MAC

Task File: LSICPU.SAV

PURPOSE: To test LSI-11/2 CPU

DESCRIPTION: This program tests all LSI-11/2 single, double and jump instruction sets. The program loops until an error is detected in the CPU; when detected, the current program address is displayed showing what instruction set is causing the error.


```

1 2
3 4
5 6
7 8 001102 112702 000377 TEST2: MOV #000377,R2 1000
9 001105 105002 CLRB R2 ;177777 0100
10 001110 105102 COMB R2 ;177400 1001
11 001112 105602 SBCB R2 ;177777 1000
12 001114 105202 INCB R2 ;177777 1000
13 001116 105402 NEGB R2 ;177401 0001
14 001120 105302 DECB R2 ;177400 0101
15 001122 000302 SWAB R2 ;000377 1000
16 001124 106302 ASLB R2 ;000376 1001
17 001126 106002 RORB R2 ;000377 1010
18 001130 106202 ASRB R2 ;000377 1001
19 001132 106102 ROLB R2 ;000377 1001
20 001134 105502 ADCB R2 ;000000 0101
21 001136 105702 TSTB R2 ;000000 0100
22 001140 001056 BNE MSTART ;ERROR
23
24
25
26
27
28
29
30
31
32
33 001142 012702 001324 TEST3: MOV #T3DATA,R2
34 001146 011201 MOV (R2),R1
35 001150 022201 CMP (R2),R1
36 001152 001051 BNE MSTART
37 001154 063201 ADD Q(R2),R1
38 001156 165201 SUB Q-(R2),R1
39 001160 044201 BIC -(R2),R1
40 001162 056201 BIS 4(R2),R1
41 001166 037201 BIT Q6(R2),R1
42 001172 001441 BEQ MSTART
43 001174 074101 XOR R1,R1
44 001176 001037 BNE MSTART ;ERROR
45
46
47
48

```

```

1 1
2 2
3 3
4 4
5 5
6 001200 012701 001210
7 001204 000111
8 001206 000433
9 001210 022121
10 001212 000131
11 001214 001216
12 001216 000161
13 001222 000425
14
15
16
17
18
19
20
21
22
23
24
25 001224 001270 001334
26 001230 005767 000100
27 001234 100220
28 001236 105767 000072
29 001242 001015
30 001244 105721
31 001246 001013
32 001250 105711
33 001252 100011
34 001254 105741
35 001256 001007
36 001260 005721
37 001262 100005
38 001264 005711
39 001266 001003
40 001270 005741
41 001272 100001
42 001274 000401
43
44
45
46 001276 000000
47
48
49
50

```

```

1  ;***TEST6:***
2  ;** CHECK ALL BYTE DOUBLE OPERAND **
3  ;** INSTRUCTIONS/MODE ZERO **
4  ;*****
5  ;*****
6  ;*****
7  TEST6:      #T6IATA,R1
8              (R1)+,R2
9              BICB (R1),R2
10             BITB (R1),R2
11             BNE  MSTART
12             BISB (R1),R2
13             CMPB -(R1),R2
14             BNE  MSTART
15             BR   TEST7
16
17            T3DATA: .WORD T3DATA
18                  .WORD T3LATA
19                  .WORD 177777
20            T3DAT4: .WORD T3DAT4
21                  .WORD 100000
22            T5DATA: .WORD 0
23                  .WORD 125377
24            T6DATA: .WORD 125377
25
26
27
28
29

```

ISI-11 CPU-DIAGNOSTICS MACRO V03.01 27-MAR-79 18:04:33 PAGE 5

```

1 2
2 3
3 4
4 5
5 6
6 7 001342 012702 001474 TEST?: MOV #T7DATA,R2
8 001346 016203 000002 MOV 2(R2),R3
9 001352 012213 MOV (R2)+,(R3)
10 001354 024223 CMP -(R2),(R3)+
11 001356 001347 BNE MSTART
12
13 001360 005063 CLR -2(R3)
14 001364 012232 MOV (R2)+,(R2)+
15 001366 062243 ADD (R2)+,-(R3)
16 001370 164213 SUB -(R2),(R3)
17 001372 005272 INC Q-2(R2)
18 001376 005352 DEC Q-(R2)
19 001400 044223 BIC -(R2),(R3)+
20 001402 001335 BNE MSTART
21
22 001404 012223 MOV (R2)+,(R3)+
23 001406 005443 NEG -(R3)
24 001410 031362 BIT (P3),2(R2)
25 001414 001730 BEQ MSTART
26
27 001416 006213 ASR (R3)
28 001420 116213 MOV 2(R2),(R3)
29 001424 005623 SBC (R3)+
30 001426 006163 ROL -2(R3)
31 001432 001321 BNE MSTART
32
33 001434 056232 BIS 2(R2),Q(R2)+
34 001440 006352 ASL Q-(R2)
35 001442 006032 ROR Q(R2)+
36 001444 000261 SEC
37 001446 005552 ADC Q-(R2)
38 001450 005332 DEC Q(R2)+
39 001452 021252 CMP (R2),Q-(R2)
40 001454 001310 BNE MSTART
41
42 001456 016243 MOV 2(R2),-(R3)
43 001462 005013 CLR (R3)
44 001464 005113 COM (R3)
45 001466 021362 CMP (R3),-2(R2)
46 001472 001403 BEQ TEST8
47
48 001474 177777 T7DATA: .WORD 177777
49 001476 000500 .WORD 000500
50 001500 000001 .WORD 000001
51
52
53
54

```

;ERROR

;ERROR

;ERROR

;ERROR

;ERROR


```

1  ;*****
2  ;* TESTB:
3  ;* CHECK ALL BYTE INSTRUCTIONS
4  ;* MODES(DMBX)
5  ;*****
6  TESTB:
7  MOV 001502 0011203
8  MOV 001504 016213
9  CLRB 001510 105013
10 COMB 001512 105123
11 SBCB 001514 105632
12 INCB 001516 105243
13 NEGB 001520 105452
14 DECB 001522 105323
15 SWAB 001524 000363
16 ASLB 001530 106332
17 ROHB 001532 106072
18 ASRB 001536 106243
19 HOLB 001540 106113
20 ADCB 001542 105523
21 CMPB 001544 121343
22 BNE 001546 001253
23 BISB 156252 177774
24 BICB 24 001550 146223 177776
25 BITB 25 001554 136243 177776
26 BEQ 26 001564 001401
27 BR 27 001566 000643
28
29
30 LOOP: JMP TEST1
31
32
33
34 .END CLEAR

```

LSI-11 CPU-DIAGNOSTICS MACRO V03.01 27-MAR-79 16:04:33 PAGE 6-1
SYMBOL TABLE

CLEAR	001000	LOOP	001570	TEST3	001142	T3DATA	001330
CLR\$	001020	MSTART	001276	TEST4	001200	T5DATA	001334
JMP1	001210	TEST1	001050	TEST5	001224	T6DATA	001340
JMP3	001216	TEST2	001102	TEST6	001300	T7DATA	001474
.ABS.	001574	000					
	000000	001					
ERRORS DETECTED:	1						

VIRTUAL MEMORY USED: 286 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
.DK:LSICPU=DK:LSICPU

3.1.2 LSI-11 MEMORY TEST

Source File: LSIMEM.MAC

Task File: LSIMEM.SAV

PURPOSE: To test LSI-11 Mos Memory board

DESCRIPTON: This program writes and reads various patterns into the LSI-11 memory starting at the last address of the program, to address 160000₈. The patterns used are all zeros, all ones, alternating ones and zeros and random numbers. If the data read from a memory location doesn't match the data written to it, the program halts displaying the current program address which is used to determine which test failed and location MSTART contains the bad memory address.

TITLE MEMORY-DIAGNOSTICS

```
.IDENT/V1.0/
.ASECT
      =1000
```

```
1
2
3 000000 001000
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
```

```
*****
;* TESTS LSI-11 MEMORY (32KW)
;*
;* 1-WRITES AND READS ADDRESS INTO MEM.
;* 2-WRITES & READS ALL ZEROS
;* 3-WRITES & READS ALL ONES
;* 4-WRITES & READS ALT. ONES & ZEROS
;* 5-WRITES & READS ALT. ZEROS & ONES
;* 6-WRITES & READS RANDOM NUMBERS
;*
;* NOTE: PROGRAM HALTS ON A DETECTED ERROR
;* SEE LISTING FOR CAUSE(PC), WITH
;* (MSTART) HOLDING MEMORY ERROR
;* LOCATION.
*****
```

```

CLEAR:  MOV     #340,  R0
        MTPS    R0
        MOV     #500,  SP
        MOV     #400,  R2
CLR$:   CLR     -(R2)
        MOV     R2,    -(R2)
        TST     R2
        BNE     CLR$,  Q#40
        MOV     #CLEAR, Q#40
        MOV     #TIME, Q#100
        BR      START
        TIME:   RTI

;RESET BUS
;PRI=7
;SET PS
;INITIALIZE STACK
;R2=TOP OF VECTOR AREA
;INSERT A HALT
;POINT VECTOR TO HALT
;?R2=0 (DONE)
;NO CONTINUE
;RESTART ADDRESS
;CLOCK RETURN
;BEGIN
;IF CLOCK ON RETURN
```

```
;SEGMENT WRITES THE ADDRESS OF THE MEMORY ADDRESS
;INTO THAT ADDRESS
```

```

START:  MOV     Q#LOWLIM, R2
        MOV     Q#TOPLIM, R3
ALDR$:  MOV     R2,(R2)+
        CMP     R2,R3
        BLOS   ALDR$
CHECK:  CMP     -(R2),R2
        BEQ    ADCONT
        MOV     R2,    Q#MSTART
        HALT
ADCONT: CMP     R2,    R3
        BNE     CHECK
```

```
;LOW ADDRESS OF MEMORY
;HIGHEST ADDRESS
;ADDRESS=ADDRESS
;LOW=TOP
;? ADDRESS=ADDRESS
;YES
;NO ADDRESS AND HALT
;***ERROR IN TEST
;TOP=LOW
```



```

1
2
3
4 001104 012737 000000 001424 MASK1: MOV #0, G#MASK
5 001112 004767 000044 JSR PC, RDWTM
6 001116 012737 177777 MASK2: MOV #177777, G#MASK
7 001124 004767 000032 JSR PC, RDWTM
8 001130 012737 125252 MASK3: MOV #125252, G#MASK
9 001136 004767 000020 JSR PC, RDWTM
10 001142 012737 052525 MASK4: MOV #52525, G#MASK
11 001150 004767 000006 JSR PC, RDWTM
12 001154 004767 000044 MASK5: JSR PC, RANDOM
13
14 001160 000733 BR START
15
16
17
18
19
20 001162 013701 001424 RDWTM: MOV G#MASK, R1
21 001166 013702 001426 MOV G#LOWLIM, R2
22 001172 013703 001430 MOV G#TOPLIM, R3
23
24 001176 010122 WRITE: MOV R1, (R2)+
25 001200 020203 CMP R2, R3
26 001202 101775 BLOS WHITE
27 001204 024201 CMP -(R2), R1
28 001206 001403 BEQ MEMOK
29 001210 010237 MOV R2, G#MSTART
30 001214 000000 HALT
31
32 001216 020203 MEMOK: CMP R2, R3
33 001220 001371 BNE READ
34 001222 000207 RTS PC
35
;SEGMENT SELECTS MASKS AND CALLS WRITER
;AND READER
;MASK=000...0
;MASK=111...1
;MASK=1010...10
;MASK=0101...01
;RANDOM NUMBERS
;LOOP AROUND

;SEGMENT WPITES AND THEN READS BACK MASK
;HALTS ON ERROR
;LOW=TOP
;ADDRESS=MASK
;ERROP ADDRESS
;***ERROR IN TEST
;TOP=LOW

```

```

1 1
2 2
3 3
4 4 001224 013703 001426 001430 001432 001434
5 5 001230 013704 001430 001432 001434 001436
6 6 001234 012737 176543 001432 001434 001436
7 7 001242 012737 123456 001432 001434 001436
8 8
9 9 001250 004767 000056 000056 000056 000056
10 10 001254 013723 001436 001436 001436 001436
11 11 001260 020304 001436 001436 001436 001436
12 12 001262 101772 001436 001436 001436 001436
13 13 001264 012737 176543 001432 001434 001436
14 14 001272 012737 123456 001432 001434 001436
15 15 001300 013703 001426 001432 001434 001436
16 16 001304 004767 000022 000022 000022 000022
17 17 001310 022337 001436 001436 001436 001436
18 18 001314 001403 001436 001436 001436 001436
19 19 001316 010337 001436 001436 001436 001436
20 20 001322 000000 001436 001436 001436 001436
21 21
22 22 001324 020304 001436 001436 001436 001436
23 23 001326 001366 001436 001436 001436 001436
24 24 001330 000207 001436 001436 001436 001436
25 25
26 26
27 27
28 28 001332 010046 001436 001436 001436 001436
29 29 001334 010146 001436 001436 001436 001436
30 30 001336 010246 001436 001436 001436 001436
31 31 001340 013700 001434 001434 001434 001434
32 32 001344 013701 001432 001432 001432 001432
33 33 001350 012702 177771 177771 177771 177771
34 34 001354 006300 001434 001434 001434 001434
35 35 001356 006101 001434 001434 001434 001434
36 36 001360 005202 001434 001434 001434 001434
37 37 001362 001374 001434 001434 001434 001434
38 38 001364 063700 001434 001434 001434 001434
39 39 001370 005501 001434 001434 001434 001434
40 40 001372 062701 001434 001434 001434 001434
41 41 001376 010037 001434 001434 001434 001434
42 42 001402 010137 001432 001432 001432 001432
43 43 001406 013737 001432 001432 001432 001432
44 44 001414 012602 001432 001432 001432 001432
45 45 001416 012601 001432 001432 001432 001432
46 46 001420 012600 001432 001432 001432 001432
47 47 001422 000207 001432 001432 001432 001432
48 48
49 49
50 50
51 51 001424 001442 001442 001442 001442 001442
52 52 001426 001442 001442 001442 001442 001442
53 53 001430 150000 150000 150000 150000 150000
54 54 001432 176543 176543 176543 176543 176543
55 55 001434 123456 123456 123456 123456 123456
56 56 001436 001436 001436 001436 001436 001436
57 57 001440 001440 001440 001440 001440 001440

```

;SEGMENT GENERATES RANDOM NUMBERS AND WRITES THEN INTO
 ;MEMORY RESET SEEDS AND READS AND COMPARES MEMORY
 ;LOW ADDRESS OF MEMORY
 ;HIGH LIMIT OF MEMORY
 ;HI SEED
 ;LOW SEED
 ;GENERATE RANDOM NUMBER
 ;LOAD NUMBER
 ;?ALL OF MEMORY YET
 ;RESET SEED
 ;RESET SEED
 ;RESET LIMIT
 ;GENFRATE RANDOM NUMBER
 ;MEM=RAM#
 ;ERROR IN MEMORY
 ;STOP CPU
 ;?ALL OF MEMORY YET

***** RANDOM NUMBER GENERATOR *****
 ;SAVE R0
 ;SAVE R1
 ;SAVE R2
 ;LOW VALUE
 ;HIGH VALUE
 ;SHIFT COUNT
 ;SHIFT R0 LEFT AND
 ;ROTATE CARRY INTO R1 AND
 ;CHECK IF DONE
 ;CONTINUE SHIFT LOOP
 ;ADD # TO MAKE X 129
 ;PROPOGATE CARRY
 ;ADD HIGH CONSTANT
 ;SAVE NUMBER
 ;DEPOSIT RANDOM #
 ;POP STACK INTO R2
 ;POP STACK INTO R1
 ;POP STACK INTO R0

MASK: .BLKW
 LOWLIM: .WORD
 TOPLIM: .WORD
 HINUM: .WORD
 LONUM: .WORD
 RANUM: .BLKW
 MSTART: .BLKW

MEMORY-DIAGNOSTICS MACRO V03.01 27-MAR-79 18:04:00 PAGE 3-1

58
59 001000

.END CLEAR

MEMORY-DIAGNOSTICS MACRO V03.01 27-MAR-79 16:04:00 PAGE 3-2

SYMBOL TABLE

ALCONT 001100	GENRAN 001332	MASK1 001104	MSTART 001440	READ 001204
ADDRT 001060	G1\$ 001354	MASK2 001116	RANDOM 001224	START 001050
CHECK 001066	HINUM 001432	MASK3 001130	RANUM 001436	TIME 001046
CLEAR 001000	LONUM 001434	MASK4 001142	RDRAN 001264	TOPLIM 001430
CLR\$ 001020	LOWLIM 001426	MASK5 001154	RLWTM 001162	WRITE 001176
CONT 001324	MASK 001424	MEMOK 001216	RD1 001304	WRTRAN 001250

. ABS. 001442 000
 002002 001
 ERRORS DETECTED: 2

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
 DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
 .DX:LSIMEM=DX:LSIMEM

3.1.3 BLIUI INTERFACE TEST

Source File: LIUINT.MAC

Task File: LIUINT.SAV

PURPOSE: To test the LSI-11/LIU interface

DESCRIPTION: This program tests the interface registers with patterns of all zeros and ones and continues by selecting INPUT BUFFER 0, where it performs a DMA write of pattern of alternating zeros and ones and then performs a DMA READ, testing the data read back. Should the data written not compare to the data read, the program halts, displaying the current program address.

```

1  .TITLE BLUI INTERFACE DIAG.
2  .IDENT /V1.0/
3  .ASECT
4  .= 1000
5  BAR= 172410
6  WCR= 172412
7  CSR= 172414
8  OPREG= 172416
9
10 *****
11 ** DIAGNOSTIC FOR BUS LOOP INTERFACE UNIT
12 **
13 ** INTERFACE (BLUI).
14 ** (1)-CLEAR AND SET UP VECTORS
15 ** (2)-RESET INBUF0 POINTER
16 ** (3)-DMA 256 BYTES FROM LIU
17 ** (4)-COMPARE REC. DATA TO SENT DATA HALT
18 ** IF NOT THE SAME.
19 *****
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56

```

BLUI INTERFACE DIAG. MACRO V03.01 27-MAR-79 18:43:04 PAGE 2

```

1 001152 012700 001762 001760 TST2: MOV #SIATA, R0
2 001156 012737 052652 001760 MOV #052652, G#MASK
3 001164 005037 001756 CLR G#FLAG
4 001170 012702 177600 MOV #-128., R2
5 001174 013720 001760 MOV G#MASK, (R0)+
6 001200 005202 INC R2
7 001202 001374 BNE T2$
8 001204 000400 BR REG
9
10
11
12 001206 012737 177777 172410 REG: MOV #177777, G#BAR
13 001214 013700 172410 MOV G#BAR, R0
14 001220 022700 177777 CMP #177777, R0
15 001224 001401 BEQ 1$
16 001226 000000 HALT
17 001230 012737 000000 MOV #000000, G#BAR
18 001236 013700 172410 MOV G#BAR, R0
19 001242 022737 000000 CMP #0, G#BAR
20 001250 001401 BEQ 2$
21 001252 000000 HALT
22 001254 012737 177777 172412 2$: MOV #177777, G#WCR
23 001262 013700 172412 MOV G#WCR, R0
24 001266 022700 177777 CMP #177777, R0
25 001272 001401 BEQ 3$
26 001274 000000 HALT
27 001276 012737 000000 172412 3$: MOV #000000, G#WCR
28 001304 013700 172412 MOV G#WCR, R0
29 001310 022700 000000 CMP #0, R0
30 001314 001401 BEQ 4$
31 001316 000000 HALT
32 001320 052737 040100 172414 4$: BIS #040100, G#CSR
33 001326 032737 040100 172414 BIT #040100, G#CSR
34 001334 001001 BNE 5$
35 001336 000000 HALT
36 001340 005037 172414 5$: CLR G#CSR
37 001344 032737 040100 172414 BIT #040100, G#CSR
38 001352 001401 BEQ SEND
39 001354 000000 HALT
40

```

```

;DATA BLOCK ADDRESS
;010101..01
;CLEAR LOCAL STATUS
;# FO DATA BYTES
;DEPOSIT DATA
;256 TIMES

;WRITE ALL ONES
;READ
;WAS IF THE SAME
;YES

;WRITE ALL ZEROS
;READ
;SAME
;YES
;NO ERROR
;WRITE ALL ONES WCR
;READ
;SAME
;YES
;NO ERROR
;WRITE ALL ONE WCR
;READ
;SAME
;YES
;NO HALT
;SET R/W BITS
;BITS ON
;YES
;ERROR
;CLEAR BITS
;BITS ON
;NO
;YES ERROR

```

```

1
2
3
4 001356 012737 010440 172416 SEND: MOV #4384., @OPREG
5 001364 012737 001762 172410 MOV #SIATA, @BAR
6 001372 012705 000400 MOV #256., R5
7 001376 005405 NEG R5
8 001400 010537 172412 MOV R5, @WCR
9 001404 012737 024000 172416 MOV #10240., @OPREG
10 001412 000240 NOP
11 001414 105737 172414 SDMA: TSTB @CSR
12 001420 100401 BMI RECV
13 001422 000000 ES$: HALT
14
15 001424 105037 172414 RECV: CLRB @CSR
16 001430 012737 001400 MOV #768., @OPREG
17 001436 105737 172414 TSTB @CSR
18 001442 100375 BPL -4
19 001444 105037 172414 RV1$: CLRB @CSR
20 001450 012737 010440 MOV #4384., @OPREG
21 001456 012737 002364 172410 MOV #RDATA, @BAR
22 001464 012705 000400 MOV #256., R5
23 001470 005405 NEG R5
24 001472 010537 172412 MOV R5, @WCR
25 001476 012737 021000 172416 MOV #8704., @OPREG
26 001504 000240 NOP
27 001506 105737 172414 RDMA: TSTB @CSR
28 001512 100375 BPL -4
29
30
31
32 001514 105037 172414 SWAP: CLRB @CSR
33 001520 012700 002364 MOV #RDATA, R0
34 001524 012701 177600 MOV #-128., R1
35 001530 000320 SWB: SWAB (R0)+
36 001532 005201 INC R1
37 001534 001375 BNE SWB
38
39
40
41 001536 013700 001760 TSTDAT: MOV @MASK, R0
42 001542 012701 002364 MOV #RDATA, R1
43 001546 012702 177600 MOV #-128., R2
44 001552 020021 CMP R0, (R1)+
45 001554 001401 BEQ INCT
46 001556 000000 HALT
47 001560 005202 INCT: INC R2
48 001562 001373 BNE CMP
49
50 001564 012700 177600 CLRRD: MOV #-128., R0
51 001570 012701 002364 MOV #RDATA, R1
52 001574 005021 C1$: CLR (R1)+
53 001576 005200 INC R0
54 001600 001375 BNE C1$
55 001602 000167 JMP TST
56

```



```

1 3 001606 012700 010410 BUFFNT: MOV #4360., R0
2 4 001612 012701 010440 MOV #4384., R1
3 5 001616 010037 172416 MOV R0, Q#OPREG
4 6 001622 012737 001400 MOV #768., Q#OPREG
5 7 001630 105737 172414 TSTB Q#CSR
6 8 001634 100375 BPL -4
7 9 001636 105037 172414 Q#CSR
8 10 001642 013702 172416 MOV Q#OPREG, R2
9 11 001646 042702 177400 BIC #177400, R2
10 12 001652 010137 172416 MOV R1, Q#OPREG
11 13 001656 022702 000000 CMP #0, R2
12 14 001662 001412 BEQ CKBP
13 15 001664 012737 001400 MOV #768., Q#OPREG
14 16 001672 105737 172414 TSTB Q#CSR
15 17 001676 100375 BPL -4
16 18 001700 105037 172414 Q#CSR
17 19 001704 005302 DEC R2
18 20 001706 000763 BR CP1$
19 21 001710 010037 172416 MOV R0, Q#OPREG
20 22 001714 012737 001400 MOV #768., Q#OPREG
21 23 001722 105737 172414 TSTB Q#CSR
22 24 001726 100375 BPL -4
23 25 001730 105037 172414 Q#CSR
24 26 001734 013702 172416 MOV Q#OPREG, R2
25 27 001740 042702 177400 BIC #177400, R2
26 28 001744 022702 000000 CMP #0, R2
27 29 001750 001001 BNE EBPT
28 30 001752 000207 RTS PC
29 31 001754 000000 EBPT: HALT
30 32
31 33
32 34
33 35 001756 000000 FLAG: .WORD 0
34 36 001760 000000 MASK: .BLKW
35 37 001762 177777 SDATA: .BLKB 256.
36 38 002362 177777 EOBS: .WORD 177777
37 39 002364 177777 RDATA: .BLKB 256.
38 40 002764 177777 EOBR: .WORD 177777
39 41
40 42
41 43 001000 .EVEN
42 .END CLEAR
43

```

BLUI INTERFACE DIAG. MACRO V03.01 27-MAR-79 18:03:04 PAGE 4-1
SYMBOL TABLE

BAR = 172410	CLR\$	001020	ES\$	001422	REG	001206	TIME	001004
BUFFN\$ 001606	CMP	001552	FLAG	001756	RUN	001066	TST	001104
B1\$ 001636	CP1\$	001650	INCT	001560	RV1\$	001444	TSTDAT	001536
B2\$ 001700	CSR	172414	MASK	001760	SDATA	001762	TST1	001114
B3\$ 001730	C1\$	001574	OPREG =	172416	SDMA	001414	TST2	001152
CXBP 001710	EBPT	001754	RDATA	002364	SEND	001356	T1\$	001140
CLEAR 001000	EOBR	002764	RDMA	001506	SWAP	001514	T2\$	001174
CLRDRD 001564	EOBS	002362	RCV	001424	SWE	001530	WCR	= 172412

. ABS. 002766 000
000000 001
ERRORS DETECTED: 1

VIRTUAL MEMORY USED: 286 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
,DK:LIUINT-DK:LIUINT

3.1.4 LIU ADDRESS COMPARISON RAM TEST

Source File: LIURAM.MAC

Task File: LIURAM.SAV

PURPOSE: To test the address comparison RAM of the LIU

DESCRIPTION: This program writes and reads various patterns to the address comparison RAM. The patterns used are all zeros, all ones, alternating ones and zeros and random numbers. If the data read from a memory location doesn't match the data written to it, the program halts displaying current program address. Otherwise, the program loops, repeating the test indefinitely.

```

1
2
3 000000
4
5 001000
6 172416
7 172414
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22

```

```

.TITLE ACRAM-DIAGNOSTICS
.IDENT /V1.0/
.ASECT
=1000
OPREG= 172416
CSR= 172414

```

```

*****
;* TESTS LIU ACRAM MEMORY
;*
;* TEST1- WRITE & READ 0000
;* TEST2- WRITE & READ 1111
;* TEST3- WRITE & READ 1010
;* TEST4- WRITE & READ 0101
;* TEST5- RANDOM NUMBERS
;* RESET- CHECK POINTER
;*
;* NOTE: PROGRAM HALTS ON A DETECTED ERROR
;*
;* SEE LISTING FOR CAUSE(PC).
*****

```

```

23 001000 000005
24 001002 012701 000340
25 001006 106400
26 001010 012706 000500
27 001014 012702 000400
28 001020 005042
29 001022 010242
30 001024 005702
31 001026 001374
32 001030 012737 001000 000040
33 001036 012737 000100 000100
34 001044 052737 000001 172414
35 001052 000240
36 001054 042737 000001 172414
37 001062 000401
38 001064 000002
39
40
41
42 001066 004567 001024
43 001072 012700 000400
44 001076 012737 010401 172416
45 001104 012737 004400 172416
46 001112 105737 172414
47 001116 100375
48 001120 105037 172414
49 001124 077011
50

```

```

;RESET BUS
;PRI=7
;SET PS
;INITIALIZE STACK
;R2=TOP OF VECTOR AREA
;INSERT A HALT
;POINT VECTOR TO HALT
;R2=0 (DONE)
;NO CONTINUE
;RESTART ADDRESS
;CLOCK
;INIT LIU

;RESET LIU
;BEGIN
;IF CLOCK ON RETURN

;COUNTER
;ACRAM SELECT
;WD
;VALID PD
;NO LOOP UNTIL
;CLEAR DONE
;256 TIMES

```



```

1 3 001126 004567 000764
2 4 001132 005000
3 5 001134 012737 010401 172416 RESET
4 6 001142 012737 001400 172416 R1: MOV #4353., @OPREG
5 7 001150 105737 172414 TSTB @CSR
6 8 001154 100375 BPL -4
7 9 001156 105037 172414 CLR @CSR
8 10 001162 013703 172416 MOV @CFRFG, R3
9 11 001166 042703 177760 BIC #177760, R3
10 12 001172 022703 000000 CMP #0, R3
11 13 001176 001005 BNE E1:
12 14 001200 005200 INC R0
13 15 001202 022700 000400 CMP #256., R0
14 16 001206 001355 BNE R1:
15 17 001210 000401 BR TEST2
16 18 001212 000000 E1: HALT
17 19
18 20
19 21
20 22 001214 004567 000676 TEST2: JSR R5, RESET
21 23 001220 012700 000400 MOV #256., R0
22 24 001224 012737 010401 172416 MOV #4353., @OPREG
23 25 001232 012737 004417 172416 W2: MOV #4417, @OPREG
24 26 001240 105737 172414 TSTB @CSR
25 27 001244 100375 BPL -4
26 28 001246 105037 172414 CLR @CSR
27 29 001252 077011 SOB R0, W2:
28 30
29 31
30 32 001254 004567 000636 JSR R5, RESET
31 33 001260 005000 CLR R0
32 34 001262 012737 010401 172416 MOV #4353., @OPREG
33 35 001270 012737 001400 172416 R2: MOV #768., @OPREG
34 36 001276 105737 172414 TSTB @CSR
35 37 001302 100375 BPL -4
36 38 001304 105037 172414 CLR @CSR
37 39 001310 013703 172416 MOV @OPREG, R3
38 40 001314 042703 177760 BIC #177760, R3
39 41 001320 022703 000017 CMP #17, R3
40 42 001324 001005 BNE E2:
41 43 001326 005200 INC R0
42 44 001330 022700 000400 CMP #256., R0
43 45 001334 001355 BNE R2:
44 46 001336 000401 BR TEST3
45 47 001340 000000 E2: HALT
46 48
47 49
48 50 001342 004567 000550 JSR R5, RESET
49 51 001346 016700 177026 MOV #256., R0
50 52 001352 012737 010401 172416 TEST3: MOV #4353., @OPREG
51 53 001360 012737 004412 172416 W3: MOV #4412, @OPREG
52 54 001366 105737 172414 TSTB @CSR
53 55 001372 100375 BPL -4
54 56 001374 105037 172414 CLR @CSR
55 57 001400 077011 SOB R0, W3:
56 58
57 59
58 60
59 61
60 62
61 63
62 64
63 65
64 66
65 67
66 68
67 69
68 70
69 71
70 72
71 73
72 74
73 75
74 76
75 77
76 78
77 79
78 80
79 81
80 82
81 83
82 84
83 85
84 86
85 87
86 88
87 89
88 90
89 91
90 92
91 93
92 94
93 95
94 96
95 97
96 98
97 99
98 100
99 101
100 102
101 103
102 104
103 105
104 106
105 107
106 108
107 109
108 110
109 111
110 112
111 113
112 114
113 115
114 116
115 117
116 118
117 119
118 120
119 121
120 122
121 123
122 124
123 125
124 126
125 127
126 128
127 129
128 130
129 131
130 132
131 133
132 134
133 135
134 136
135 137
136 138
137 139
138 140
139 141
140 142
141 143
142 144
143 145
144 146
145 147
146 148
147 149
148 150
149 151
150 152
151 153
152 154
153 155
154 156
155 157
156 158
157 159
158 160
159 161
160 162
161 163
162 164
163 165
164 166
165 167
166 168
167 169
168 170
169 171
170 172
171 173
172 174
173 175
174 176
175 177
176 178
177 179
178 180
179 181
180 182
181 183
182 184
183 185
184 186
185 187
186 188
187 189
188 190
189 191
190 192
191 193
192 194
193 195
194 196
195 197
196 198
197 199
198 200
199 201
200 202
201 203
202 204
203 205
204 206
205 207
206 208
207 209
208 210
209 211
210 212
211 213
212 214
213 215
214 216
215 217
216 218
217 219
218 220
219 221
220 222
221 223
222 224
223 225
224 226
225 227
226 228
227 229
228 230
229 231
230 232
231 233
232 234
233 235
234 236
235 237
236 238
237 239
238 240
239 241
240 242
241 243
242 244
243 245
244 246
245 247
246 248
247 249
248 250
249 251
250 252
251 253
252 254
253 255
254 256
255 257
256 258
257 259
258 260
259 261
260 262
261 263
262 264
263 265
264 266
265 267
266 268
267 269
268 270
269 271
270 272
271 273
272 274
273 275
274 276
275 277
276 278
277 279
278 280
279 281
280 282
281 283
282 284
283 285
284 286
285 287
286 288
287 289
288 290
289 291
290 292
291 293
292 294
293 295
294 296
295 297
296 298
297 299
298 300
299 301
300 302
301 303
302 304
303 305
304 306
305 307
306 308
307 309
308 310
309 311
310 312
311 313
312 314
313 315
314 316
315 317
316 318
317 319
318 320
319 321
320 322
321 323
322 324
323 325
324 326
325 327
326 328
327 329
328 330
329 331
330 332
331 333
332 334
333 335
334 336
335 337
336 338
337 339
338 340
339 341
340 342
341 343
342 344
343 345
344 346
345 347
346 348
347 349
348 350
349 351
350 352
351 353
352 354
353 355
354 356
355 357
356 358
357 359
358 360
359 361
360 362
361 363
362 364
363 365
364 366
365 367
366 368
367 369
368 370
369 371
370 372
371 373
372 374
373 375
374 376
375 377
376 378
377 379
378 380
379 381
380 382
381 383
382 384
383 385
384 386
385 387
386 388
387 389
388 390
389 391
390 392
391 393
392 394
393 395
394 396
395 397
396 398
397 399
398 400
399 401
400 402
401 403
402 404
403 405
404 406
405 407
406 408
407 409
408 410
409 411
410 412
411 413
412 414
413 415
414 416
415 417
416 418
417 419
418 420
419 421
420 422
421 423
422 424
423 425
424 426
425 427
426 428
427 429
428 430
429 431
430 432
431 433
432 434
433 435
434 436
435 437
436 438
437 439
438 440
439 441
440 442
441 443
442 444
443 445
444 446
445 447
446 448
447 449
448 450
449 451
450 452
451 453
452 454
453 455
454 456
455 457
456 458
457 459
458 460
459 461
460 462
461 463
462 464
463 465
464 466
465 467
466 468
467 469
468 470
469 471
470 472
471 473
472 474
473 475
474 476
475 477
476 478
477 479
478 480
479 481
480 482
481 483
482 484
483 485
484 486
485 487
486 488
487 489
488 490
489 491
490 492
491 493
492 494
493 495
494 496
495 497
496 498
497 499
498 500
499 501
500 502
501 503
502 504
503 505
504 506
505 507
506 508
507 509
508 510
509 511
510 512
511 513
512 514
513 515
514 516
515 517
516 518
517 519
518 520
519 521
520 522
521 523
522 524
523 525
524 526
525 527
526 528
527 529
528 530
529 531
530 532
531 533
532 534
533 535
534 536
535 537
536 538
537 539
538 540
539 541
540 542
541 543
542 544
543 545
544 546
545 547
546 548
547 549
548 550
549 551
550 552
551 553
552 554
553 555
554 556
555 557
556 558
557 559
558 560
559 561
560 562
561 563
562 564
563 565
564 566
565 567
566 568
567 569
568 570
569 571
570 572
571 573
572 574
573 575
574 576
575 577
576 578
577 579
578 580
579 581
580 582
581 583
582 584
583 585
584 586
585 587
586 588
587 589
588 590
589 591
590 592
591 593
592 594
593 595
594 596
595 597
596 598
597 599
598 600
599 601
600 602
601 603
602 604
603 605
604 606
605 607
606 608
607 609
608 610
609 611
610 612
611 613
612 614
613 615
614 616
615 617
616 618
617 619
618 620
619 621
620 622
621 623
622 624
623 625
624 626
625 627
626 628
627 629
628 630
629 631
630 632
631 633
632 634
633 635
634 636
635 637
636 638
637 639
638 640
639 641
640 642
641 643
642 644
643 645
644 646
645 647
646 648
647 649
648 650
649 651
650 652
651 653
652 654
653 655
654 656
655 657
656 658
657 659
658 660
659 661
660 662
661 663
662 664
663 665
664 666
665 667
666 668
667 669
668 670
669 671
670 672
671 673
672 674
673 675
674 676
675 677
676 678
677 679
678 680
679 681
680 682
681 683
682 684
683 685
684 686
685 687
686 688
687 689
688 690
689 691
690 692
691 693
692 694
693 695
694 696
695 697
696 698
697 699
698 700
699 701
700 702
701 703
702 704
703 705
704 706
705 707
706 708
707 709
708 710
709 711
710 712
711 713
712 714
713 715
714 716
715 717
716 718
717 719
718 720
719 721
720 722
721 723
722 724
723 725
724 726
725 727
726 728
727 729
728 730
729 731
730 732
731 733
732 734
733 735
734 736
735 737
736 738
737 739
738 740
739 741
740 742
741 743
742 744
743 745
744 746
745 747
746 748
747 749
748 750
749 751
750 752
751 753
752 754
753 755
754 756
755 757
756 758
757 759
758 760
759 761
760 762
761 763
762 764
763 765
764 766
765 767
766 768
767 769
768 770
769 771
770 772
771 773
772 774
773 775
774 776
775 777
776 778
777 779
778 780
779 781
780 782
781 783
782 784
783 785
784 786
785 787
786 788
787 789
788 790
789 791
790 792
791 793
792 794
793 795
794 796
795 797
796 798
797 799
798 800
799 801
800 802
801 803
802 804
803 805
804 806
805 807
806 808
807 809
808 810
809 811
810 812
811 813
812 814
813 815
814 816
815 817
816 818
817 819
818 820
819 821
820 822
821 823
822 824
823 825
824 826
825 827
826 828
827 829
828 830
829 831
830 832
831 833
832 834
833 835
834 836
835 837
836 838
837 839
838 840
839 841
840 842
841 843
842 844
843 845
844 846
845 847
846 848
847 849
848 850
849 851
850 852
851 853
852 854
853 855
854 856
855 857
856 858
857 859
858 860
859 861
860 862
861 863
862 864
863 865
864 866
865 867
866 868
867 869
868 870
869 871
870 872
871 873
872 874
873 875
874 876
875 877
876 878
877 879
878 880
879 881
880 882
881 883
882 884
883 885
884 886
885 887
886 888
887 889
888 890
889 891
890 892
891 893
892 894
893 895
894 896
895 897
896 898
897 899
898 900
899 901
900 902
901 903
902 904
903 905
904 906
905 907
906 908
907 909
908 910
909 911
910 912
911 913
912 914
913 915
914 916
915 917
916 918
917 919
918 920
919 921
920 922
921 923
922 924
923 925
924 926
925 927
926 928
927 929
928 930
929 931
930 932
931 933
932 934
933 935
934 936
935 937
936 938
937 939
938 940
939 941
940 942
941 943
942 944
943 945
944 946
945 947
946 948
947 949
948 950
949 951
950 952
951 953
952 954
953 955
954 956
955 957
956 958
957 959
958 960
959 961
960 962
961 963
962 964
963 965
964 966
965 967
966 968
967 969
968 970
969 971
970 972
971 973
972 974
973 975
974 976
975 977
976 978
977 979
978 980
979 981
980 982
981 983
982 984
983 985
984 986
985 987
986 988
987 989
988 990
989 991
990 992
991 993
992 994
993 995
994 996
995 997
996 998
997 999
998 1000
999 1001
1000 1002
1001 1003
1002 1004
1003 1005
1004 1006
1005 1007
1006 1008
1007 1009
1008 1010
1009 1011
1010 1012
1011 1013
1012 1014
1013 1015
1014 1016
1015 1017
1016 1018
1017 1019
1018 1020
1019 1021
1020 1022
1021 1023
1022 1024
1023 1025
1024 1026
1025 1027
1026 1028
1027 1029
1028 1030
1029 1031
1030 1032
1031 1033
1032 1034
1033 1035
1034 1036
1035 1037
1036 1038
1037 1039
1038 1040
1039 1041
1040 1042
1041 1043
1042 1044
1043 1045
1044 1046
1045 1047
1046 1048
1047 1049
1048 1050
1049 1051
1050 1052
1051 1053
1052 1054
1053 1055
1054 1056
1055 1057
1056 1058
1057 1059
1058 1060
1059 1061
1060 1062
1061 1063
1062 1064
1063 1065
1064 1066
1065 1067
1066 1068
1067 1069
1068 1070
1069 1071
1070 1072
1071 1073
1072 1074
1073 1075
1074 1076
1075 1077
1076 1078
1077 1079
1078 1080
1079 1081
1080 1082
1081 1083
1082 1084
1083 1085
1084 1086
1085 1087
1086 1088
1087 1089
1088 1090
1089 1091
1090 1092
1091 1093
1092 1094
1093 1095
1094 1096
1095 1097
1096 1098
1097 1099
1098 1100
1099 1101
1100 1102
1101 1103
1102 1104
1103 1105
1104 1106
1105 1107
1106 1108
1107 1109
1108 1110
1109 1111
1110 1112
1111 1113
1112 1114
1113 1115
1114 1116
1115 1117
1116 1118
1117 1119
1118 1120
1119 1121
1120 1122
1121 1123
1122 1124
1123 1125
1124 1126
1125 1127
1126 1128
1127 1129
1128 1130
1129 1131
1130 1132
1131 1133
1132 1134
1133 1135
1134 1136
1135 1137
1136 1138
1137 1139
1138 1140
1139 1141
1140 1142
1141 1143
1142 1144
1143 1145
1144 1146
1145 1147
1146 1148
1147 1149
1148 1150
1149 1151
1150 1152
1151 1153
1152 1154
1153 1155
1154 1156
1155 1157
1156 1158
1157 1159
1158 1160
1159 1161
1160 1162
1161 1163
1162 1164
1163 1165
1164 1166
1165 1167
1166 1168
1167 1169
1168 1170
1169 1171
1170 1172
1171 1173
1172 1174
1173 1175
1174 1176
1175 1177
1176 1178
1177 1179
1178 1180
1179 1181
1180 1182
1181 1183
1182 1184
1183 1185
1184 1186
1185 1187
1186 1188
1187 1189
1188 1190
1189 1191
1190 1192
1191 1193
1192 1194
1193 1195
1194 1196
1195 1197
1196 1198
1197 1199
1198 1200
1199 1201
1200 1202
1201 1203
1202 1204
1203 1205
1204 1206
1205 1207
1206 1208
1207 1209
1208 1210
1209 1211
1210 1212
1211 1213
1212 1214
1213 1215
1214 1216
1215 1217
1216 1218
1217 1219
1218 1220
1219 1221
1220 1222
1221 1223
1222 1224
1223 1225
1224 1226
1225 1227
1226 1228
1227 1229
1228 1230
1229 1231
1230 1232
1231 1233
1232 1234
1233 1235
1234 1236
1235 1237
1236 1238
1237 1239
1238 1240
1239 1241
1240 1242
1241 1243
1242 1244
1243 1245
1244 1246
1245 1247
1246 1248
1247 1249
1248 1250
1249 1251
1250 1252
1251 1253
1252 1254
1253 1255
1254 1256
1255 1257
1256 1258
1257 1259
1258 1260
1259 1261
1260 1262
1261 1263
1262 1264
1263 1265
1264 1266
1265 1267
1266 1268
1267 1269
1268 1270
1269 1271
1270 1272
1271 1273
1272 1274
1273 1275
1274 1276
1275 1277
1276 1278
1277 1279
1278 1280
1279 1281
1280 1282
1281 1283
1282 1284
1283 1285
1284 1286
1285 1287
1286 1288
1287 1289
1288 1290
1289 1291
1290 1292
1291 1293
1292 1294
1293 1295
1294 1296
1295 1297
1296 1298
1297 1299
1298 1300
1299 1301
1300 1302
1301 1303
1302 1304
1303 1305
1304 1306
1305 1307
1306 1308
1307 1309
1308 1310
1309 1311
1310 1312
1311 1313
1312 1314
1313 1315
1314 1316
1315 1317
1316 1318
1317 1319
1318 1320
1319 1321
1320 1
```

```

ACPAM-DIAGNOSTICS      MACRO V03.01 27-MAR-79 16:02:25 PAGE 3

1  3 001402 004567 000510          ;READ 1010
2
3  4 001405 005000          JSR R5, RESET
4  5 001410 012737 010401 172416 CLR R0
5  6 001416 012737 001400 172416 MOV #4353., Q#OPREG
6  7 001424 105737 172414 172416 MOV #768., Q#OPREG
7  8 001430 100375          TSTB Q#CSR
8  9 001432 105037 172414          BPL -4
9 10 001436 013703 172416          CLRB Q#CSR
10 11 001442 042703 172416          MOV Q#OPREG, R3
11 12 001446 022703 000012          BIC #177760, R3
12 13 001452 001005          CMF #12, R3
13 14 001454 005200          BNE E3$
14 15 001456 022700          INC R0
15 16 001462 001355          CMP #256., R0
16 17 001464 000401          BNE R3$
17 18 001466 000000          BR TEST4
19
20
21
22 22 001470 004567 000422          ;WRITE 0101
23 23 001474 012700 000400          JSR R5, RESET
24 24 001500 012737 010401 172416 MOV #256., R0
25 25 001506 012737 004405 172416 MOV #4353., Q#OPREG
26 26 001514 105737 172414          MOV #4405, Q#OPREG
27 27 001520 100375          TSTB Q#CSR
28 28 001522 105037 172414          BPL -4
29 29 001526 077011          CLRB Q#CSR
30
31
32
33 33 001530 004567 000362          ;READ 0101
34 34 001534 005000          JSR R5, RESET
35 35 001536 012737 010401 172416 CLR R0
36 36 001544 012737 001400 172416 MOV #4353., Q#OPREG
37 37 001552 105737 172414          MOV #768., Q#OPREG
38 38 001556 100375 172414          TSTB Q#CSR
39 39 001560 105037 172414          BPL -4
40 40 001564 013703 172416          CLRB Q#CSR
41 41 001570 042703 172416          MOV Q#OPREG, R3
42 42 001574 022703 000005          BIC #177760, R3
43 43 001600 001005          CMP #5, R3
44 44 001602 005200          BNE E4$
45 45 001604 022700          INC R0
46 46 001610 001355          CMP #256., R0
47 47 001612 000401          BNE R4$
48 48 001614 000000          BR TEST5
49

```

```

;ADDRESS=0
;SEL ACRAM
;RD COMMAND
;VALID R1
;NO LOOP UNTIL
;CLEAR DONE
;FETCH DATA
;CLEAR UNUSED BITS
;DATA=1010
;NO
;256 TIMES
;ERROR IN DATA

;ADDRESS=0
;COMMAND SEL ACRAM
;WD MASK=0101
;VALID WD
;NO LOOP UNTIL
;CLEAR DONE
;LOOP

;RESET POINTER
;SEL ACRAM
;RD
;VALID RD
;NO LOOP UNTIL
;CLEAR DONE
;FETCH DATA
;CLEAR UNUSED BITS
;DATA=0101
;NO
;256 TIMES
;ERROR IN DATA

```

```

1
2
3 001616 004567 002074 172416 TEST5: JSR R5, RESET
4 001622 012737 010401 172416 MOV #4353., @OPREG
5 001630 012705 000400 MOV #256., R5
6 001634 012737 176543 002110 MOV #176543, @HINUM
7 001642 012737 123456 002112 MOV #123456, @LONUM
8
9 001650 004767 000142 W5$: JSR PC, GENRAN
10 001654 013704 002114 MOV @HINUM, R4
11 001660 062704 004400 ADD #4400, R4
12 001664 010437 172416 MOV R4, @OPREG
13 001670 105737 172414 TSTB @CSR
14 001674 100375 172414 BPL .-4
15 001676 105037 172414 CLRB @CSR
16 001702 077516 SOB R5, W5$
17
18 001704 004567 000206 JSR R5, RESET
19 001710 012737 010401 MOV #4353., @OPREG
20 001716 005005 CLR R5
21 001720 012737 176543 002110 MOV #176543, @HINUM
22 001726 012737 123456 002112 MOV #123456, @LONUM
23
24 001734 004767 000056 R5$: JSR PC, GENRAN
25 001740 013704 002114 MOV @HINUM, R4
26 001744 012737 001400 MOV #768., @OPREG
27 001752 105737 172414 TSTB @CSR
28 001756 100375 172414 BPL .-4
29 001760 105037 172414 CLRB @CSR
30 001764 013703 172416 MOV @OPREG, R3
31 001770 042703 177760 BIC #177760, R3
32 001774 020304 CMP R3, R4
33 001776 001006 BNE E5$
34 002000 005205 INC R5
35 002002 022705 CMP #256., R5
36 002006 001352 BNE R5$
37 002010 000167 JMP TEST1
38 002014 000000 E5$: HALT
39

```



```

1  3 002016 013700 002112
2  4 002022 013701 002110
3  5 002026 012702 177771
4  6 002032 006300
5  7 002034 006101
6  8 002036 005202
7  9 002040 001374
8 10 002042 063700 002112
9 11 002046 005501
10 12 002050 063701 002110
11 13 002054 062700 001057
12 14 002060 005501
13 15 002062 062701 047401
14 16 002066 010037 002112
15 17 002072 010137 002110
16 18 002076 042701 177760
17 19 002102 010137 002114
18 20 002106 000207
19 21
20 22 002110 176543
21 23 002112 123456
22 24 002114
23 25
24 26
25 27
26 28
27 29
28 30 002116 012737 010402 172416
29 31 002124 012737 004400 172416
30 32 002132 105737 172414
31 33 002136 100375
32 34 002140 105037 172414
33 35 002144 000205
34 36
35 37 002146
36 38
37 39 001000
38
39

;*****RANDOM NUMBER GENERATOR
GENRAN: MOV G#LONUM, R0
MOV G#HINUM, R1
MOV #7, R2
GR: ASL R0
ROL R1
INC R2
BNE GR
G#LONUM, R0
ADD G#LONUM, R0
ADC R1
ADD G#HINUM, R1
ADD #1057, R0
ADC R1
ADD #47401, R1
MOV R0, G#LONUM
MOV R1, G#HINUM
BIC #177760, R1
MOV R1, G#RANUM
RTS PC
HINUM: .WORD 176543
LONUM: .WORD 123456
RANUM: .BLKW

;*****
;RESET ACRAM POINTER
RESET: MOV #4354, G#OPREG
MOV #2304, G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
RTS R5
MSTART: .BLKW
.END CLEAR

```

```

;SEED 1
;SEED 2
;SHIFT COUNT
;SHIFT R0 LEFT AND
;ROTATE CARRY INTO R1
;INC AND CHECK FOR 0
;ADD NUMBER TO MAKE 129
;PROPOGATE CARRY
;ADD NUMBER TO 129
;ADD LOW CONSTANT
;PROPOGATE CARRY
;ADD HIGH CONSTANT
;SAVE R0
;SAVE R1
;CLEAR HIGH BITS FOR AR
;NUMBER GENERATED
;RETURN

```

```

;LOAD ADDRESS
;WD 0
;VALID WD
;NO LOOP UNTIL
;CLEAR DONE

```


ACRAM-DIAGNOSTICS MACRO V03.01 27-MAR-79 18:02:25 PAGE 5-1

SYMBOL TABLE

CLEAR	001000	E5\$	002014	RANUM	002114	R5\$	001734	TIME	0010E4
CLR\$	001020	GENRAN	002016	RESET	002116	TEST1	001066	W1\$	001104
CSR	= 172414	GR	002032	R1\$	001142	TEST2	001214	W2\$	001232
E1\$	001212	HINUM	002110	R2\$	001270	TEST3	001342	W3\$	001360
E2\$	001340	LONUM	002112	R3\$	001416	TEST4	001470	W4\$	001506
E3\$	001466	MSTART	002146	R4\$	001544	TEST5	001616	W5\$	001650
E4\$	001614	OPREG	= 172416						

. ABS. 002150 000
 000000 001
 ERRORS DETECTED: 1

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
 DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
 .DK:LIURAM=DK:LIURAM

3.1.5 LIU BUFFER TEST

Source File: LIUBUF.MAC

Task File: LIUBUF.SAV

PURPOSE: To test the input and output buffers in the LIU

DESCRIPTION: This program initializes the buffers and then writes and reads various patterns from the LIU buffers. The patterns used are all ones, all zeros, alternating ones and zeros and random numbers. If the data read from the LIU buffer does not match the data written to it, the program stores an indicator of which buffer is in error and halts, displaying the current program address. Otherwise, the program loops, repeating the test indefinitely.

BUFFER-DIAGNOSTICS MACRO V03.01 27-MAR-79 16:01:51 PAGE 1-1


```

1
2
3
4
5 001426 013737 002022 172416 TEST2: MOV Q#SELBUF,Q#OPREG
6 001434 013700 002026 Q#MASK, R0
7 001440 042700 177400 BIC #177400,R0
8 001444 010001 MOV R0, R1
9 001446 012702 177400 MOV #-256., R2
10 001452 062700 004400 ADD #4400, R0
11 001456 010037 172416 MOV R0, Q#OPREG
12 001462 105737 172414 TSTB Q#CSR
13 001466 100375 BPL -4
14 001470 105037 CLR B Q#CSR
15 001474 005202 INC R2
16 001476 001367 BNE W2$
17
18 001500 013737 002022 172416 Q#SELBUF,Q#OPREG
19 001506 012700 MOV #-256., R0
20 001512 012737 001400 MOV #768., Q#OPREG
21 001520 105737 172414 TSTB Q#CSR
22 001524 100375 BPL -4
23 001526 105037 CLR B Q#CSR
24 001532 013702 MOV Q#OPREG, R2
25 001536 042702 BIC #177400, R2
26 001542 120102 CMPB R1, R2
27 001544 001003 BNE E2$
28 001546 005200 INC R0
29 001550 001360 BNE R2$
30 001552 000207 RTS PC
31 001554 000000 E2$: HALT
32
33
34

```

BUFFER-DIAGNOSTICS

MACRO V03.01 27-MAR-79 18:01:51 PAGE 4

```

1
2
3
4 001556 004767 000134 PC, GENRAN
5 001562 012704 002034 #RANUM, R4
6 001566 012700 000400 #256., R0
7 001572 013737 002022 172416 #SELBUF, @OPREG
8 001600 012403 002022 (R4)+, R3
9 001602 062703 004400 R3
10 001606 013337 172416 #4400, R3
11 001612 105737 172414 @OPREG
12 001616 100375 BPL -4
13 001620 105037 172414 @CSR
14 001624 077013 SUB W3$
15 001626 013737 002022 R0,
16 001634 012737 001400 @SELBUF, @OPREG
17 001642 105737 172414 #1400, @OPREG
18 001646 100375 TSTB @CSR
19 001650 105037 BPL -4
20 001654 012700 CLRB @CSR
21 001660 012737 172414 #256., R0
22 001666 105737 172414 #1400, @OPREG
23 001672 100375 TSTB @CSR
24 001674 013703 BPL -4
25 001700 042703 MOV @OPREG, R3
26 001704 024403 BIC #177400, R3
27 001706 001002 CMP -(R4), R3
28 001710 077015 BNE E3$
29 001712 000207 SOB R0,
30 001714 000000 RTS PC
31
32
33
34 001716 012703 002034 #RANUM, R3
35 001722 012704 177400 #256., R4
36 001726 013700 002030 @LONUM, R0
37 001732 013701 002032 @HINUM, R1
38 001736 012702 177771 #7, R2
39 001742 006300 MOV #7, R2
40 001744 006101 ASL R0
41 001746 005202 ROL R1
42 001750 001374 INC R2
43 001752 063700 BNE GR
44 001756 005501 @LONUM, R0
45 001760 063701 @HINUM, R1
46 001764 062700 ADD #1057, R0
47 001770 005501 R1
48 001772 062701 ADD #47401, R1
49 001776 010037 MOV R0, @LONUM
50 002002 016137 MOV R1, @HINUM
51 002006 042701 BIC #177400, R1
52 002012 016123 MOV R1, (R3)+
53 002014 005204 INC R4
54 002016 001343 BNE LP
55 002020 000207 RTS PC
56

```

```

;SEGMENT READS AND WRITES RANDOM NUMBERS
;INTO SELECTED BUFFER HALTS ON ERROR.

```

```

;GENERATE # BLOCH
;ADDRESS OF BLOCK
;NUMBER OF BYTES
;SEL BUFFER
;RANDOM NUMBER
;WD COMMAND
;WD
;GOOD WD
;LOOP UNTIL
;CLEAR
;LOOP UNTIL 0
;SEL BUFFER
;FALSE RD
;OK
;LOOP UNTIL
;COUNT
;RD
;READY
;LOOP UNTIL
;FETCH BYTE
;CLEAR MST
;DATA = DATA
;NO ERROR
;YES CONTINUE
;RETURN
;ERROR IN TEST3

```

```

;DATA BLOCK
;GENERATE BY 256
;SEED 1
;SEED 2
;COUNTER
;SHIFT
;ROTATE
;INC AND CHECK
;MAKE IT X129
;PROP CARRY
;MAKE IT X129
;ADD CONSTANT
;PROP CARRY
;ADD CONSTANT
;SAVE
;SAVE
;CLEAR UNUSED BITS
;STORE NUMBER
;COUNT +1
;<>0 CONTINUE

```

```

;*****RANDOM NUMBER GENERATOR

```

```

GENRAN: MOV #RANUM, R3
LP: MOV #256., R4
MOV @LONUM, R0
MOV @HINUM, R1
MOV #7, R2
GR: ASL R0
ROL R1
INC R2
BNE GR
ADD @LONUM, R0
ADC R1
ADD @HINUM, R1
ADD #1057, R0
ADC R1
ADD #47401, R1
MOV R0, @LONUM
MOV R1, @HINUM
BIC #177400, R1
MOV R1, (R3)+
INC R4
BNE LP
RTS PC

```

```

;*****

```

AD-A078 392

BURROUGHS CORP PAOLI PA FEDERAL AND SPECIAL SYSTEMS GROUP F/G 9/2
SOFTWARE MAINTENANCE MANUAL FOR THE MODULAR SYSTEM CONTROL DEVE--ETC(U)
NOV 79 DCA100-76-C-0083

UNCLASSIFIED

66158

SBIE-AD-E100 314

NL

2 of 4
ADA
078392



EUFFER-LIAGNOSTICS

1 2 3 4 5 6 7 8 9 10 11 12

BUFFER-DIAGNOSTICS MACRO V03.01 27-MAR-79 16:01:51 PAGE 5-1
 SYMBOL TABLE

BPPCMD	002024	E1\$	001424	LONUM	002030	R2\$	001512	TEST2	001426
CKBP	001356	E2\$	001554	LP	001726	R3\$	001660	TEST3	001556
CLEAR	001000	E3\$	001714	MASK	002026	SELBUF	002022	TIME	001064
CLR\$	001020	GENRAN	001716	OP=EC	172416	START	001066	W2\$	001456
CSR	= 172414	GR	001742	RANUM	002034	TEST1	001252	W3\$	001600
C1\$	001324	HINUM	002032	RUN	001170				
<p>. ABS. 003034 000 000000 001 ERRORS DETECTED: 1</p>									

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
 DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
 ,DK:LIUBUF=DK:LIUBUF

3.1.6 LSI-11 REAL TIME CLOCK TEST

Source File: LSICLK.MAC

Task File: LSICLK.SAV

PURPOSE: To test LSI-11 RTC

DESCRIPTION: This program waits for interrupts from the real time clock and increments a counter when one is received. When the program does not detect an interrupt in a given time, it halts, detecting an error, and displays current program address.

```

1
2
3 000000 001000
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22

```

```

.TITLE LSI-11 RTC DIAG.
.IDENT /V1.0/
.ASECT 1000
.=

```

```

*****
;* DIAGNOSTIC CHECKS (BEVENT L) CLOCK SIGNAL *
;* AT 16.834 USEC USED AS THE RTC GENERATOR. *
;* VECTORS TO LOCATION 100 FOR ADDRESS OF *
;* HANDLER *
;* NOTE: PROGRAM HALTS AT (ETO:) IF AN *
;* INTERRUPT FAILS TO BE RECEIVED. *
*****

```

```

;CLEAR INTERRUPT AND VECTOR AREA
;SET LOCATION 100 TO TIMER

```

```

CLEAR: RESET
MOV #340, R0
MTPS R0
MOV #500, SP
MOV #200, R2
CLR -(R2)
MOV R2, -(R2)
TST R2
BNE CLR$
MOV #CLEAR, Q#40
MOV #100, R0
MOV #TIMER, (R0)
MOV #000, R1
MTPS R1

000005
23 001000 012700 000340
24 001002 012700
25 001006 106400
26 001010 012700 000500
27 001014 012700 000200
28 001020 005042
29 001022 010242
30 001024 005702
31 001026 001374
32 001030 012737
33 001036 012700
34 001042 012710
35 001046 012701
36 001052 106401
37
38
39
40

```

```

;RESET BUS
;PRI=7
;INITIALIZE STACK
;R2=TOP OF VECTOR AREA
;INSERT HALF INSTR.
;POINT TO HALT
;LOC 0 YET
;NO CONTINUE
;RESTART ADDRESS
;VECTOR
;ADDRESS OF HANDLER
;CPU PRIORITY 2
;SET CPU PRI.

```

```

;TIMEOUT LOOP: COUNTS UNTIL INTERRUPT
;IS RECEIVED, HALTS IF TIME-OUT COUNT
;REACHES LIMIT

```

```

TIMEOUT: CLR R0
CLR R1
INCT: INC R0
TST R0
BPL INCT
CCC
INC R1
CMP Q#LIMIT, R1
BEQ ETO
CLR R0
BR INCT
ETO: HALT

```

```

;CLEAR COUNT
;CLEAR LIMIT
;COUNT+1
;OVER-FLOW YET?
;NO CONTINUE
;CLEAR COND. CODES
;COUNT+1
;LIMIT YET
;YES HALT
;CLEAR COUNTER
;CONTINUE COUNTER
;ERROR TIME-OUT

```


LSI-11 RTC DIAG.

MACRO V03.02B2-NOV-79 10:45:29 PAGE 2

```

1
2
3 001106 005237 001162 001162 001164
4 001112 023737 001162 001162 001164
5 001120 001001
6 001122 000000
7 001124 013737 001162 001164 001162
8 001132 022737 177777 001162
9 001140 001004
10 001142 005037 001162
11 001146 005037 001164
12 001152 005000
13 001154 005001
14 001156 000002
15
16
17
18 001160 000010
19 001162 000000
20 001164 000000
21
22
23 001000

; INTERRUPT HANDLER FOR RTC
; TIME+1
; DID CLOCK ADVANCE
; YES
; NO-ERROR IN CLOCK
; RESET OLDTIM
; TIME LIMIT
; NO RTI
; YES CLEAR TIME
; CLEAR OLDTIM
; CLEAR COUNT
; RETURN FROM INTERRUPT

TIMER: INC Q#TIME
CMP Q#TIME, Q#OLDTIM
BNE T2$
HALT
MOV Q#TIME, Q#OLDTIM
CMP #177777, Q#TIME
BNE T3$
CLR Q#TIME
CLR Q#OLDTIM
CLR R0
CLR R1
RTI

; DATA AREA
LIMIT: .WORD 10
TIME: .WORD 0
OLDTIM: .WORD 0

.END CLEAR

```

LSI-11 RTC DIAG.
SYMBOL TABLE

MACRO V03.02B2-NOV-79 10:45:29 PAGE 2-1

CLEAR	001000
CLR\$	001020
ETO	001104

INCT	001060
LIMIT	001160

OLDTIM	001164
TIME	001162

TIMER	001106
TIMOUT	001054

T2\$	001124
T3\$	001152

```

. ABS. 001166 000
000000 001
ERRORS DETECTED: 1

```

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 56 PAGES
DK:LSICKL,DK:LSICKL=DK:LSICKL

3.1.7 MSCDM DIAGNOSTIC OPERATING INSTRUCTIONS

3.1.7.1 RUNNING DIAGNOSTICS ON PDU NODE

1. Insert diskette #1 into DX0:
2. Insert diagnostic diskette #16 into DX1:
3. Type .GET fid where fid=file name
4. Press BREAK KEY
5. Insure that the RTC is off
6. TYPE @1000G
7. Diagnostic will then loop until an error is detected whereby the current program address (PC): +2 will be displayed on the CRT. By looking at the source code for that diagnostic the address (PC): will indicate the error that was detected, and the user registers will hold the cause of the error.

3.1.7.2 RUNNING DIAGNOSTICS ON A LOOP NODE

1. Insert diskette #1 into DX0:
2. Insert diagnostic diskette #16 into DX1:
3. Turn the RTC off on node(s): under test
4. Run FDMLDR to load and start the diagnostic
5. Insure the RUN LED on the front panel for that node stays on (diagnostic running). Should the LED extinguish, an error was detected and the software has halted.

6. By attaching a CRT MAINTENANCE CABLE to the node and restarting program at address 1000 (@ 1000G); using the CRT the diagnostic will again begin to run and when the error develops again the (PC); will be displayed on the CRT. By looking at the source code for that diagnostic the address (PC); will indicate the error that was detected, and the user registers hold the cause of the error.

Note: Not all nodes contain a DLVII interface card that can support the CRT. Cards may have to be swapped with another node.

3.1.7.3 RUNNING DIAGNOSTICS ON SIG

SIG takes a special type of software which is in the .LDA format and when loaded it STARTS automatically. Also note that the SIGN loads via the asynchronous interface from node 26. The procedure other than that is the same for any other node on loop.

3.1.7.4 SEQUENCE OF DIAGNOSTICS

By running diagnostics in a certain sequence, the hardware error can be isolated down to a specific card. The recommended sequence is given below:

1. RUN LSICPU - CPU diagnostic
2. RUN LSIMEM - MEMORY diagnostic
3. RUN LIUINT - INTERFACE diagnostic
4. RUN LIURAM - ACRAM diagnostic
5. RUN LIUBUF - BUFFER diagnostic

3.2 IEEE PROGRAMS

	PAGE
Description	
3.2.1 IEEE RECEIVE PROGRAM	100
3.2.2 IEEE SEND PROGRAM	109

3.2.1 IEEE Receive Program Description (IEEEER)

The program initializes the LIU and then loops, waiting for packets from the loop. When a packet is received it is ACKed and printed on the LA36.

IEEE RECEIVE MACRO V03.01 27-MAR-79 19:50:30 PAGE 1

```

1      .TITLE IEEE RECEIVE
2      .IDENT /V1.0/
3      .ASECT 1000
4      .=
5      BAR= 172410
6      WCR= 172412
7      CSR= 172414
8      OPRG= 172416
9      RCSR= 177560
10     RBUF= 177562
11     XCSR= 177564
12     XBUF= 177566
13
14
15     BIT15= 100000
16     BIT14= 40000
17     BIT08= 400
18     BIT07= 200
19     BIT06= 100
20     BIT05= 40
21     BIT04= 20
22     BIT03= 10
23     BIT02= 4
24     BIT01= 2
25     BIT00= 1
26
27
28     ;CLEAR INTERRUPT AND TRAP VECTOR AREA
29
30     CLEAR: RESET
31     MOV #340, R0
32     MTPS R0, SP
33     MOV #500, R2
34     MOV #200, R2
35     CLR -(R2)
36     TST R2
37     BNE CLR$
38     ;CLEAR, C#40
39     MOV #40100, G#CSR
40     MOV #124, R0
41     MOV #124, (R0)+
42     MOV #340, (R0)
43     MOV #102, R0
44     MOV #2, (R0)
45
46     000005 000340
47     000100 012700
48     000102 106400
49     000106 012706
50     000110 012706
51     000114 012702
52     000120 005042
53     000122 010242
54     000124 005702
55     000126 001374
56     000130 012737
57     000136 012737
58     000144 012700
59     000150 012720
60     000154 012710
61     000160 012700
62     000164 012710
63
64     000040 000040
65     000100 040100
66     000124 000124
67     000126 001720
68     000130 000340
69     000136 000102
70     000144 000002
71
72     ;RESET BUS
73     ;CPU PRI=7
74
75     ;INIT STACK POINTER
76     ;R2=TOP OF VECTOR AREA
77     ;INSERT A HALT
78     ;POINT TO HALT
79     ;DONE ALL VECTORS
80     ;NO CONTINUE
81     ;RESTART ADDRESS
82     ;DISABLE INTERRUPTS(LIU)
83     ;LIU VECTOR ADDRESS
84     ;POINT TO HANDLER
85     ;INTER. AT PRI=340
86     ;CLOCK VECTOR
87     ;INSERT A RTI

```

[illegible]

IEEE RECEIVE MACRO V03.01 27-MAR-79 19:50:30 PAGE 2-1

```

58 001412 012737 010510 002534 0TBUF0: MOV      #10510, Q#BUF0CMD
59 001420 012737 010540 002532 0TBUF0: MOV      #10540, Q#SELBUF
60 001426 004767 000022 000000 JSR      PC, ZEROBP
61 001432 012737 010710 002534 0TBUF1: MOV      #10710, Q#BUF0CMD
62 001440 012737 010740 002532 0TBUF1: MOV      #10740, Q#SELBUF
63 001446 004767 000002 000000 JSR      PC, ZEROBP
64 001452 000466 000000 BR      STAT
65
;RDBUFADR COMMAND
;SEL OUTBUF0
;POINTER=0
;RDBUFADR COMMAND
;SEL OUTBUF1 COMMAND
;RESET COMPLETE

```

IEEE RECEIVE MACRO V03.01 27-MAR-79 19:50:30 PAGE 3

```

1 001454 013700 002532      ZEROBP: MOV      G#SELBUF, R0
2 001460 013701 002534      MOV      G#BUFCTL, R1
3 001464 010137 172416      MOV      R1, G#OPREG
4 001470 012737 001400 172416      MOV      #1400, G#OPREG
5 001476 105737 172414      TSTB     G#CSR
6 001502 100375      BPL      -4
7 001504 105037 172414      CLR      G#CSR
8 001510 013702 172416      MOV      G#OPREG, R2
9 001514 042702 177400      BIC      #177400, R2
10 001520 010037 172416      MOV      R0, G#OPREG
11 001524 022702 000000      CMP      R0, R2
12 001530 001412      BEQ      RECK
13 001532 012737 001400 172416      MOV      #1400, G#OPREG
14 001540 105737 172414      TSTB     G#CSR
15 001544 100375      BPL      -4
16 001546 105037 172414      CLR      G#CSR
17 001552 005302      DEC      R2
18 001554 000763      BR       CMP
19 001556 010137 172416      MOV      R1, G#OPREG
20 001562 012737 001400 172416      MOV      #1400, G#OPREG
21 001570 105737 172414      TSTB     G#CSR
22 001574 100375      BPL      -4
23 001576 105037 172414      CLR      G#CSR
24 001602 013702 172416      MOV      G#OPREG, R2
25 001606 042702 177400      BIC      #177400, R2
26 001612 022702 000000      CMP      R0, R2
27 001616 001401      BEQ      RTNB
28 001620 000000      HALT
29 001622 010037 172416      MOV      R0, G#OPREG
30 001626 000207      RTS      PC
31
32
33      ;READ AND CLEAR STATUS
34 001630 012737 001400 172416      MOV      #4352, G#OPREG
35 001636 012737 002400 172416      MOV      #1280, G#OPREG
36 001644 012737 002400 172416      MOV      #1280, G#OPREG
37 001652 013700 172416      MOV      G#OPREG, R0
38 001656 042700 177400      BIC      #177400, R0
39 001662 022700 000000      CMP      R0, R0
40 001666 001003      BNE      ESTAT
41 001670 005037 172414      CLR      G#CSR
42 001674 000401      BR       LSLP
43 001676 000000      ESTAT:  HALT
44
45
;SEL BUFFER COMMAND
;RDBUFADR COMMAND
;RD
;GOOD RD
;NO LOOP UNTIL

;FETCH POINTER
;CLEAR MST BYTE
;SEL BUFFER
;POINTER=0?
;YES RECHECK
;NO FALSE RD
;GOOD RD
;NO LOOP UNTIL

;LOCAL POINTER-1
;BR UNTIL 0
;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL

;FETCH NEW POINTER
;CLEAR MST BYTE
;POINTER=0
;YES RETURN
;ERROR IN BUFFER POINTER
;SEL BUFFER
;RETURN

;WCR:RS
;RS(FALSE)
;RS
;FETCH STATUS
;CLEAR MST BYTE
;STATUS=0 ?
;NO STOP PROGRAM
;YES CLEAR BLUI CSR
;START TEST
;ERROR IN LIU STATUS

```

IEEE RECEIVE MACRO V03.01 27-MAR-79 19:50:30 PAGE 4

```

1
2
3 001700 012700 000000      LSLP:  MOV  #000, R0
4 001704 106400          MTPS  R0
5 001706 052737 040000 172414  BIS  #BIT14, Q#CSR
6 001714 000777          BR
7 001716 000000          HALT
8
9
10
11 001720 010046          LIU:    MOV  R0, -(SP)
12 001722 010146          MOV  R1, -(SP)
13 001724 010246          MOV  R2, -(SP)
14 001726 010346          MOV  R3, -(SP)
15 001730 012737 010400 172416  MOV  #4352., Q#OPREG
16 001736 012737 002400 172416  MOV  #1280., Q#OPREG
17 001744 013700 172416  MOV  Q#OPREG, R0
18 001750 042700 177400  BIC  #177400, R0
19
20
21
22 001754 132700 000004      BFOV0:  BITB  #BIT02, R0
23 001760 001401          BEQ  BFOV1
24 001762 000000          HALT
25 001764 132700 000010      BFOV1:  BITB  #BIT03, R0
26 001770 001401          BEQ  PRSW
27 001772 000000          HALT
28 001774 132700 000020      PRSW:   BITB  #BIT04, R0
29 002000 001402          BEQ  BKSX
30 002002 000167 000450      BKSX:   JMP  DONE
31 002006 132700 000040      WTLT:   BITB  #BIT05, R0
32 002012 001402          BEQ  WTLT
33 002014 000167 000436      JMP  DONE
34 002020 132700 000100      BITB  #BIT06, R0
35 002024 001401          BEQ  LATCH
36 002026 000000          HALT
37 002030 132700 000200      LATCH:  BITB  #BIT07, R0
38 002034 001402          BEQ  IE0F
39 002036 000167 000414      JMP  DONE
40

```

```

;CPU PRI=0
;ENABLE INTERRUPTS
;LOOP
;PROGRAM ERROR

;SAVE R0
;SAVE R1
;SAVE R2
;SAVE R3
;READ STATUS 1
;RS
;FETCH & CLEAR MST BYTE

;DID INBUF0 OVER-FLOW
;NO
;YES STOP PROGRAM
;DID INBUF1 OVER-FLOW
;NO
;YES STOP PROGRAM
;DID PRIMARY GO DOWN
;NO
;YES ENABLE INTERRUPTS
;DID BACKUP GO DOWN
;NO
;YES ENABLE INTERRUPTS
;DID WRT-TOKEN DETECT
;NO
;YES STOP PROGRAM
;DID LATCH GET SET
;NO
;YES ENABLE INTERRUPTS

```

```

;LOWER PRIORITY AND LOOP FOR INTERRUPT

;LIU INTERRUPT HANDLER

;FIND WHY IT INTERRUPTED

```

```

1 2 3 002042 132700 000001      ; INPUT BUFFER 0
4 002046 001501      IBUF:  BITB #BIT00, R0
5 002050 012737 172416      BEQ  IB1F
6 002056 012737 001400 172416  MOV  #4360., Q#OPREG
7 002064 105737 172414      MOV  #768., Q#OPREG
8 002070 100375      TSTB  Q#CSR
9 002072 105037 172414      BPL  -4
10 002076 013703 172416      CLRB  Q#CSR
11 002102 042703 177400      MOV  Q#OPREG, R3
12 002106 012737 010440 172416  BIC  #177400, R3
13 002114 012737 001400 172416  MOV  #4384., Q#OPREG
14 002122 105737 172414      MOV  #768., Q#OPREG
15 002126 100375      TSTB  Q#CSR
16 002130 105037 172414      BPL  -4
17 002134 005403      CLRB  Q#CSR
18 002136 010337 172412      NEG  R3
19 002142 012737 002536 172410  MOV  R3, Q#WCR
20 002150 012737 021000 172416  MOV  #DATA, Q#BAR
21 002156 000240      MOV  #8704., Q#OPREG
22 002160 105737 172414      NOP
23 002164 100375      TSTB  Q#CSR
24 002166 105037 172414      BPL  -4
25 002172 012737 004400 172416  CLRB  Q#CSR
26 002200 105737 172414      MOV  #2304., Q#OPREG
27 002204 100375      TSTB  Q#CSR
28 002206 105037 172414      BPL  -4
29 002212 004767 000264      CLRB  Q#CSR
30 002216 012737 010600 172416  PC,  PRTPK
31 002224 012737 002400 172416  MOV  #4480., Q#OPREG
32 002232 013702 172416      MOV  #1280., Q#OPREG
33 002236 042702 177400      MOV  Q#OPREG, R2
34 002242 132702 000001      DIB  #177400, R2
35 002246 001001      BITB  #BIT00, R2
36 002250 000000      BNE  IB1F
37      ECR0:  HALT

38      ; INPUT BUFFER 1
39
40 002252 132700 000002      IBUF:  BITB #BIT01, R0
41 002256 001477      BEQ  DONE
42 002260 012737 172416      MOV  #4488., Q#OPREG
43 002266 012737 001400 172416  MOV  #768., Q#OPREG
44 002274 105737 172414      TSTB  Q#CSR
45 002300 100375      BPL  -4
46 002302 105037 172414      CLRB  Q#CSR
47 002306 013703 172416      MOV  Q#OPREG, R3
48 002312 042703 177400      BIC  #177400, R3
49 002316 012737 010640 172416  MOV  #4512., Q#OPREG
50 002324 012737 001400 172416  MOV  #768., Q#OPREG
51 002332 105737 172414      TSTB  Q#CSR
52 002336 100375      BPL  -4
53 002340 105037 172414      CLRB  Q#CSR
54 002344 005403      NEG  R3
55 002346 010337 172412      MOV  R3, Q#WCR
56 002352 012737 002536 172410  MOV  #DATA, Q#BAR
57 002360 012737 021000 172416  MOV  #8704., Q#OPREG

```

```

;DID INPUT BUF 0 FILL
;NO
;YES READ POINTER
;RD
;READY
;NO LOOP UNTIL

```

```

;FETCH DATA
;CLEAR MST BYTE
;SEL INBF0
;FALSE READ
;VALID RD
;NO LOOP UNTIL

```

```

;2'S COMP. OF COUNT
;BYTE COUNT
;DMA TO LOC=5000
;FIRE DMA
;DELAY
;GOOD DMA

```

```

;FALSE WD
;VALID WD
;NO LOOP UNTIL

```

```

;PRINT IT
;READ STATUS 1
;RS
;FETCH DATA
;CLEAR MST BYTE
;CRC GOOD
;YES CONTINUE
;ERROR BAD PACKET

```

```

;DID INBUF1 FILL
;ALL DONE
;RDBUF POINTER
;RD
;READY
;NO LOOP UNTIL

```

```

;FETCH DATA
;CLEAR MST BYTE
;SEL INBUF1
;FALSE READ
;VALID RD
;NO LOOP UNTIL

```

```

;2'S COMP OF BYTE COUNT
;BYTE COUNT
;WRITE TO LOC=6000
;FIRE DMA

```



```

58 002366 000240      NOP
59 002370 105737      TSTB
60 002374 100375      BPL
61 002376 012737      MOV
62 002404 105737      TSTB
63 002410 100375      BPL
64 002412 105037      CLRB
65 002416 004767      JSR
66 002422 012737      MOV
67 002430 012737      MOV
68 002436 013702      MOV
69 002442 042702      BIC
70 002446 132702      BITB
71 002452 001001      BNE
72 002454 000000      ECRC1: HALT
73
74 002456 012603      DONE:
75 002460 012602      MOV
76 002462 012601      MOV
77 002464 012600      MOV
78 002466 005037      CLR
79 002472 052737      DIS
80 002500 000002      RTI
81
82
83 002502 012705      PRTPK:
84 002506 105737      P$:
85 002512 100375      BPL
86 002514 112537      MOV
87 002520 122715      CMPB
88 002524 001370      BNE
89 002526 000207      RTS
90
91
92
93 002530 014        ;DATA BLOCK
94 002531 000        .BYTE 014
95 002532 000        .BYTE 00
96 002534 000        .BLKW
97 002536 000        .BLKW
98 002567 000        .BLKB 25.
99 001000 000        .BLKB 0
100

```

```

;DELAY
;?GOOD DMA
;FALSE WD
;VALID WD
;NO LOOP UNTIL
;PRINT IT
;READ STATUS 1
;RS
;FETCH DATA
;CLEAR MST BYTE
;GOOD CRC
;CRC OK
;BAD CRC INBUF1
;RESTORE R3
;RESTORE R2
;RESTORE R1
;RESTORE R0
;CLEAR BLIUI STATUS
;ENABLE BLIUI

```

```

;ADDRESS OF PACKET
;PORT READY
;NO LOOP
;PRINT BYTE
;ETI YET

```

IEEE RECEIVE MACRO V03.01 27-MAR-79 19:50:30 PAGE 5-2
SYMBOL TABLE

BAR = 172410	002454	OTBUF0	001412	RTNB	001622
BFOV0 001754	001676	OTBUF1	001432	R2\$	001204
BFOV1 001764	002042	PB0\$	002172	R4\$	001264
BITS0 = 000001	002252	PB1\$	002376	SELBUF	002532
BITS1 = 000002	001312	PRSW	001774	STAT	001630
BITS2 = 000004	001352	PRTPK	002502	SWT	001070
BITS3 = 000010	002030	P\$	002506	WCR	= 172412
BITS4 = 000020	001720	RAM	001144	WTDI	= 002020
BITS5 = 000040	001700	RBUF	= 177562	XBUP	= 177566
BITS6 = 000100	002530	RCSR	= 177560	XCSR	= 177564
BITS7 = 000200	OPREG = 172416	RECK	001556	ZEROBP	001454
BITS8 = 000400					

. ABS. 002570 000

000000 001

ERRORS DETECTED: 1

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
DE:RIEE-DI:RIEE

3.2.2 IEEE Send Program Description (IEEES)

The program initializes the LIU and IEEE interface, once initialized, the program reads data from the IEEE interface and writes it to the loop.

[illegible]


```
1
2
3 001144 012737 010410 002016 INBUF0: MOV #10410, Q#BUFCMD
4 001152 012737 010440 002014 INBUF0: MOV #10440, Q#SELBUF
5 001160 004767 000122 JSR PC, ZEROBP
6 001164 012737 001400 172416 MOV #1400, Q#OPREG
7 001172 105737 172414 TSTB Q#CSR
8 001176 100375 BPL -4
9 001200 105037 172414 CLR B Q#CSR
10 001204 012737 010610 002016 INBUF1: MOV #10610, Q#BUFCMD
11 001212 012737 010640 002014 INBUF1: MOV #10640, Q#SELBUF
12 001220 004767 000062 JSR PC, ZEROBP
13 001224 012737 001400 172416 MOV #1400, Q#OPREG
14 001232 105737 172414 TSTB Q#CSR
15 001236 100375 BPL -4
16 001240 105037 172414 CLR B Q#CSR
17 001244 012737 010510 002016 OUTBUF0: MOV #10510, Q#BUFCMD
18 001252 012737 010540 002014 OUTBUF0: MOV #10540, Q#SELBUF
19 001260 004767 000022 JSR PC, ZEROBP
20 001264 012737 010710 002016 OUTBUF1: MOV #10710, Q#BUFCMD
21 001272 012737 010740 002014 OUTBUF1: MOV #10740, Q#SELBUF
22 001300 004767 000002 JSR PC, ZEROBP
23 001304 000520 BR STAT
24
25 001306 013700 002014 ZEROBP: MOV Q#SELBUF, R0
26 001312 013701 002016 ZEROBP: MOV Q#BUFCMD, R1
27 001316 010137 172416 MOV R1, Q#OPREG
28 001322 012737 001400 172416 MOV #1400, Q#OPREG
29 001330 105737 172414 TSTB Q#CSR
30 001334 100375 BPL -4
31 001336 105037 172414 CLR B Q#CSR
32 001342 013702 172416 MOV Q#OPREG, R2
33 001346 042702 177400 BIC #177400, R2
34 001352 010037 172416 MOV R0, Q#OPREG
35 001356 022702 000000 CMP #0, R2
36 001362 001412 BEQ RECK
37 001364 012737 001400 172416 MOV #1400, Q#OPREG
38 001372 105737 172414 TSTB Q#CSR
39 001376 100375 BPL -4
40 001400 105037 172414 CLR B Q#CSR
41 001404 005302 DEC R2
42 001406 000763 BR CMP
43 001410 010137 172416 MOV R1, Q#OPREG
44 001414 012737 001400 172416 MOV #1400, Q#OPREG
45 001422 105737 172414 TSTB Q#CSR
46 001426 100375 BPL -4
47 001430 105037 172414 CLR B Q#CSR
48 001434 013702 172416 MOV Q#OPREG, R2
49 001440 042702 177400 BIC #177400, R2
50 001444 022702 000000 CMP #0, R2
51 001450 001401 BEQ RTN
52 001452 000000 HALT
53 001454 010037 RTN: MOV R0, Q#OPREG
54 001460 000207 RTS
55
```

```

1
2
3
4 001462 012737 010402 172416
5 001470 012737 004400 172416
6 001476 105737 172414
7 001502 100375
8 001504 105037 172414
9 001510 012737 010401 172416
10 001516 012700 177400
11 001522 012737 004407 172416 1$:
12 001530 105737 172414
13 001534 100375
14 001536 105037 172414
15 001542 005200
16 001544 001366
17
18
19
20 001546 012737 010400 172416
21 001554 012737 002400 172416
22 001562 012737 002400 172416
23 001570 013700 172416
24 001574 042700 177400
25 001600 022700 000000
26 001604 001003
27 001606 005037 172414
28 001612 000401
29 001614 000000
30

```

;INITIALIZE ACRAM

```

MOV #4354., G#OPREG
MOV #2304., G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
MOV #4353., G#OPREG
MOV #-256., R0
MOV #2311., G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
INC R0
BNE 1$

```

;READ AND CLEAR STATUS

```

MOV #4352., G#OPREG
MOV #1280., G#OPREG
MOV #1280., G#OPREG
MOV G#OPREG, R0
BIC #177400, R0
CMP #0, R0
BNE ESTAT
CLRB G#CSR
BR START
ESTAT: HALT

```

```

;LDACR
;ADD=0
;GOOD WD
;LOOP UNTIL
;CLEAR DONE
;SEL ACRAM
;ALL ADDRESS
;NULL
;GOOD WD
;LOOP UNTIL
;CLEAR DONE

;RS(0)
;READ FALSE STATUS
;RS
;FETCH STATUS
;CLEAR MST BYTE
;STATUS=0
;NO ERROR STOP PROGRAM
;CLEAR BLUI CSR
; AND CONTINUE
;ERROR IN STATUS

```

IEEE SEND MACRO V03.01 27-MAR-79 19:51:15 PAGE 4

```

1 2
3 001616 012700 002020
4 001622 005001
5 001624 012700 000000
6 001630 106400
7 001632 012737 000320 177560
8 001640 000777
9
10 001642 113705 177562
11 001646 042705 177400
12 001652 110520
13 001654 005201
14 001656 022701 000015
15 001662 001005
16 001664 004767 000014
17 001670 012700 002020
18 001674 005001
19 001676 005037 177562
20 001702 000002
21
22
23
24 001704 012737 010540 172416
25 001712 012737 002020 172410
26 001720 012700 000026
27 001724 005400
28 001726 010037 172412
29 001732 012737 024000 172416
30 001740 000240
31 001742 105737 172414
32 001746 100401
33 001750 000000
34
35
36
37 001752 012737 010420 172416
38 001760 012737 004421 172416
39 001766 105737 172414
40 001772 100375
41 001774 105037 172414
42 002000 012700 077777
43 002004 000240
44 002006 077002
45 002010 000207
46
47
48
49 002012 125125
50 002014
51 002016
52 002020
53 002044 003
54 002045 014
55
56 001000

;IEEE FETCH AND SEND
START: MOV #DATA, R0
CLR R1
MOV R0, R0
MTPS R0
MOV #320, Q#ICB
WAIT: BR .

IEEE: MOV Q#IDB, R5
BIC #177400, R5
MOV RS, (R0)+
INC R1
CMP #13., R1
BNE ERTN
JSR PC, SEND
MOV #DATA, R0
CLR R1
CLR Q#IDB
ERTN: CLR Q#IDB
RTI

;DMA DATA BLOCK TO OUTBUF0
MOV #4448., Q#OPREG
MOV #DATA, Q#BAR
MOV #22., R0
NEG R0
MOV R0, Q#WCR
MOV #10240., Q#OPREG
NOP
TSTB Q#CSR
BHI SBITS
HALT

;SET OBUFF/WRTCMD BITS
MOV #4368., Q#OPREG
MOV #4421, Q#OPREG
TSTB Q#CSR
BPL -4
CLR Q#CSR
MOV #077777, R0
DELAY: NOP
SOB R0, DELAY
RTS PC

;DATA BLOCK
MASK: .WORD 125125
SELBUF: .BLKW
BUFCMD: .BLKW
DATA: .BLKB 20.
.BYTE 003
.BYTE 014
.END CLEAR

;DATA BLOCK
;DATA COUNTER
;PRI=0
;LOWER CPU
;FIRE IEEE BUS OK
;LOOP UNTIL READY
;FETCH BYTE
;CLEAR MST
;SAVE BYTE
;COUNT +1
;MESS. ALL REV YET
;NO DAC
;SEND IT ON THE LOOP
;RESET STACK
;RESET POINTER
;DAC

;SEL OUTBUF0
;BUS ADDRESS
;BYTE COUNT
;2 COMP OF BC
;WRITE IT
;FIRE DMA
;DELAY
;GOOD DMA
;YES SET WRITE BITS
;ERROR BAD DMA

;MODSTAT
;SET BUFFER FULL/WTCMD
;GOOD WRITE?
;NO LOOP UNTIL
;DELAY
;LOOP ON DELAY

```

IEEE SEND
SYMBOL TABLE
MACRO V03.01 27-MAR-79 19:51:15 PAGE 4-1

BAR = 172410	DELAY 002004	INBUF0 001144	RECK 001410	STAT 001546
BUFCMD 002016	ERTN 001676	INBUF1 001204	RTN 001454	SWT 001070
CLEAR 001000	ESTAT 001614	MASK 002012	SBITS 001752	S1\$ 001116
CLR\$ 001020	ICB = 177560	OPREG = 172416	SELBUF 002014	WAIT = 172412
CMP 001356	IDB = 177562	OTBUF0 001244	SEND 001704	WCR 001640
CSR = 172414	IEEE 001642	OTBUF1 001264	START 001616	ZEROBP 001306
DATA 002020				

. ABS. 002046 000
000000 001
ERRORS DETECTED: 1

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
.DK:SIIEEE-DK:SIIEEE

3.3 LOADER PROM DESCRIPTION

The prom loading programs for MSCDM are written in DEC MARCRO-11 Assembly language and the object code used to burn the loading proms. This allows a node attached to the loop to be loaded on power-up from the local host processor (PDP-11/V03). Nodes 21, 22, 23, 27 and 28 all have identical loader proms except for their individual read addresses. Node 25 proms also has a subroutine which allows packets received from the loop to be printed on its local printer (LA-36). This subroutine is used by the PGLOOP program. Node 26 proms also has a subroutine which allows packet received from loop destine for the SIG is passed over the local 9600 baud interface attached to the SIG. This subroutine is used by the FDMLDR program. The SIG has a special type of loader prom which load from a 9600 baud interface. This loader checks CRC on memory bytes and also automatically start the loader program.

```

1
2
3 000000
4 173000
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23

```

```

172410
172412
172414
172416
172418
100000
040000
000400
000200
000100
000040
000020
000010
000004
000002
000001

BAR= 172410
WCR= 172412
CSR= 172414
IOBUF= 172416
OPREG= 172418
BIT15= 100000
BIT14= 40000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1

```

```

24
25 173000 000005
26 173002 012700 000340
27 173006 106400
28 173010 012706 160500
29
30
31
32 173014 012737 010420 172416
33 173022 012737 004414 172416
34 173030 105737 172414
35 173034 100375
36 173036 105037 172414
37 173042 012737 004540 172416
38 173050 105737 172414
39 173054 100375
40 173056 105037 172414
41 173062 013703 174652
42 173066 005005

START: RESET #340, R0
MOV R0
MTPS R0
MOV #160500, SP

; INITIALIZE LINE SWITCHES

MOD STAT #4368., Q#OPREG
SET SWITCHES #4414, Q#OPREG
GOOD WD Q#CSR
LOOP UNTIL -4
RESET SWITCHES Q#CSR
GOOD WD #4540, Q#OPREG
LOOP UNTIL -4
LO CATION OF STATUS Q#CSR
CLEAR ERROR COUNT Q#STATUS, R3
CLR R5

```

```

;RESET BUS
;PHI=7
;CPU PRI
;RESET STACK

```

```

;MOD STAT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL

```

```

;RESET SWITCHES
;GOOD WD
;LOOP UNTIL

```

```

;LO CATION OF STATUS
;CLEAR ERROR COUNT

```

[illegible]

```

1
2
3 173420 010137 172416 172416 172416 172416
4 173424 012737 001400 172416
5 173432 105737 172414
6 173436 100375
7 173440 105037 172414
8 173444 013702 172416
9 173450 042702 177400
10 173454 010037 172416
11 173460 022702 000000
12 173464 001412
13 173466 012737 001400 172416
14 173474 105737 172414
15 173500 100375
16 173502 105037 172414
17 173506 005302
18 173510 000763
19 173512 010137 172416
20 173516 012737 001400 172416
21 173524 105737 172414
22 173530 100375
23 173532 105037 172414
24 173536 013702 172416
25 173542 042702 177400
26 173546 022702 000000
27 173552 001401
28 173554 005204
29 173556 010037 172416
30 173562 000207
31 173564 012737 010400 172416
32 173572 012737 002400 172416
33 173600 012737 002400 172416
34 173606 013700 172416
35 173612 042700 177400
36 173616 022700 000000
37 173622 001401
38 173624 005204
39 173626 022704 000000
40 173632 001406
41 173634 005205
42 173636 022705 000005
43 173642 001406
44 173644 000167 177220
45 173650 012713 000001
46 173654 000167 000004
47 173660 012713 177777
48

```

INITIALIZE BUFFERS & STATUS

```

;RDEUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH POINTER
;CLEAR MST BYTE
;SEL BUFFER
;POINTER=0
;NO FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;POINTER-1
;RDEUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH NEW POINTER
;CLEAR MST BYTE
;POINTER=0
;REPORT ERROR
;SEL BUFFER
;RETURN
;RCR-RS
;RS(FALSE)
;RS
;FETCH STATUS
;CLEAR MST BYTE
;STATUS=0 ?
;STATUS BAD REPORT
;WERE THERE ERRORS
;NO GOOD INIT
;ERROR +1
;ERROR LIMIT YET
;YES RETURN A 1(ERROR)
;NO RESTART VINIT
;GOOD INIT.
;LIU BAD INIT.

```



```

1 2 173664 005077 000764
3 173670 005077 000762
4 173674 005277 000756
5 173674 005277 000756
6 173700 105037 172414
7 173704 005737 172414
8 173710 100375
9 173712 013700 174650
10 173716 005010
11 173720 012706 160500
12 173724 012737 010400
13 173732 012737 002400
14 173740 013701 172416
15 173744 042701 17400
16
17
18 173750 132701 000004
19 173754 001410
20 173756 012702 010410
21 173762 012703 010440
22 173766 004767 000144
23 173772 004767 000530
24 173776 132701 000010
25 174002 001410
26 174004 012702 010610
27 174010 012703 010640
28 174014 004767 000116
29 174020 004767 000502
30
31
32
33 174024 132701 000001
34 174030 001410
35 174032 012702 010410
36 174036 012703 010440
37 174042 012704 000001
38 174046 004767 000026
39 174052 132701 000002
40 174056 001712
41 174060 012702 010610
42 174064 012703 010640
43 174070 012704 000002
44 174074 004767 000000
45

;LOADER START
SRTLDR: CLR QMEMPT
          CLR QPKCNT
          INC QPKCNT
          CLR QCSR
          TST QCSR
          BPL -4
          MOV Q#DATA, R0
          CLR (R0)
          MOV #160500, SP
          MOV #4352., Q#OPREG
          MOV #1280., Q#OPREG
          MOV Q#IOBUF, R1
          BIC #177400, R1

;BUFFER OVERFLOW
B2$: BITB #BIT02, R1
    BEQ B3$
    MOV #4360., R2
    MOV #4384., R3
    JSR PC, EMBF
    JSR PC, NAK
    BITB #BIT03, R1
    BEQ B01$
    MOV #4488., R2
    MOV #4512., R3
    JSR PC, EMBF
    JSR PC, NAK
    ;BUFFERS FULL
B01$: BITB #BIT00, R1
    BEQ B2$
    MOV #4360., R2
    MOV #4384., R3
    MOV #BIT00, R4
    JSR PC, PACK
    BITB #BIT01, R1
    BEQ B2$
    MOV #4488., R2
    MOV #4512., R3
    MOV #BIT01, R4
    JSR PC, PACK

;MEMORY POINTER
;PACKET COUNTER
;START'S AT 1
;CLEAR CSR
;REQUEST TET
;NO LOOP ON LIO
;ADDRESS OF DATA
;CLEAR DATA
;RESET SP
;WCR : RS(0)
;RD
;CLEAR UNUSED BITS

;OV-FL
;COM-SEL INBUF 0
;COM-RDBUFNT
;EMPTY BUFFER
;SEND NAK
;OV-FL
;COM-SEL INBUF 1
;COM-RDBUFNT
;SEND NAK

;BUFFER 0 FULL
;NO CK 1
;YES, COM-RDBUFADR
;COM-SEL INBUF0
;WHICH CRC BIT
;GO FIND PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM-RDBUFADR
;COM-SEL INBUF1
;WHICH CRC BIT
;GO FIND PACKET

```

```

1 3 174100 005010
2 4 174102 004767 000030
3 5 174106 005710
4 6 174110 100003
5 7 174112 004767 000410
6 8 174116 000207
7 9 174120 004767 000226
8 10 174124 005710
9 11 174126 100771
10 12 174130 004767 000364
11 13 174134 000207
12 14 174136 01E737 010600 172416
13 15 174144 012737 002400 172416
14 16 174152 013705 172410
15 17 174156 130405
16 18 174160 001002
17 19 174162 012710
18 20 174166 012737 160000
19 21 174174 010237 172416
20 22 174200 012737 001400 172416
21 23 174206 105737 172414
22 24 174212 100375
23 25 174214 105037 172414
24 26 174220 013704 172416
25 27 174224 042704 177400
26 28 174230 022704 000004
27 29 174254 001405
28 30 174256 022704 000204
29 31 174262 001402
30 32 174274 012710
31 33 174250 005404 177777
32 34 174252 010437 172412
33 35 174256 010337 172416
34 36 174262 012737 001400 172416
35 37 174270 105737 172414
36 38 174274 100375
37 39 174276 105037 172414
38 40 174302 012737 021000 172416
39 41 174310 000240
40 42 174312 105737 172414
41 43 174316 100402
42 44 174320 012710
43 45 174324 105037 172414
44 46 174330 012737 004400 172416
45 47 174336 105737 172414
46 48 174342 100375
47 49 174344 105037 172414
48 50 174350 000007
49 51

```

UNLOAD BUFFERS

```

CLR (R0)
JSR PC, EMBF
TST (R0)
BPL 4$
JSR PC, NAK
RTS PC, LOAD
TST (R0)
BPL 3$
JSR PC, ACK
RTS PC
MOV #4480., Q#OPREG
MOV #1280., Q#OPREG
MOV Q#IOBUF, R5
BITB R4, R5
BNE 5$
MOV #-1, (R0)
MOV #160000, Q#BAR
MOV R2, Q#OPREG
MOV #768., Q#OPREG
Q#CSR
BPL -4
Q#CSR
Q#IOBUF, R4
BIC #177400, R4
CMP #4, R4
BEQ 6$
CMP #132., R4
BEQ 6$
MOV #-1, (R0)
NEG R4
MOV R4, Q#WCR
MOV R3, Q#OPREG
MOV #768., Q#OPREG
Q#CSR
BPL -4
Q#CSR
Q#BAR, Q#OPREG
NOP
TSTB Q#CSR
BPL 7$
MOV #-1, (R0)
Q#CSR
CLR Q#CSR
MOV #2304., Q#OPREG
Q#CSR
BPL -4
CLR Q#CSR
RTS PC
.ENABLE LSB

```

;CLEAR STATUS
 ;EMPTY BUFFER
 ;GOOD UNLOAD
 ;YES
 ;NO NAK TO REPLACE
 ;RETURN
 ;LOAD + CONTROL PARSE
 ;CHECK RESPONSE
 ;BAD NAK
 ;ACK IT AND CONTINUE
 ;RS (CRC)
 ;RS CMD
 ;FETCH STATUS
 ;CRC OK
 ;YES CONTINUE
 ;FLAG BAD CRC
 ;ADDRESS TO WRITE
 ;RDBUFADR
 ;READ POINTER
 ;GOOD RD
 ;NO LOOP UNTIL READY
 ;CLEAR DONE BIT
 ;READ POINTER
 ;CLEAR UNUSED BITS
 ;CONTROL PACKET
 ;YES UNLOAD
 ;MEMORY IMAGE UNLOAD
 ;BAD UNLOAD
 ;2'S COMP
 ;BYTE COUNT
 ;SEL BUFFER
 ;FALSE RD
 ;GOOD RD
 ;NO LOOP UNTIL READY
 ;PIRE DMA RD
 ;DELAY
 ;GOOD DMA
 ;YES
 ;BAD DMA XFER
 ;CLEAR CSR
 ;RESET POINTER TO 255
 ;GOOD WD
 ;NO RETRY
 ;CLEAR DONE BIT

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35			
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35			
174352	016702	000302	000302	174356	122712	000000	000000	174362	001426	000266	000266	000144	174364	016703	000266	000266	000144	174370	022713	000100	000100	174374	001002	000100	000100	174376	005013	000501	000501	174400	005213	000521	000521	174402	121312	000140	000140
174404	000207	000207	000207	174410	062702	000002	000002	174416	017702	000232	000232	000100	174422	012704	000232	000232	000100	174426	012225	000232	000232	174430	077402	000232	000232	174432	010377	000216	000216	174436	000207	000207	000207	174440	122702	000202	000202
174446	013703	174450	013703	174454	011310	000207	000207	174456	000207	000001	000001	000001	174460	122702	000005	000005	000005	174464	001005	000005	000005	174468	001005	000005	000005	174470	001370	000005	000005	174474	012706	000110	000110	174478	000110	000110	000110
174482	122762	000006	000006	174486	122762	000006	000006	174490	000100	000100	000100	174494	000100	000100	000100	000100	000100	174498	000100	000100	000100	174502	000100	000100	000100	174506	000100	000100	000100	174510	000100	000100	000100	174514	000100	000100	000100
174518	000207	000207	000207	174522	000207	000207	000207	174526	000207	000207	000207	000207	174530	000207	000207	000207	000207	174534	000207	000207	000207	174538	000207	000207	000207	174542	000207	000207	000207	174546	000207	000207	000207	174550	000207	000207	000207

FROM NODE 21 MACRO V03.01 27-MAR-79 18:54:23 PAGE 7

```

1
2
3 174520 012705 174630 ACK: MOV #ACKPK, R5
4 174524 000402 BR 85
5 174526 012705 174640 NAK: MOV #NAKPK, R5
6 174532 012737 010540 172416 85: MOV #4448., G#OPREG
7 174540 012504 MOV (R5)+, R4
8 174542 067704 000110 ADD QPKCNT, R4
9 174546 010437 172416 MOV R4, G#OPREG
10 174552 105737 172414 TSTB Q#CSR
11 174556 100375 BPL -4
12 174560 012704 000003 MOV #3, R4
13 174564 012537 172416 MOV (R5)+, G#OPREG
14 174570 105737 172414 TSTB Q#CSR
15 174574 100375 BPL -4
16 174576 077406 SOB R4, 95
17 174600 012737 010420 MOV #1368., G#OPREG
18 174606 012737 004421 172416 MOV #4421, G#OPREG
19 174614 105737 172414 TSTB Q#CSR
20 174620 100375 BPL -4
21 174622 105037 CLR B Q#CSR
22 174626 000207 RTS PC

```

```

;ADDRESS OF ACK
;ADDRESS OF NAK
;SEL OUTBFO
;FETCH HEADER
;ADD PACKET COUNT
;WRITE IT
;GOOD WD
;NO LOOP
;BYTE COUNT
;WD
;GOOD WD
;NO LOOP
;YES LOOP TIL 0
;MODSTAT
;SET BITS
;GOOD WD
;LOOP UNTIL

```

```

24 174630 004400 ACKPK: .WORD 4400
25 174632 004402 .WORD 4402
26 174634 004403 .WORD 4403
27 174636 004405 .WORD 4405
28 174640 004400 NAKPK: .WORD 4400
29 174642 004403 .WORD 4403
30 174644 004405 .WORD 4405
31 174646 004400 DATA: .WORD 160400
32 174650 160400 STATUS: .WORD 160402
33 174652 160402 MEMPT: .WORD 160404
34 174654 160404 PKCNT: .WORD 160406
35 174656 160406 BUFFER: .WORD 160000
36 174660 160000 MODE: .WORD 4404
37 174662 004404 .END START
38 174664 173000
39
40

```


PROM NODE 21 MACRO V03.01 27-MAR-79 18:54:23 PAGE 7-1
SYMBOL TABLE

ACK	174520	BIT06 = 000100	CMEQ	174440	PACK	174100
ACKPK	174630	BIT07 = 000200	CSR	= 172414	PCNT	174656
DAR	= 172410	BIT08 = 000400	DATA	174650	SRTIDR	173664
BIT00 = 000001		BIT14 = 040000	EMBF	174136	START	173000
BIT01 = 000002		BIT15 = 100000	INBUF0	173276	STAT	173564
BIT02 = 000004		BUFER	174660	INBUF1	173332	
BIT03 = 000010		B01\$	174024	INIT	173070	
BIT04 = 000020		B2\$	173750	INITLS	173014	
BIT05 = 000040		B3\$	173770	IOBUF	= 172416	

.ABS. 174664 000
000000 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
.DE: PROM21=DI:PROM21

LIO	173704	OTBUF1	173402
LOAD	174352		
MEMPT	174654		
NAK	174526		
NAKPK	174640		
MODE	174662		
OPREG	= 172416		
OTBUF0	173366		
OTBUF1	173402		

STRAM	173222
WCR	= 172412
ZEROBP	173420

PROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 1

```

1
2
3 000000      173000
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42

      .TITLE PROM NODE 22
      .IDENT /V2.0/
      .ASECT
      .-173000

      BAR= 172410
      WCR= 172412
      CSR= 172414
      IOBUF= 172416
      OPREG= 172416
      BIT15= 1000000
      BIT14= 400000
      BIT08= 400
      BIT07= 200
      BIT06= 100
      BIT05= 40
      BIT04= 20
      BIT03= 10
      BIT02= 4
      BIT01= 2
      BIT00= 1

      START: RESET #340, R0
              MOV R0
              MTPS R0
              MOV #160500, SP

              ; INITIALIZE LINE SWITCHES

              MOD STAT
              ;SET SWITCHES
              ;GOOD WD
              ;LOOP UNTIL

              ;RESET SWITCHES
              ;GOOD WD
              ;LOOP UNTIL

              ;LO CATION OF STATUS
              ;CLEAR ERROR COUNT

```

PROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 2

INITIALIZE	LIU
1	
2	
3	173070 005004
4	173072 012737 010402
5	173100 012737 004400
6	173106 105737 172414
7	173112 100375
8	173114 105037 172414
9	173120 012737 010401
10	173126 012700 000400
11	173132 012737 004407
12	173140 105737 172414
13	173144 100375
14	173146 105037 172414
15	173152 077011
16	173154 012700 000400
17	173160 012737 001400
18	173166 105737 172414
19	173172 100375
20	173174 105037 172414
21	173200 013901 172416
22	173204 042701 177760
23	173210 022701 000007
24	173214 001401
25	173216 005204
26	173220 077021
27	173222 012737 010402
28	173230 013737 174062
29	173236 105737 172414
30	173242 100375
31	173244 105037 172414
32	173250 012737 010401
33	173256 012737 004404
34	173264 105737 172414
35	173270 100375
36	173272 105037 172414
37	173276 012701 010410
38	173302 012700 010440
39	173306 004767 000106
40	173312 012737 001400
41	173320 105737 172414
42	173324 100375
43	173326 105037 172414
44	173332 012701 010610
45	173336 012700 010640
46	173342 004767 000052
47	173346 012737 001400
48	173354 105737 172414
49	173360 100375
50	173362 105037 172414
51	173366 012701 010510
52	173372 012700 010540
53	173376 004767 000016
54	173402 012701 010710
55	173406 012700 010740
56	173412 004767 000002
57	173416 000462
58	
59	
60	
61	
62	
63	
64	
65	
66	
67	
68	
69	
70	
71	
72	
73	
74	
75	
76	
77	
78	
79	
80	
81	
82	
83	
84	
85	
86	
87	
88	
89	
90	
91	
92	
93	
94	
95	
96	
97	
98	
99	
100	

FROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 3

```

1
2
3 173420 010137 172416 172416 172416
4 173424 012737 001400 172416
5 173432 105737 172414
6 173436 100375
7 173440 105037 172414
8 173444 013702 172416
9 173450 042702 177400
10 173454 010037 172416
11 173460 022702 000000
12 173464 001412
13 173466 012737 001400 172416
14 173474 105737 172414
15 173500 100375
16 173502 105037 172414
17 173506 005302
18 173510 000763
19 173512 010137 172416
20 173516 012737 001400 172416
21 173524 105737 172414
22 173530 100375
23 173532 105037 172414
24 173536 013702 172416
25 173542 042702 177400
26 173546 022702 000000
27 173552 001401
28 173554 005204
29 173556 010037 172416
30 173562 000207
31 173564 012737 010400 172416
32 173572 012737 002400 172416
33 173600 012737 002400 172416
34 173606 013700 172416
35 173612 042700 177400
36 173616 022700 000000
37 173622 001401
38 173624 005204
39 173626 022704 000000
40 173632 001406
41 173634 005205
42 173636 022705 000005
43 173642 001406
44 173644 000167 177220
45 173650 012713 000001
46 173654 000167 000004
47 173660 012713 172777
48

;INITIALIZE BUFFERS & STATUS
ZEROBF: MOV R1, G#OPREG
MOV #1400, G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
MOV G#IOBUF, R2
BIC #177400, R2
MOV R0, G#OPREG
CMP #0, R2
BEQ 4$
MOV #1400, G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
DEC R2
BR 4$
MOV R1, G#OPREG
MOV #1400, G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
MOV G#IOBUF, R2
BIC #177400, R2
CMP #0, R2
BEQ 6$
INC R4
MOV R0, G#OPREG
RTS PC
MOV #4352, G#OPREG
MOV #1280, G#OPREG
MOV #1280, G#OPREG
MOV G#OPREG, R0
BIC #177400, R0
CMP #0, R0
BEQ 7$
INC R4
MOV R4, R4
CMP 8$
INC R5
CMP #5, R5
BEQ 9$
JMP INIT
MOV #1, (R3)
JMP SRTDR
MOV #-1, (R3)
;ENABLE LSB

;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH POINTER
;CLEAR MST BYTE
;SEL BUFFER
;POINTER=0
;NO FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;POINTER-1
;RDBUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH NEW POINTER
;CLEAR MST BYTE
;POINTER=0
;REPORT ERROR
;SEL BUFFER
;RETURN
;WCR:RS
;RS(FALSE)
;RS
;FETCH STATUS
;CLEAR MST BYTE
;STATUS=0 ?
;STATUS BAD REPORT
;WERE THERE ERRORS
;NO GOOD INIT
;ERROR +1
;ERROR LIMIT YET
;YES RETURN A 1(ERROR)
;NO RESTART VINIT
;GOOD INIT.
;LIV BAD INIT.

```


FROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 4

```

1 2 173664 005077 000764 SRTLDR: CLR GMEPT
3 4 173670 005077 000762 QPKCNT
5 173674 005277 000756 INC QPKCNT
6 173700 105037 172414 CLR GCSR
7 173704 005732 172414 LIO: TST GCSR
8 173710 100375 BPL -4
9 173712 013700 174550 MOV G#DATA, R0
10 173716 005010 CLR (R0)
11 173720 012706 MOV #160500, SP
12 173724 012737 MOV #4352., G#OPREG
13 173732 012737 MOV #1280., G#OPREG
14 173740 013701 MOV G#IOBUF, R1
15 173744 042701 BIC #177400, R1
16
17
18
19 173750 132701 B2$: BITB #BIT02, R1
20 173754 001410 BEQ B3$
21 173756 012702 MOV #4360., R2
22 173762 012703 MOV #4384., R3
23 173766 004767 JSR PC, EMBF
24 173772 004767 JSR PC, NAK
25 173776 132701 B3$: BITB #BIT03, R1
26 174002 001410 BEQ B01$
27 174004 012702 MOV #4488., R2
28 174010 012703 MOV #4512., R3
29 174014 004767 JSR PC, EMBF
30 174020 004767 JSR PC, NAK
31
32
33
34 174024 132701 B01$: BITB #BIT00, R1
35 174030 001410 BEQ 2$
36 174032 012702 MOV #4360., R2
37 174036 012703 MOV #4384., R3
38 174042 012704 MOV #BIT00, R4
39 174046 004767 JSR PC, PACK
40 174052 132701 2$: BITB #BIT01, R1
41 174056 001712 BEQ LIO
42 174060 012702 MOV #4488., R2
43 174064 012703 MOV #4512., R3
44 174070 012704 MOV #BIT01, R4
45 174074 004767 JSR PC, PACK

;MEMORY POINTER
;PACKET COUNTER
;START'S AT 1
;CLEAR CSR
;REQUEST YET
;NO LOOP ON LIO
;ADDRESS OF DATA
;CLEAR DATA
;RESET SP
;WCR : RS(0)
;RD
;CLEAR UNUSED BITS

;OV-FL
;COM=SEL INBUF 0
;COM=RDBUFFNT
;EMPTY BUFFER
;SEND NAK
;OV-FL
;COM=SEL INBUF 1
;COM=RDBUFFNT
;SEND NAK

;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUFADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO FIND PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUFADR
;COM=SEL INBUF1
;WHICH CRC BIT
;GO FIND PACKET

```

FROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 5

```

1 2 174100 005010
3 4 174102 004767 000030
4 174106 005710
5 174110 100003
6 174112 004767
7 174116 000207
8 174120 004767 000226
9 174124 005710
10 174126 100771
11 174130 004767 000364
12 174134 000207
13 174136 012737 010600 172416
14 174140 012737 002400 172416
15 174144 013705 172416
16 174152 130405
17 174156 001002
18 174160 001002
19 174162 012710
20 174166 012737 160000
21 174174 010237 172416
22 174200 012737 001400 172416
23 174206 105737 172414
24 174212 100375
25 174214 105037 172414
26 174220 013704 172416
27 174224 042704 177400
28 174230 022704 000004
29 174234 001405
30 174236 022704 000204
31 174242 001402
32 174244 012710 177777
33 174250 005404
34 174252 010437 172412
35 174256 010337 172416
36 174262 012737 001400 172416
37 174270 105737 172414
38 174274 100375
39 174276 105037 172414
40 174302 012737 021000 172416
41 174310 000240
42 174312 105737 172414
43 174316 100402
44 174320 012710 177777
45 174324 105037 172414
46 174330 012737 004400 172416
47 174336 105737 172414
48 174342 100375
49 174344 105037 172414
50 174350 000207
51

```

UNLOAD BUFFERS

```

CLR (R0)
JSR PC, EMBF
TST (R0)
BPL 4$
PC, NAK
PC, LOAD
RTS PC
JSR PC, LOAD
TST (R0)
BMI 3$
PC, ACK
RTS PC
MOV #4480., G#OPREG
MOV #1280., G#OPREG
MOV G#IOBUF, R5
BITB R4, R5
BNE 5$
MOV #1, (R0)
MOV #160000, G#BAR
MOV R2, G#OPREG
MOV #768., G#OPREG
G#CSR
TSTB G#CSR
BPL -4$
CLR G#CSR
MOV G#IOBUF, R4
BIC #177400, R4
CMP #4, R4
BEQ 6$
CMP #132., R4
BEQ 6$
MOV #1, (R0)
NEG R4
MOV R4, G#WCR
MOV R3, G#OPREG
MOV #768., G#OPREG
TSTB G#CSR
BPL -4$
CLR G#CSR
MOV #8704., G#OPREG
NOP
TSTB G#CSR
BMI 7$
MOV #1, (R0)
CLR G#CSR
MOV #2304., G#OPREG
TSTB G#CSR
BPL -4$
CLR G#CSR
RTS PC
.ENABLE LSB

```

```

;CLEAR STATUS
;EMPTY BUFFER
;GOOD UNLOAD
;YES
;NO NAK TO REPLACE
;RETURN
;LOAD + CONTROL PARSE
;CHECK RESPONSE
;BAD NAK
;ACK IT AND CONTINUE
;RS (CRC)
;RS CMD
;FETCH STATUS
;CRC OK
;YES CONTINUE
;FLAG-BAD CRC
;ADDRESS TO WRITE
;RDBUFADR
;HEAD POINTER
;GOOD RD
;NO-LOOP UNTIL READY
;CLEAR DONE BIT
;HEAD POINTER
;CLEAR UNUSED BITS
;CONTROL PACKET
;YES UNLOAD
;MEMORY IMAGE UNLOAD
;BAD UNLOAD
;2'S COMP
;BYTE COUNT
;SEL BUFFER
;FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;FIRE DMA RD
;DELAY
;GOOD DMA
;YES
;BAD DMA XFER
;CLEAR CSR
;RESET POINTER TO 255
;GOOD WD
;NO RETRY
;CLEAR DONE BIT

```

PROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 6

```

1 2 174352 016702 000302          ;PACKET PARSE
3 4 174356 122712 000000          MOV    BUFFER, R2
5 6 174362 001426          BEQ    #0, (R2)
7 8 174364 016703 000266          MOV    PCNT, R3
9 10 174370 022713 000144          CMP    #100., (R3)
11 12 174374 001002          BNE    1$, (R3)
13 14 174376 005013          CLR    (R3)
15 16 174400 005213          INC    (R3), (R2)
17 18 174402 121312          CMPB   2$, (R2)
19 20 174404 001401          BEC    PC
21 22 174406 000207          RTS     R2
23 24 174410 062702 000002          ADD    #2, R2
25 26 174414 005213          INC    (R3)
27 28 174416 017703 000232          MOV    QMEMPT, R3
29 30 174422 012704 000100          MOV    #64., R4
31 32 174426 012223          MOV    (R2)+, (R3)+
33 34 174430 077402          SOB    R4, 3$
35 36 174432 010377 000216          MOV    R3, QMEMPT
37 38 174436 000207          RTS     PC
39 40 174440 122762 000001          CMPB   #1, 1(R2)
41 42 174446 001004          BNE    5$, Q#STATUS, R3
43 44 174450 013703 174652          MOV    (R3), (R0)
45 46 174454 011310          RTS     PC
47 48 174456 000207          CMPB   #5, 1(R2)
49 50 174460 122762 000005          BNE    6$, Q#40, R0
51 52 174466 001005          MOV    #1000, SP
53 54 174470 013700 000040          MOV    (R0), (R0)
55 56 174474 012706 001000          JMP    #6, 1(R2)
57 58 174500 000110 000006          CMPB   7$, SRTIDR
59 60 174502 122762 000001          BNE    PC
61 62 174510 001002 177146          JMP    PC
63 64 174512 000167          RTS
65 66 174516 000207          RTS

```

;BUFFER AREA
 ;MEMORY OR CONTROL
 ;CONTROL PACKET
 ;PACKET COUNTER
 ;LIMIT
 ;NO CONTINUE
 ;RESET POINTER
 ;NEW PACKET
 ;RETURN
 ;OFFSET HEADER
 ;COUNT + 1
 ;BYTE COUNT
 ;LOAD MEMORY
 ;LOOP UNTIL ZERO
 ;IS IT A REPORT
 ;NO
 ;LOCATION OF STATUS
 ;TRANSFER STATUS
 ;IS IT A START
 ;NO
 ;FETCH START ADDRESS
 ;RESET USER STACK
 ;LEAVE FROM TO RAM
 ;WAS IT A LOAD COMMAND
 ;NO
 ;YES GOTO LOAD START

FROM NODE 22 MACRO V03.01 27-MAR-79 18:55:12 PAGE 7

```

1 2
3 174520 012705 174630
4 174524 000402
5 174526 012705 174640
6 174532 012737 010540 172416
7 174540 012504
8 174542 067704
9 174546 010437 000110
10 174552 105737 172416
11 174556 100375 172414
12 174560 012704 000003
13 174564 012537 172416
14 174570 105737 172414
15 174574 100375
16 174576 077406
17 174580 012737 010420 172416
18 174586 012737 004421 172416
19 174614 105737 172414
20 174620 100375
21 174622 105037 172414
22 174626 000207
23
24
25 174630 004400
26 174632 004402
27 174634 004403
28 174636 004405
29 174640 004400
30 174642 004403
31 174644 004403
32 174646 004405
33 174650 160400
34 174652 160402
35 174654 160404
36 174656 160406
37 174660 160000
38 174662 004403
39
40 173000

;ACK/NAK ROUTINE
MOV #ACKPK, R5
BR B5
MOV #NAKPK, R5
MOV #4448., G#OPREG
MOV (R5)+, R4
ADD QPKCNT, R4
MOV R4, G#OPREG
TSTB G#CSR
BPL -4
MOV #3, R4
MOV (R5)+, G#OPREG
TSTB G#CSR
BPL -4
SOB R4, 95
MOV #4368., G#OPREG
MOV #4421, G#OPREG
TSTB G#CSR
BPL -4
CLRB G#CSR
RTS PC

;ADDRESS OF ACK
;ADDRESS OF NAK
;SEL OUTBUF0
;FETCH HEADER
;ADD PACKET COUNT
;WRITE IT
;GOOD WD
;NO LOOP
;BYTE COUNT
;WD
;GOOD WD
;NO LOOP
;YES LOOP TIL 0
;MODSTAT
;SET BITS
;GOOD WD
;LOOP UNTIL

```


SYMBOL TABLE

ACK	174520	BIT06 = 000100	CMEQ	174440	LIO	173704	PACK	174100
ACKPK	174630	BIT07 = 000200	CSR	172414	LOAD	174352	PKINT	174656
BAR	172410	BIT08 = 000400	DATA	174650	NAKPT	174654	SRT1DR	173664
BIT00 = 000001	BIT14 = 040000	EMBF	174136	NEK	174526	START	173000	
BIT01 = 000002	BIT15 = 100000	INBUF0	173276	NARPK	174640	STAT	173564	
BIT02 = 000004	BUFFER	INBUF1	173332	NODE	474662	STATUS	174652	
BIT03 = 000010	B01\$	INIT	173070	OPREG =	172416	STRAM	173222	
BIT04 = 000020	B2\$	INITLS	173750	OTBUF0	173366	WCR	172412	
BIT05 = 000040	B3\$	IOBUF =	173776	OTBUF1	173402	ZBROBP	173420	

. ABS. 174664 000 000

000000	001
--------	-----

ERRORS DETECTED:

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)

DYNAMIC MEMORY AVAILABLE FOR 51 PAGES

```

,DK:PROM22=DK:PROM22

```

```

1
2
3 000000
4 173000
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23

```

```

172410
172412
172414
172416
172416
100000
040000
000400
000200
000100
000040
000020
000010
000004
000002
000001

BAR= 172410
WCR= 172412
CSR= 172414
IOBUF= 172416
OPREG= 172416
BIT15= 100000
BIT14= 40000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1

```

```

24
25 173000 000005
26 173002 012700 000340
27 173006 106400
28 173010 012706 160500
29
30
31
32 173014 012737 010420 172416 172416
33 173022 012737 004414 172416
34 173030 105737 172414
35 173034 100375
36 173036 105037 172414
37 173042 012737 004540 172416
38 173050 105737 172414
39 173054 100375
40 173056 105037 172414
41 173062 013703 174652
42 173066 005005

START: RESET
MOV #340, R0
MTPS R0
MOV #160500, SP

; INITIALIZE LINE SWITCHES

INITLS: MOV #4368., Q#OPREG
MOV #4414., Q#OPREG
TSTB Q#CSR
BPL -4
CLRQ Q#CSR
MOV #4540., Q#OPREG
TSTB Q#CSR
BPL -4
CLRQ Q#CSR
MOV Q#STATUS, R3
CLR R5

;RESET BUS
;PRI=7
;CPU PRI
;RESET STACK

;MOD STAT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL

;RESET SWITCHES
;GOOD WD
;LOOP UNTIL

;LOCATION OF STATUS
;CLEAR ERROR COUNT

```


FROM NODE 23

INITIALIZE BUFFERS & STATUS

```

:RDBUFADR
:RD
:GOOD RD
:NO LOOP UNTIL READY
:CLEAR DONE BIT
:FETCH POINTER
:CLEAR MST BYTE
:SEL BUFFER
:POINTER=0

:NO FALSE RD
:GOOD RD
:NO LOOP UNTIL READY
:CLEAR DONE BIT
:POINTER-1

:RDBUFADR
:RD
:GOOD RD
:NO LOOP UNTIL READY
:CLEAR DONE BIT
:FETCH NEW POINTER
:CLEAR MST BYTE
:POINTER=0

:REPORT ERROR
:SEL BUFFER
:RETURN
:WCR:RS
:RS(FALSE)
:RS
:FETCH STATUS
:CLEAR MST BYTE
:STATUS=0 ?

:STATUS BAD REPORT
:WENT THERE ERRORS
:NO GOOD INIT
:ERROR +1
:ERROR LIMIT YET
:YES RETURN A-1(ERROR
:NO RESTART VINIT
:GOOD INIT.

:LIU BAD INIT.

```



```

1  ;LOADER START
2
3 173664 005077 000764
4 173670 005077 000762
5 173674 005277 000756
6 173700 105037 172414
7 173704 005737 172414
8 173710 100375
9 173712 013700 174650
10 173716 005010
11 173720 012706 160500
12 173724 012737 010400
13 173732 012737 002400
14 173740 013701 172416
15 173744 042701 177400
16
17
18 ;BUFFER OVERFLOW
19 173750 132701 000004
20 173754 001410
21 173756 012702 010410
22 173762 012703 010440
23 173766 004767 000144
24 173772 004767 000530
25 173776 132701 000010
26 174002 001410
27 174004 012702 010610
28 174010 012703 010640
29 174014 004767 000116
30 174020 004767 000502
31
32
33 ;BUFFERS FULL
34 174024 132701 000001
35 174030 001410
36 174032 012702 010410
37 174036 012703 010440
38 174042 012704 000001
39 174046 004767 000026
40 174052 132701 000002
41 174056 001712
42 174060 012702 010610
43 174064 012703 010640
44 174070 012704 000002
45 174074 004767 000000

;MEMORY POINTER
;PACKET COUNTER
;START'S AT 1
;CLEAR CSR
;REQUEST YET
;NO LOOP ON LIO
;ADDRESS-OF DATA
;CLEAR DATA
;RESET SP
;WCR : RS(0)
;RD
;CLEAR UNUSED BITS

;OV-FL
;COM=SEL INBUF 0
;COM=RDBUPPNT
;EMPTY BUFFER
;SEND NAK
;OV-FL
;COM=SEL INBUF 1
;COM=RDBUPPNT
;SEND NAK

;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUFADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO FIND PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUFADR
;COM=SEL INBUF1
;WHICH CRC BIT
;GO FIND PACKET

```

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51
005010	174100	004767	000030																																															
004767	174102	005710																																																
100003	174110	000063																																																
000410	174112	0004767																																																
000207	174116	000207																																																
0004767	174120	0004767																																																
000226	174124	0005710																																																
	174126	100771																																																
000364	174130	0004767																																																
	174134	000207																																																
010600	174136	012737																																																
002400	174144	012737																																																
172416	174152	013705																																																
	174156	130405																																																

PROM NODE 23 MACRO V03.01 27-MAR-79 18:56:02 PAGE 6

[illegible]

PROM NODE 23 MACRO V03.01 27-MAR-79 18:56:02 PAGE 7

```

1 2
3 174520 012705 174630      ACK:      MOV  #ACKPK, R5      ;ACK/NAK ROUTINE
4 174524 000402      BR
5 174526 012705      MOV  #NAKPK, R5      ;ADDRESS OF NAK
6 174532 012737      MOV  #4448., G#OPREG  ;SEL OUTREG
7 174540 012504      MOV  (R5)+, R4      ;FETCH HEADER
8 174542 067704      ADD   QPKCNT, R4      ;ADD PACKET COUNT
9 174546 010437      TSTB  Q#CSR         ;WRITE IT
10 174552 105737 172414      BPL  -4      ;GOOD WD
11 174556 100375      MOV  #3, R4         ;NO LOOP
12 174560 012704      MOV  (R5)+, G#OPREG  ;BYTE COUNT
13 174564 012537      TSTB  Q#CSR         ;WD
14 174570 105737 172414      BPL  -4      ;GOOD WD
15 174574 100375      SOB  R4, 9$        ;NO LOOP
16 174576 077406      MOV  #4368., G#OPREG  ;YES LOOP TIL 0
17 174600 012737      MOV  #4421., G#OPREG  ;MODSTAT
18 174606 012737      TSTB  Q#CSR         ;SET BITS
19 174614 105737      BPL  -4      ;GOOD WD
20 174620 100375      CLR  B              ;LOOP UNTIL
21 174622 105037      RTS
22 174626 000207      PC
23
24
25 174630 004400      ACKPK:  .WORD  4400
26 174632 004402      .WORD  4402
27 174634 004403      .WORD  4403
28 174636 004405      .WORD  4405
29 174640 004400      NAKPK:  .WORD  4400
30 174642 004403      .WORD  4403
31 174644 004403      .WORD  4403
32 174646 004405      .WORD  4405
33 174650 160400      DATA:  .WORD  160400
34 174652 160402      STATUS:  .WORD  160402
35 174654 160404      MEMPT:  .WORD  160404
36 174656 160406      PKCNT:  .WORD  160406
37 174660 160000      BUFFER: .WORD  160000
38 174662 004406      NODE:   .WORD  4406
39
40
41      .END      START
      173000

```


ACK	174520	BIT06 = 000100	CNEQ	174440	LIO	173704	PACK	174100
ACKPR	174630	BIT07 = 000200	CSR	172414	LOAD	174352	PKCNT	174656
BAR	172410	BIT08 = 000400	DATA	174650	MMPT	174654	SRTLDR	173664
BIT00	000001	BIT14 = 040000	ENR	174136	NAX	174526	START	173000
BIT01	000002	BIT15 = 100000	INBUF0	173276	NAXPK	174640	STAT	173564
BIT02	000004	BIT16 = 000000	INBUF1	173332	NODE	174662	STATUS	174652
BIT03	000010	B01\$	INIT	173070	OBRG =	172416	STRAM	173222
BIT04	000020	B2\$	INITLS	173014	OTRBU0	173366	WCR	172412
BIT05	000040	B3\$	ICBU0 =	172416	OTRBU1	173402	ZEROBP	173420

```

. ABS. 174664 000
          000000 001
ERRORS DETECTED: 0

```

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
;DK: PROM23=DK: PROM23

PROM NODE 25 MACRO V03.01 27-MAR-79 18:56:53 PAGE 1

```

1  TITLE PROM NODE 25
2  .IDENT /V2.0/
3  .ASECT
4  . =173000
5
6
7  172410
8  172412
9  172414
10 172416
11 172416
12 172416
13 172416
14 172416
15 172416
16 172416
17 172416
18 172416
19 172416
20 172416
21 172416
22 172416
23 172416
24 172416
25 172416
26 172416
27 172416
28 172416
29 172416
30 172416
31 172416
32 172416
33 172416
34 172416
35 172416
36 172416
37 172416
38 172416
39 172416
40 172416
41 172416
42 172416
43 172416
44 172416

```

BAR= 172410
WCR= 172412
CSR= 172414
IOBUF= 172416
OPREG= 172416
XCSR= 172416
XBUF= 172416
BIT15= 100000
BIT14= 40000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1

START: RESET #340, R0
MOV R0
MTPS R0
MOV #160500, SP

INITIALIZE LINE SWITCHES

INITLS: MOV #4368., Q#OPREG
MOV #4414., Q#OPREG
TSTB Q#CSR
BPL -4
CLRQ Q#CSR
MOV #4540., Q#OPREG
TSTB Q#CSR
BPL -4
CLRQ Q#CSR
MOV Q#STATUS, R3
CLR R5

173000 000005
173002 012700 000340
173006 106400
173010 012700 160500

173014 012737 010420 172416
173022 012737 004414 172416
173030 105737 172414
173034 100375
173036 105037 172414
173042 012737 004540 172416
173050 105737 172414
173054 100375
173056 105037 172414
173062 013703 174712
173066 005005

RESET BUS
PRI=7
CPU PRI
RESET STACK

MOD STAT
SET S ITCHES
GOOD WD
LOOP UNTIL

RESET SWITCHES
GOOD WD
LOOP UNTIL

LO CATION OF STATUS
CLEAR ERROR COUNT

```

1 1
2 2
3 3 173070 005004
4 4 173072 012737
5 5 173100 012737
6 6 173106 105737
7 7 173112 100375
8 8 173114 105037
9 9 173120 012737
10 10 173126 012700
11 11 173132 012737
12 12 173140 105737
13 13 173144 100375
14 14 173146 105037
15 15 173152 077011
16 16 173154 012700
17 17 173160 012737
18 18 173166 105737
19 19 173172 100375
20 20 173174 105037
21 21 173200 013701
22 22 173204 042701
23 23 173210 022701
24 24 173214 001401
25 25 173216 005204
26 26 173220 077021
27 27 173222 012737
28 28 173230 013737
29 29 173236 105737
30 30 173242 100375
31 31 173244 105037
32 32 173250 012737
33 33 173256 012737
34 34 173264 105737
35 35 173270 100375
36 36 173272 105037
37 37 173276 012701
38 38 173302 012700
39 39 173306 004767
40 40 173312 012737
41 41 173320 105737
42 42 173324 100375
43 43 173326 105037
44 44 173332 012701
45 45 173336 012700
46 46 173342 004767
47 47 173346 012737
48 48 173354 105737
49 49 173360 100375
50 50 173362 105037
51 51 173366 012701
52 52 173372 012700
53 53 173376 004767
54 54 173402 012701
55 55 173406 012700
56 56 173412 004767
57 57 173416 000462

INIT:
172416 CLR R4
172416 MOV #4354., G#OPREG
172416 MOV #2304., G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 MOV #4353., G#OPREG
172416 MOV #256., R0
172416 MOV #2311., G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 SOB R0, 1$
172416 MOV #256., R0
172416 MOV #768., G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 MOV G#IOBUF, R1
172416 BIC #177760, R1
172416 CMP #7, R1
172416 BEQ 3$
172416 INC R4
172416 SOB R0, 2$
172416 MOV #4354., G#OPREG
172416 MOV G#NODE, G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 MOV #4353., G#OPREG
172416 MOV #4404, G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 MOV #10410, R1
172416 MOV #10640, R0
172416 JSR PC, ZEROBP
172416 MOV #1400, G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 MOV #10610, R1
172416 MOV #10640, R0
172416 JSR PC, ZEROBP
172416 MOV #1400, G#OPREG
172416 TSTB G#CSR
172416 BPL -4
172416 CLRB G#CSR
172416 MOV #10510, R1
172416 MOV #10540, R0
172416 JSR PC, ZEROBP
172416 MOV #10710, R1
172416 MOV #10740, R0
172416 JSR PC, ZEROBP
172416 JSR STAT
172416 BR

;CLEAR LOOP COUNT
;LDADR
;ADDRESS=0
;GOOD WD
;NO RETRY
;SEL AGRAM
;COUNTER
;WRITE A NULL
;GOOD WD
;NO LOOP UNTIL READY
;LOOP UNTIL ZERO
;COUNTER
;RD
;GOOD RD
;NO RETRY
;FETCH DATA
;CLEAR BITS
;DATA=NULL
;YES
;NO REPORT IT
;LOOP UNTIL ZERO
;LDCTADR
;SEND ADDRESS
;GOOD WRITE
;LOOP UNTIL
;SEL AGRAM
;LOAD A 4
;GOOD WRITE
;NO LOOP UNTIL
;RDBUFADR CMD
;SEL INBUFO
;SET POINTER=0
;FALSE READ DATA
;GOOD READ
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL INBUF1 COMMAND
;POINTER=0
;FALSE READ DATA
;GOOD RD
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;ZERO BUFFER POINTER
;GO CLEAR STATUS

```

FROM NODE 25 MACRO V03.01 27-MAR-79 18:56:53 PAGE 3

```

; INITIALIZE BUFFERS & STATUS
1
2
3 173420 010137 172416 172416 ZEROPB: MOV R1, @#OPREG ;RDBUFADR
4 173424 012737 001400 172416 MOV #1400, @#OPREG ;RD
5 173432 105737 172414 TSTB @#CSR ;GOOD RD
6 173436 100375 BPL -4 ;NO LOOP UNTIL READY
7 173440 105037 172414 CLRB @#CSR ;CLEAR DONE BIT
8 173444 013702 172416 MOV @#IOBUF, R2 ;FETCH POINTER
9 173450 042702 172416 BIC #177400, R2 ;CLEAR MST BYTE
10 173454 010037 172416 MOV R0, @#OPREG ;SEL BUFFER
11 173460 022702 000000 CMP #0, R2 ;POINTER=0
12 173464 001412 BEQ 5$ ;NO FALSE RD
13 173466 012737 172416 MOV #1400, @#OPREG ;GOOD RD
14 173474 105737 172414 TSTB @#CSR ;NO LOOP UNTIL READY
15 173500 100375 BPL -4 ;CLEAR DONE BIT
16 173502 105037 172414 CLRB @#CSR ;POINTER-1
17 173506 005302 DEC R2
18 173510 000763 BR 4$
19 173512 010137 172416 MOV R1, @#OPREG ;RDBUFADR
20 173516 012737 001400 MOV #1400, @#OPREG ;RD
21 173524 105737 172414 TSTB @#CSR ;GOOD RD
22 173530 100375 BPL -4 ;NO LOOP UNTIL READY
23 173532 105037 172414 CLRB @#CSR ;CLEAR DONE BIT
24 173536 013702 172416 MOV @#IOBUF, R2 ;FETCH NEW POINTER
25 173542 042702 177400 BIC #177400, R2 ;CLEAR MST BYTE
26 173546 022702 000000 CMP #0, R2 ;POINTER=0
27 173552 001401 BEQ 6$
28 173554 005204 INC R4
29 173556 010037 172416 MOV R0, @#OPREG ;REPORT ERROR
30 173562 000207 RTS PC ;SEL BUFFER
31 173564 012737 172416 MOV #4352, @#OPREG ;RETURN
32 173572 012737 172416 MOV #1280, @#OPREG ;WCR:RS
33 173600 012737 172416 MOV #1280, @#OPREG ;RS(FALSE)
34 173606 013700 172416 MOV @#OPREG, R0 ;FETCH STATUS
35 173612 042700 177400 BIC #177400, R0 ;CLEAR MST BYTE
36 173616 022700 000000 CMP #0, R0 ;STATUS=0 ?
37 173622 001401 BEQ 7$
38 173624 005204 INC R4
39 173626 022704 CMP #0, R4 ;STATUS BAD REPORT
40 173632 001406 BEQ 8$ ;WERE THERE ERRORS
41 173634 005205 INC R5 ;ERROR +1
42 173636 022705 CMP #5, R5 ;ERROR LIMIT YET
43 173642 001406 BEQ 9$ ;YES RETURN A 1(ERROR)
44 173644 000167 JMP INIT ;NO RESTART VINIT
45 173650 012713 MOV #1, (R3) ;GOOD INIT.
46 173654 000167 JMP SRTLDH ;LIU BAD INIT.
47 173660 012713 MOV #-1, (R3) ;ENABLE LSB
48

```



```

1
2
3 173664 005077 001024 SRTLDR: CLR QMEMPT
4 173670 005077 001022 QPKCNT CLR
5 173674 005277 001016 INC
6 173700 105037 172414 CLR B
7 173704 005737 172414 TST
8 173710 100375 172414 BPL
9 173712 013700 174710 MOV Q#DATA, R0
10 173716 005010 174710 CLR (R0)
11 173720 012706 160500 MOV #160500, SP
12 173724 012737 010400 MOV #4352., Q#OPREG
13 173732 012737 002400 MOV #1280., Q#OPREG
14 173740 013701 172416 MOV Q#IORUF, R1
15 173744 042701 177400 BIC #177400, R1
16
17
18
19 173750 132701 000004 B2$: BITB #BIT02, R1
20 173754 001410 000004 BEQ B3$
21 173756 012702 010410 MOV #4360., R2
22 173762 012703 010440 MOV #4384., R3
23 173766 004767 000144 JSR PC, EMBF
24 173772 004767 000570 JSR PC, NAK
25 173776 132701 000010 B3$: BITB #BIT03, R1
26 174002 001410 000010 BEQ B01$
27 174004 012702 010610 MOV #4488., R2
28 174010 012703 010640 MOV #4512., R3
29 174014 004767 000116 JSR PC, EMBF
30 174020 004767 000542 JSR PC, NAK
31
32
33
34 174024 132701 000001 B01$: BITB #BIT00, R1
35 174030 001410 000001 BEQ B2$
36 174032 012702 010410 MOV #4360., R2
37 174036 012703 010440 MOV #4384., R3
38 174042 012704 000001 MOV #BIT00, R4
39 174046 004767 000026 JSR PC, PACK
40 174052 132701 000002 B2$: BITB #BIT01, R1
41 174056 001712 000002 BEQ LIO
42 174060 012702 010610 MOV #4488., R2
43 174064 012703 010640 MOV #4512., R3
44 174070 012704 000002 MOV #BIT01, R4
45 174074 004767 000000 JSR PC, PACK

MEMORY POINTER
:PACKET COUNTER
:START'S AT 1
:CLEAR CSR
:REQUEST YET
:NO LOOP ON LIO
:ADDRESS OF DATA
:CLEAR DATA
:RESET SP
:WCR : RS(0)
:RD
:CLEAR UNUSED BITS

:OV-FL
:COM=SEL INBUF 0
:COM=RDRUPPNT
:EMPTY BUFFER
:SEND NAK
:OV-FL
:COM=SEL INBUF 1
:COM=RDRUPPNT
:SEND NAK

:BUFFER 0 FULL
:NO CK 1
:YES, COM=RDRUPADR
:COM=SEL INBUF0
:WHICH CRC BIT
:GO FIND PACKET
:BUFFER 1 FULL
:NO LOOP BACK
:YES, COM=RDRUPADR
:COM=SEL INBUF1
:WHICH CRC BIT
:GO FIND PACKET

```


FROM NODE 25 MACRO V03.01 27-MAR-79 18:56:53 PAGE 6

```

1 3 174352 016702 000342      ;PACKET PARSE
2 4 174356 122712 000000      MOV    BUFFER, R2
3 5 174362 001432      CMPB   #0, (R2)
4 6 174364 016703 000326      BEQ    CMEQ
5 7 174370 022713 000144      MOV    PKCNT, R3
6 8 174374 001000      CMF    #100., (R3)
7 9 174376 005013      BNE     1$
8 10 174400 005213      CLR     (R3)
9 11 174402 121312      INC     (R3)
10 12 174404 001401      CMFB   (R3), (R2)
11 13 174406 000207      BEQ    2$
12 14 174410 122762 000001      RTS    PC
13 15 174416 001444      CMPB   #7, 1(R2)
14 16 174420 062702 000002      BEQ    #2, R2
15 17 174424 005213      INC     (R3)
16 18 174426 017703 000262      MOV    QMEMPT, R3
17 19 174432 012704 000100      MOV    #64., R4
18 20 174436 012223      MOV    (R2)+, (R3)+
19 21 174440 077402      SOB     R4, 3$
20 22 174442 010377 000246      MOV    R3, QMEMPT
21 23 174446 000207      RTS    PC
22 24 174450 122762 000001      CMPB   #1, 1(R2)
23 25 174456 001004      BNE     5$
24 26 174460 013703 174712      MOV    QSTATUS, R3
25 27 174464 011310      MOV    (R3), (R0)
26 28 174466 000207      RTS    PC
27 29 174470 122762 000005      CMPB   #5, 1(R2)
28 30 174476 001005      BNE     6$
29 31 174500 013700 000040      MOV    Q#40, R0
30 32 174504 012706 001000      MOV    #1000, SP
31 33 174510 000110      JMP     (R0)
32 34 174512 122762 000006      CMPB   #6, 1(R2)
33 35 174520 001002      BNE     7$
34 36 174522 000167 177136      SRTLDR PC
35 37 174526 000207      RTS    PC
36 38 174530 012704 000200      MOV    #128., R4
37 39 174534 062702 000002      ADD    #2, R2
38 40 174540 005213      INC     (R3)
39 41 174542 105737 177564      Q#YCSR
40 42 174546 100375      BPL     -4
41 43 174550 112237 177566      MOVB   (R2)+, Q#XBUF
42 44 174554 077406      SOB     R4, 11$
43 45 174556 000207      RTS    PC
46

```

```

;BUFFER AREA
;MEMORY OR CONTROL
;CONTROL PACKET
;PACKET COUNTER
;LIMIT
;NO CONTINUE
;RESET POINTER
;NEW PACKET
;RETURN
;TO BE PRINTED
;OFFSET HEADER
;COUNT + 1
;BYTE COUNT
;LOAD MEMORY
;LOOP UNTIL ZERO
;IS IT A REPORT
;NO
;LOCATION OF STATUS
;TRANSFER STATUS
;IS IT A START
;NO
;FETCH START ADDRESS
;RESET USER STACK
;LEAVE PROM TO RAM
;WAS IT A LOAD COMMAND
;NO
;YES GOTO LOAD START
;PRINT 200. BYTES
;OFFSET HEADER
;INC PKCT COUNTER
;PORT READY
;NO WAIT UNTIL
;PRINT A BYTE
;DO ALL 200

```

FROM NODE 25 MACRO V03.01 27-MAR-79 18:56:53 PAGE 7

```

1 2 174560 012705 174670 ACK: MOV #ACKPK, R5
3 4 174564 000402 BR S$
5 174566 012705 174700 NAK: MOV #NAKPK, R5
6 174572 012737 010540 172416 8$: MOV #4448., G#OPREG
7 174600 012504 MOV (R5)+, R4
8 174602 067704 ADD QPKCNT, R4
9 174606 010437 172416 MOV R4, G#OPREG
10 174612 105737 -172414 TSTB G#CSR
11 174616 100375 BPL -4
12 174620 012704 000003 MOV #3, R4
13 174624 012537 172416 9$: MOV (R5)+, G#OPREG
14 174630 105737 172414 TSTB G#CSR
15 174634 100375 BPL -4
16 174636 077406 SOB R4, S$
17 174640 012737 010420 172416 MOV #4368., G#OPREG
18 174646 012737 004421 172416 MOV #4421, G#OPREG
19 174654 105737 -172414 TSTB G#CSR
20 174660 100375 BPL -4
21 174662 105037 172414 CLR PC
22 174666 000207 RTS
23
24 174670 004400 ACKPK: .WORD 4400
25 174672 004402 .WORD 4402
26 174674 004403 .WORD 4403
27 174676 004405 .WORD 4405
28 174700 004400 NAKPK: .WORD 4400
29 174702 004403 .WORD 4403
30 174704 004405 .WORD 4405
31 174706 004400 DATA: .WORD 160400
32 174710 160402 STATUS: .WORD 160402
33 174712 160404 MEMPT: .WORD 160404
34 174716 160406 PKCNT: .WORD 160406
35 174720 160000 BUFFER: .WORD 160000
36 174722 004407 NODE: .WORD 4407
37
38
39
40 .END START

```

```

;ADDRESS OF ACK
;ADDRESS OF NAK
;SEL OUTBFO
;FETCH HEADER
;ADD PACKET COUNT
;WRITE IT
;GOOD WD
;NO LOOP
;BYTE COUNT
;WD
;GOOD WD
;NO LOOP
;YES LOOP TIL 0
;MODSTAT
;SET BITS
;GOOD WD
;LOOP UNTIL

```


PROM NODE 25 MACRO V03.01 27-MAR-79 18:56:53 PAGE 7-1
SYMBOL TABLE

ACK	174560	DATA	174710	MEMPT	174714	SRTLDR	173664
ACKPK	174570	EMBF	174136	NAK	174566	START	173000
BAR	= 172410	INBUF0	173276	NAKPK	174700	STAT	173564
BIT00	= 000001	INBUF1	173332	NODE	174722	STATUS	174712
BIT01	= 000002	INIT	173070	OPREG	= 172416	STRAM	173222
BIT02	= 000004	INITLS	173014	OTBUF0	173366	WCR	= 172412
BIT03	= 000010	IOBUF	= 172416	OTBUF1	173402	XBUF	= 177566
BIT04	= 000020	LIO	173704	PACK	174100	XCSR	= 177564
BIT05	= 000040	LOAD	174352	PKCNT	174716	ZEROBP	173420
BIT06	= 000100						

. ABS. 174724 000
000000 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
.DK:FROM25-DL:PROM25

PROM NODE 26 MACRO V03.01 27-MAR-79 18:57:45 PAGE 1

```

1
2
3 000000
4 173000
5
6
7 172410
8 172412
9 172414
10 172416
11 172416
12 177560
13 177562
14 177564
15 177566
16 100000
17 040000
18 000400
19 000200
20 000100
21 000040
22 000020
23 000010
24 000004
25 000002
26 000001
27
28
29 173000 000005
30 173002 012700 000340
31 173006 106400
32 173010 012706 160500
33
34
35
36 173014 012737 010420 172416 172416
37 173022 012737 004414 172416
38 173030 105737 172414
39 173034 100375 172414
40 173036 105037 172414
41 173042 012737 004540 172416
42 173050 105737 172414
43 173054 100375 172414
44 173056 105037 172414
45 173062 013703 175006
46 173066 005005

.TITLE PROM NODE 26
.IDENT /V2.0/
.ASECT
.=173000

BAR= 172410
WCR= 172412
CSR= 172414
IOBUF= 172416
OPREG= 172416
SRCR= 177560
SRBUF= 177562
SICR= 177564
SIUF= 177566
BIT15= 100000
BIT14= 40000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1

;RESET BUS
;PRI=7
;CPU PRI
;RESET STACK

;MOD STAT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL

;RESET SWITCHES
;GOOD WD
;LOOP UNTIL

;LO CATION OF STATUS
;CLEAR ERROR COUNT

```

```

;RESET BUS
;PRI=7
;CPU PRI
;RESET STACK

```

```

;MOD STAT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL

;RESET SWITCHES
;GOOD WD
;LOOP UNTIL

```

```

;LO CATION OF STATUS
;CLEAR ERROR COUNT

```

```

1  ;INITIALIZE LIU
2
3  173070 005004
4  173072 012737 010402 172416
5  173100 012737 004400 172416
6  173106 105737 172414
7  173112 100375
8  173114 105037 172414
9  173120 012737 010401 172416
10 173126 012700 000400
11 173132 012737 004407 172416
12 173140 105737 172414
13 173144 100375
14 173146 105037 172414
15 173152 077011
16 173154 012700
17 173160 012737 001400
18 173166 105737 172414
19 173172 100375
20 173174 105037 172414
21 173200 013701 172416
22 173204 042701 177760
23 173210 022701 000007
24 173214 001401
25 173216 005204
26 173220 077021
27 173222 012737
28 173230 013737 175016
29 173236 105737 172414
30 173242 100375
31 173244 105037 172414
32 173250 012737 010410
33 173256 012737 004404 172416
34 173264 105737 172414
35 173270 100375
36 173272 105037 172414
37 173276 012701 010410
38 173302 012700 010440
39 173306 004767 000106
40 173312 012737 001400 172416
41 173320 105737 172414
42 173324 100375
43 173326 105037 172414
44 173332 012701 010610
45 173336 012700 010640
46 173342 004767 000052
47 173346 012737 001400 172416
48 173354 105737 172414
49 173360 100375
50 173362 105037 172414
51 173366 012701 010510
52 173372 012700 010540
53 173376 004767 000016
54 173402 012701 010710
55 173406 012700 010740
56 173412 004767 000002
57 173416 000462

;CLEAR LOOP COUNT
;LDADR
;ADDRESS=0
;GOOD WD
;NO RETRY
;SEL ACRAM
;COUNTER
;WRITE A NULL
;GOOD WD
;NO LOOP UNTIL READY
;LOOP UNTIL ZERO
;COUNTER
;RD
;GOOD RD
;NO RETRY
;FETCH DATA
;CLEAR BITS
;DATA=NULL
;YES
;NO REPORT IT
;LOOP UNTIL ZERO
;LDCTADR
;SEND ADDRESS
;GOOD WRITE
;LOOP UNTIL
;SEL ACRAM
;LOAD A 4
;GOOD WRITE
;NO LOOP UNTIL
;RDBUFADR CMD
;SEL INBUFO
;SET POINTER=0
;FALSE READ DATA
;GOOD READ
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL INBUF1 COMMAND
;POINTER=0
;FALSE READ DATA
;GOOD RD
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;SEL OUTBUF0 COMMAND
;ZERO BUFFER POINTER
;GO CLEAR STATUS

```

FROM NODE 26 MACRO V03.01 27-MAR-79 18:57:45 PAGE 3

```

1 2 3 173420 010137 172416 172416 172416 172416 172416 172416 172416 172416
2 4 173424 012737 001400 172416 172416 172416 172416 172416 172416 172416
3 5 173432 105737 172414 172414 172414 172414 172414 172414 172414 172414
4 6 173436 100375 172414 172414 172414 172414 172414 172414 172414 172414
5 7 173440 105037 172414 172414 172414 172414 172414 172414 172414 172414
6 8 173444 013702 172416 172416 172416 172416 172416 172416 172416 172416
7 9 173450 042702 177400 177400 177400 177400 177400 177400 177400 177400
8 10 173454 010037 172416 172416 172416 172416 172416 172416 172416 172416
9 11 173460 022702 000000 000000 000000 000000 000000 000000 000000 000000
10 12 173464 001412 001412 001412 001412 001412 001412 001412 001412 001412
11 13 173466 012737 001400 172416 172416 172416 172416 172416 172416 172416
12 14 173474 105737 172414 172414 172414 172414 172414 172414 172414 172414
13 15 173500 100375 172414 172414 172414 172414 172414 172414 172414 172414
14 16 173502 105037 172414 172414 172414 172414 172414 172414 172414 172414
15 17 173506 005302 172414 172414 172414 172414 172414 172414 172414 172414
16 18 173510 000763 172416 172416 172416 172416 172416 172416 172416 172416
17 19 173512 010137 172416 172416 172416 172416 172416 172416 172416 172416
18 20 173516 012737 001400 172416 172416 172416 172416 172416 172416 172416
19 21 173524 105737 172414 172414 172414 172414 172414 172414 172414 172414
20 22 173530 100375 172414 172414 172414 172414 172414 172414 172414 172414
21 23 173532 105037 172414 172414 172414 172414 172414 172414 172414 172414
22 24 173536 013702 172416 172416 172416 172416 172416 172416 172416 172416
23 25 173542 042702 177400 177400 177400 177400 177400 177400 177400 177400
24 26 173546 022702 000000 000000 000000 000000 000000 000000 000000 000000
25 27 173552 001401 001401 001401 001401 001401 001401 001401 001401 001401
26 28 173554 005204 172416 172416 172416 172416 172416 172416 172416 172416
27 29 173556 010037 172416 172416 172416 172416 172416 172416 172416 172416
28 30 173562 000207 172416 172416 172416 172416 172416 172416 172416 172416
29 31 173564 012737 010400 172416 172416 172416 172416 172416 172416 172416
30 32 173572 012737 002400 172416 172416 172416 172416 172416 172416 172416
31 33 173600 012737 002400 172416 172416 172416 172416 172416 172416 172416
32 34 173606 013700 172416 172416 172416 172416 172416 172416 172416 172416
33 35 173612 042700 177400 177400 177400 177400 177400 177400 177400 177400
34 36 173616 022700 000000 000000 000000 000000 000000 000000 000000 000000
35 37 173622 001401 001401 001401 001401 001401 001401 001401 001401 001401
36 38 173624 005204 172416 172416 172416 172416 172416 172416 172416 172416
37 39 173626 022704 000000 000000 000000 000000 000000 000000 000000 000000
38 40 173632 001406 001406 001406 001406 001406 001406 001406 001406 001406
39 41 173634 005205 005205 005205 005205 005205 005205 005205 005205 005205
40 42 173636 022705 000000 000000 000000 000000 000000 000000 000000 000000
41 43 173642 001406 001406 001406 001406 001406 001406 001406 001406 001406
42 44 173644 000167 177220 177220 177220 177220 177220 177220 177220 177220
43 45 173650 012713 000001 000001 000001 000001 000001 000001 000001 000001
44 46 173654 000167 000004 000004 000004 000004 000004 000004 000004 000004
45 47 173660 012713 177777 177777 177777 177777 177777 177777 177777 177777
46 48

```

INITIALIZE BUFFERS & STATUS

```

;RDEUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH POINTER
;CLEAR MST BYTE
;SEL BUFFER
;POINTER=0
;NO FALSE RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;POINTER-1
;RDEUFADR
;RD
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;FETCH NEW POINTER
;CLEAR MST BYTE
;POINTER=0
;REPORT ERROR
;SEL BUFFER
;RETURN
;WCR:RS
;RS(FALSE)
;RS
;FETCH STATUS
;CLEAR-MST BYTE
;STATUS=0 ?
;STATUS-BAD REPORT
;WERE THERE ERRORS
;NO GOOD INIT
;ERROR +1
;ERROR LIMIT YET
;YES RETURN A 1(ERROR)
;NO RESTART VINIT
;GOOD INIT.
;LIU BAD INIT.

```



```

1 2
3 173664 005077 001120
4 173670 005077 001116
5 173674 005277 001112
6 173700 105037 172414
7 173704 005237 172414
8 173710 100375
9 173712 013700 175004
10 173716 005010
11 173720 012706 160500
12 173724 012737 104000
13 173732 012737 002400 172416
14 173740 013701 172416
15 173744 042701 177400
16
17
18
19 173750 132701 000004
20 173754 001410
21 173756 012702 010410
22 173762 012703 010440
23 173766 004767 000144
24 173772 004767 000664
25 173776 132701 000010
26 174002 001410
27 174004 012702 010610
28 174010 012703 010640
29 174014 004767 000116
30 174020 004767 000636
31
32
33
34 174024 132701 000001
35 174030 001410
36 174032 012702 010410
37 174036 012703 010440
38 174042 012704 000001
39 174046 004767 000026
40 174052 132701 000002
41 174056 001712
42 174060 012702 010610
43 174064 012703 010640
44 174070 012704 000002
45 174074 004767 000000

;LOADER START
SRTLDR: CLR QMEMPT
          CLR QPKCNT
          INC QPKCNT
          CLRB Q#CSR
          TST Q#CSR
          BPL -4
          MOV Q#DATA, R0
          CLRB (R0)
          MOV #160500, SP
          MOV #4352., Q#OPREG
          MOV #1280., Q#OPREG
          MOV Q#IGBUF, R1
          BIC #177400, R1

;BUFFER OVERFLOW
B2$: BITB #BIT02, R1
    BEQ B3$
    MOV #4360., R2
    MOV #4384., R3
    JSR PC, EMBF
    JSR PC, NAK
    BITB #BIT03, R1
    BEQ B01$
    MOV #4488., R2
    MOV #4512., R3
    JSR PC, EMBF
    JSR PC, NAK

B3$: BITB
    BEQ
    MOV
    MOV
    JSR
    JSR

;BUFFERS FULL
B01$: BITB #BIT00, R1
    BEQ 2$
    MOV #4360., R2
    MOV #4384., R3
    MOV #BIT00, R4
    JSR PC, EMBF
    JSR PC, NAK
    BITB #BIT01, R1
    BEQ LIO
    MOV #4488., R2
    MOV #4512., R3
    MOV #BIT01, R4
    JSR PC, PACK

;MEMORY POINTER
;PACKET COUNTER
;START'S AT 1
;CLEAR CSR
;REQUEST YET
;NO LOOP ON LIO
;ADDRESS OF DATA
;CLEAR DATA
;RESET SP
;WCR : RS(0)
;RD
;CLEAR UNUSED BITS

;OV-FL
;COM=SEL INBUF 0
;COM=RDBUFFNT
;EMPTY BUFFER
;SEND NAK
;OV-FL
;COM=SEL INBUF 1.
;COM=RDBUFFNT
;SEND NAK

;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUPADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO FIND PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUPADR
;COM=SEL INBUF1
;WHICH CRC BIT
;GO FIND PACKET

```

PROM NODE 26 MACRO V03.01 27-MAR-79 18:57:45 PAGE 5

[illegible]


```

1
2
3 174530 122762 000010 000001 8$:      ;SIG LOADER
4 174536 001016      BNE #E., 1(R2)
5 174540 105737 177560      TSTB G#SRCR
6 174544 100402      BMI 12$
7 174546 000167      JMP SRTLDR
8 174552 113702 177562      MOV B #SRBUF, R2
9 174556 042702 177400      BIC #177400, R2
10 174562 122702 000122      CMPB #122, R2
11 174566 001757      BEQ 7$
12 174570 000167 177070      JMP SRTLDR
13 174574 122762 000011 000001 9$:      ;SIMULATE A NO RESPONSE
14 174602 001351      CMPB #9., 1(R2)
15 174604 105737 177564      TSTB G#SICR
16 174610 100375      BPL -4
17 174612 112737 000114 177566      MOV B #114, G#SIBUF
18 174620 000742      BR 7$
19 174622 062702 000002      ADD #2, R2
20 174626 005213      INC (R3)
21 174630 012704 000200      MOV #128., R4
22 174634 105737 177564      TSTB G#SICR
23 174640 100375      BPL -4
24 174642 112237 177566      MOV B (R2)+, G#SIBUF
25 174646 000240      NOP
26 174650 077407      SOB R4, 11$
27 174652 000207      RTS PC
28

```


PROG NODE 26 MACRO V03.01 27-MAR-79 18:57:45 PAGE 8

```

1
2
3
4 174654 012705 174764 ACK: MOV #ACKPK, R5
5 174660 000402 BR 1$
6 174662 012705 174774 NAK: MOV #NAKPK, R5
7 174666 012737 010540 172416 1$ MOV #4448., G#OPREG
8 174674 012504 ADD (R5)+, R4
9 174676 067704 000110 QPKCNT, R4
10 174702 010437 172416 MOV R4, G#OPREG
11 174706 105737 172414 TSTB G#CSR
12 174712 100375 BPL -4
13 174714 012704 000003 MOV #3, R4
14 174720 012537 172416 MOV (R5)+, G#OPREG
15 174724 105737 172414 TSTB G#CSR
16 174730 100375 BPL -4
17 174732 077406 SOB R4
18 174734 012737 010420 172416 MOV #4368., G#OPREG
19 174742 012737 004421 172416 MOV #4421, G#OPREG
20 174750 105737 172414 TSTB G#CSR
21 174754 100375 BPL -4
22 174756 105037 172414 CLR B G#CSR
23 174762 000207 RTS PC
24
25
26 174764 004400 ACKPK: .WORD 4400
27 174766 004402 .WORD 4402
28 174770 004403 .WORD 4403
29 174772 004405 .WORD 4405
30 174774 004400 NAKPK: .WORD 4400
31 174776 004403 .WORD 4403
32 175000 004403 .WORD 4403
33 175002 004405 .WORD 4405
34 175004 160400 DATA: .WORD 160400
35 175006 160402 STATUS: .WORD 160402
36 175010 160404 MEMPT: .WORD 160404
37 175012 160406 PICNT: .WORD 160406
38 175014 160000 BUFFER: .WORD 160000
39 175016 004410 NODE: .WORD 4410
40
41 173000 .END START

```

```

;ADDRESS OF ACK
;ADDRESS OF NAK
;SEL OUTBUF0
;FETCH HEADER
;ADD PACKET COUNT
;WRITE IT
;GOOD WD
;NO LOOP
;BYTE COUNT
;WD
;GOOD WD
;YES LOOP TIL 0
;MODSTAT
;SET BITS
;GOOD WD
;LOOP UNTIL

```

FROM NODE 26 MACRO V03.01 27-MAR-79 18:57:45 PAGE 8-1
SYMBOL TABLE

ACK	174654	BIT07 = 000200	DATA	175004	NAK	174662	SRTLDR	173664
ACKPK	174764	BIT08 = 000400	EMBP	174136	NAKPK	174774	START	173000
BAR	172410	BIT14 = 040000	INBUF0	173276	MODE	175016	STAT	173564
BIT00 = 000001		BIT15 = 100000	INBUF1	173332	OPREG =	172416	STATUS	175006
BIT01 = 000002		BUFFER	INIT	173070	OTBUF0	173366	STRAM	173222
BIT02 = 000004		B01\$	INITLS	173014	OTBUF1	173402	SXBUF =	177566
BIT03 = 000010		B2\$	IOBUF =	172416	PACK	174100	SICSR =	177564
BIT04 = 000020		B3\$	LIO	173704	PKCNT	175012	WCR	172412
BIT05 = 000040		CMEQ	LOAD	174352	SRBUF =	177562	ZEROBP	173420
BIT06 = 000100		CSR = 172414	MEMPT	175010	SRCR =	177560		

. ABS. 175020 000
000000 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
,DE:PR0M26-DI:PR0M26

FROM NODE 27 MACRO V03.01 27-MAR-79 18:58:44 PAGE 1

```

1 2 .TITLE FROM NODE 27
3 3 .IDENT /V2.0/
4 4 .ASECT
5 5 .-173000
6
7 BAR= 172410
8 WCR= 172412
9 CSR= 172414
10 IOBUF= 172416
11 OPREC= 172416
12 BIT15= 1000000
13 BIT14= 400000
14 BIT08= 400
15 BIT07= 200
16 BIT06= 100
17 BIT05= 40
18 BIT04= 20
19 BIT03= 10
20 BIT02= 4
21 BIT01= 2
22 BIT00= 1
23
24
25 173000 000005
26 173002 012700 000340
27 173006 106400
28 173010 012706 160500
29
30
31
32 173014 012737 010420 172416
33 173022 012737 004414 172416
34 173030 105737 172414
35 173034 100375
36 173036 105037 172414
37 173042 012737 004540 172416
38 173050 105737 172414
39 173054 100375
40 173056 105037 172414
41 173062 013703 174652
42 173066 005005

;RESET BUS
;PRI=7
;CPU PRI
;RESET STACK

;MOD STAT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL

;RESET SWITCHES
;GOOD WD
;LOOP UNTIL

;LC CATION OF STATUS
;CLEAR ERROR COUNT

```

```

1 2
3 173070 005004
4 173072 012737
5 173100 012737 004400
6 173106 105737 172414
7 173112 100375
8 173114 105037 172414
9 173120 012737 010401
10 173126 012700 004400
11 173132 012737 004407
12 173140 105737 172414
13 173144 100375 172414
14 173146 105037 172414
15 173152 077011 000400
16 173154 012700 001400
17 173160 012737 172414
18 173166 105737 172414
19 173172 100375
20 173174 105037 172414
21 173200 013701 172416
22 173204 042701 177760
23 173210 022701 000007
24 173214 001401
25 173216 005204
26 173220 077821
27 173222 012737
28 173230 013737 174662
29 173236 105737 172414
30 173242 100375
31 173244 105037 172414
32 173250 012737 010401
33 173256 012737 004404
34 173264 105737 172414
35 173270 100375
36 173272 105037 172414
37 173276 012701 010410
38 173302 012700 010440
39 173306 004767 000106
40 173312 012737 001400
41 173320 105737 172414
42 173324 100375
43 173326 105037 172414
44 173332 012701 010610
45 173336 012700 010640
46 173342 004767 000052
47 173346 012737 001400
48 173354 105737 172414
49 173360 100375
50 173362 105037 172414
51 173366 012701 010510
52 173372 012700 010540
53 173376 004767 000016
54 173402 012701 010710
55 173406 012700 010740
56 173412 004767 000002
57 173416 000462

;INITIALIZE LIU
INIT: CLR R4
MOV #4354., @#OPREG
MOV #2304., @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
MOV #4353., @#OPREG
MOV #256., R0
MOV #2311., @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
SOB R0, 1$
MOV #256., R0
MOV #768., @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
MOV @#IOBUF, R1
BIC #177760, R1
CMF #7, R1
BEQ 3$
INC R4
SOB R0, 2$
MOV #4354., @#OPREG
MOV @#NODE, @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
MOV #4353., @#OPREG
MOV #4404, @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
MOV #10410, R1
MOV #10440, R0
JSR PC, ZEROBP
MOV #1400, @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
MOV #10610, R1
MOV #10640, R0
JSR PC, ZEROBP
MOV #1400, @#OPREG
TSB @#CSR
BPL -4
CLRB @#CSR
MOV #10510, R1
MOV #10540, R0
JSR PC, ZEROBP
MOV #10710, R1
MOV #10740, R0
JSR PC, ZEROBP
BR STAT

;CLEAR LOOP COUNT
;LDADR
;ADDRESS=0
;GOOD WD
;NO RETRY
;SEL ACRAM
;COUNTER
;WRITE A NULL
;GOOD WD
;NO LOOP UNTIL READY
;LOOP UNTIL ZERO
;COUNTER
;RD
;GOOD RD
;NO RETRY
;FETCH DATA
;CLEAR BITS
;DATA=NULL
;YES
;NO REPORT IT
;LOOP UNTIL ZERO
;LDCTADR
;SEND ADDRESS
;GOOD WRITE
;LOOP UNTIL
;SEL ACRAM
;LOAD A 4
;GOOD WRITE
;NO LOOP UNTIL
;RDBUFADR CMD
;SEL INBUFO
;SET POINTER=0
;FALSE READ DATA
;GOOD READ
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL INBUF1 COMMAND
;POINTER=0
;FALSE READ DATA
;GOOD RD
;NO RETRY
;CLEAR DONE BIT
;RDBUFADR COMMAND
;SEL OUTBUF0 COMMAND
;ZERO BUFFER POINTER
;GO CLEAR STATUS

```


PROM NODE 27 MACRO V03.01 27-MAR-79 18:58:44 PAGE 3

[illegible]

```

1
2
3 173664 005077 000764 SRTLDR: CLR QMEMPT
4 173670 005077 000762 CLR QPKCNT
5 173674 005277 000756 INC QPKCNT
6 173700 105037 172414 CLR QCSR
7 173704 005737 172414 LIO: TST QCSR
8 173710 100375 BPL -4
9 173712 013700 174650 MOV Q#DATA, R0
10 173716 005010 CLR (R0)
11 173720 012706 MOV #160500, SP
12 173724 012737 MOV #4352., Q#OPREG
13 173732 012737 MOV #1280., Q#OPREG
14 173740 013701 MOV Q#IOBUF, R1
15 173744 042701 BIC #177400, R1
16
17
18
19 173750 132701 B2$: BITB #BIT02, R1
20 173754 001410 BEQ B3$
21 173756 012702 MOV #4360., R2
22 173762 012703 MOV #4384., R3
23 173766 004767 JSR PC, EMBF
24 173772 004767 JSR PC, NAK
25 173776 132701 B3$: BITB #BIT03, R1
26 174002 001410 BEQ B01$
27 174004 012702 MOV #4488., R2
28 174010 012703 MOV #4512., R3
29 174014 004767 JSR PC, EMBF
30 174020 004767 JSR PC, NAK
31
32
33
34 174024 132701 B01$: BITB #BIT00, R1
35 174030 001410 BEQ B1$
36 174032 012702 MOV #4360., R2
37 174036 012703 MOV #4384., R3
38 174042 012704 MOV #BIT00, R4
39 174046 004767 JSR PC, PACK
40 174052 132701 B2$: BITB #BIT01, R1
41 174056 001712 BEQ LIO
42 174060 012702 MOV #4488., R2
43 174064 012703 MOV #4512., R3
44 174070 012704 MOV #BIT01, R4
45 174074 004767 JSR PC, PACK

;MEMORY POINTER
;PACKET COUNTER
;START'S AT 1
;CLEAR CSR
;REQUEST YET
;NO LOOP ON LIO
;ADDRESS OF DATA
;CLEAR DATA
;RESET SP
;WCR : RS(0)
;RD
;CLEAR UNUSED BITS

;OV-FL
;COM=SEL INBUF 0
;COM=RDBUFPNT
;EMPTY BUFFER
;SEND NAK
;OV-FL
;COM=SEL INBUF 1
;COM=RDBUFPNT
;SEND NAK

;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUFADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO FIND PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUFADR
;COM=SEL INBUF1
;WHICH CRC BIT
;GO FIND PACKET

```

FROM NODE 27 MACRO V03.01 27-MAR-79 18:58:44 PAGE 5

```

1
2
3 174100 005010
4 174102 004767 000030
5 174106 005710
6 174110 100003
7 174112 004767 000410
8 174116 000207
9 174120 004767 000226
10 174124 005710
11 174126 100771
12 174130 004767 000364
13 174134 000207
14 174136 012737 010600 172416
15 174144 012737 002400 172416
16 174152 013705 172416
17 174156 130405
18 174160 001002
19 174162 012710 177777
20 174166 012737 160000
21 174174 010237 172416
22 174200 012737 001400 172416
23 174206 105737 172414
24 174212 100375
25 174214 105037 172414
26 174220 013704 172416
27 174224 042704 177400
28 174230 022704 000004
29 174234 001405
30 174236 022704 000204
31 174242 001402
32 174244 012710 177777
33 174250 005404
34 174252 010437 172412
35 174266 010337 172416
36 174262 012737 001400 172416
37 174270 105737 172414
38 174274 100375
39 174276 105037 172414
40 174302 012737 021000 172416
41 174310 000240
42 174312 105737 172414
43 174316 100402
44 174320 012710 177777
45 174324 105037 172414
46 174330 012737 004400 172416
47 174336 105737 172414
48 174342 100375
49 174344 105037 172414
50 174350 000207
51

```

UNLOAD BUFFERS

```

CLR (R0)
JSR PC, EMBF
TST (R0)
BPL 4$
JSR PC, NAK
RTS PC, LOAD
TST (R0)
BMI 3$
JSR PC, ACK
RTS PC,
MOV #4480., Q#OPREG
MOV #1280., Q#OPREG
MOV Q#IOBUF, R5
BITB R4, R5
BNE 5$
MOV #-1., (R0)
MOV #160000, Q#BAR
MOV R2, Q#OPREG
MOV #768., Q#OPREG
TSTB Q#CSR
BPL -4
Q#CSR
Q#IOBUF, R4
BIC #177400, R4
CMP #4., R4
BEQ 6$
CMP #132., R4
BEQ 6$
MOV #-1., (R0)
R4, Q#WCR
R3, Q#OPREG
MOV #768., Q#OPREG
TSTB Q#CSR
BPL -4
Q#CSR
Q#CSR
#8704., Q#OPREG
NOP
TSTB Q#CSR
BMI 7$
MOV #-1., (R0)
CLR Q#CSR
MOV #2304., Q#OPREG
TSTB Q#CSR
BPL -4
CLR Q#CSR
RTS PC
.ENABLE LSB

```

RS (CRC) -

```

;RS CMD
;FETCH STATUS
;CRC OK
;YES CONTINUE
;FLAG BAD CRC
;ADDRESS TO WRITE
;RDBUFADR
;HEAD POINTER
;GOOD RD
;NO LOOP UNTIL READY
;CLEAR DONE BIT
;HEAD POINTER
;CLEAR UNUSED BITS
;CONTROL PACKET
;YES UNLOAD
;MEMORY IMAGE UNLOAD

```

;BAD UNLOAD

```

;2 S COMP
;BYTE COUNT
;SEL BUFFER
;FALSE RD
;GOOD RD
;NO LOOP UNTIL READY

```

;FIRE DMA RD

```

;DELAY
;GOOD DMA
;YES
;BAD DMA XFER
;CLEAR CSR
;RESET POINTER TO 255
;GOOD WD
;NO RETRY
;CLEAR DONE BIT

```


PROM NODE 27 MACRO V03.01 27-MAR-79 18:58:44 PAGE 6

[illegible]

PROM NODE 27 MACRO V03.01 27-MAR-79 18:58:44 PAGE 7

```

1
2
3 174520 012705 174630      ACK:      MOV  #ACKPK, R5      ;ACK/NAK ROUTINE
4 174524 000402      BR      S$
5 174526 012705      MOV  #NAKPK, R5      ;ADDRESS OF NAK
6 174532 012737 172416      MOV  #4448., G#OPREG      ;SEL OUTBPO
7 174540 012504      MOV  (R5)+, R4      ;FETCH HEADER
8 174542 067704      ADD  QPKCNT, R4      ;ADD PACKET COUNT
9 174546 010437 172416      MOV  R4, G#OPREG      ;WRITE IT
10 174552 105737 172414      TSTB  G#CSR      ;GOOD WD
11 174556 100375      BPL  -4      ;NO LOOP
12 174560 012704      MOV  #3, R4      ;BYTE COUNT
13 174564 012537 172416      MOV  (R5)+, G#OPREG      ;WD
14 174570 105737 172414      TSTB  G#CSR      ;GOOD WD
15 174574 100375      BPL  -4      ;NO LOOP
16 174576 077406      SOB  R4, 9$      ;YES LOOP TIL 0
17 174600 012737      MOV  #4368., G#OPREG      ;MODSTAT
18 174606 012737 172416      MOV  #4421, G#OPREG      ;SET BITS
19 174614 105737 172414      TSTB  G#CSR      ;GOOD WD
20 174620 100375      BPL  -4      ;LOOP UNTIL
21 174622 105037 172414      CLRB  G#CSR
22 174626 000207      RTS  PC
23
24
25 174630 004400      ACKPK:  .WORD 4400
26 174632 004402      .WORD 4402
27 174634 004403      .WORD 4403
28 174636 004405      .WORD 4405
29 174640 004400      .WORD 4400
30 174642 004403      .WORD 4403
31 174644 004403      .WORD 4403
32 174646 004405      .WORD 4405
33 174650 160400      DATA:  .WORD 160400
34 174652 160402      STATUS:  .WORD 160402
35 174654 160404      MEMPT:  .WORD 160404
36 174656 160406      PKCNT:  .WORD 160406
37 174660 160000      BUFFER:  .WORD 160000
38 174662 004411      NODE:  .WORD 4411
39
40      .END      START

```

```

;ADDRESS OF ACK
;ADDRESS OF NAK
;SEL OUTBPO
;FETCH HEADER
;ADD PACKET COUNT
;WRITE IT
;GOOD WD
;NO LOOP
;BYTE COUNT
;WD
;GOOD WD
;NO LOOP
;YES LOOP TIL 0
;MODSTAT
;SET BITS
;GOOD WD
;LOOP UNTIL

```

PROM NODE 27 MACRO V03.01 27-MAR-79 18:58:44 PAGE 7-1
 SYMBOL TABLE

ACK	174520	BIT06 = 000100	CMEQ	174440	PACK	174100
ACKPK	174630	BIT07 = 000200	CSR =	172414	PICNT	174656
BAR	= 172410	BIT08 = 000400	DATA	174650	SHTLDR	173664
BIT00 = 000001		BIT14 = 040000	EMBF	174136	START	173000
BIT01 = 000002		BIT15 = 100000	INBUF0	173276	STAT	173564
BIT02 = 000004		BUFFER	INBUF1	173332	STATUS	174652
BIT03 = 000010		B01↓	INIT	173070	STRAM	173222
BIT04 = 000020		B2↓	INITLS	173014	WCR =	172412
BIT05 = 000040		B3↓	IOBUF =	172416	ZEROBP	173420

. ABS. 174664 000

000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)

DYNAMIC MEMORY AVAILABLE FOR 51 PAGES

.DI:PROM27-DI:PROM27

PROM NODE 28 MACRO V03.01 27-MAR-79 18:59:38 PAGE 1

.TITLE PROM NODE 28
.IDENT /V2.0/
.ASECT
.=173000

1			
2			
3	000000	173000	
4			
5			
6			
7			
8		172410	
9		172412	
10		172414	
11		172416	
12		172416	
13		100000	
14		040000	
15		000400	
16		000200	
17		000100	
18		000040	
19		000020	
20		000010	
21		000004	
22		000002	
23		000001	
24	173000	000005	
25	173002	012700	000340
26	173006	106400	
27	173010	012706	160500
28			
29			
30			
31			
32	173014	012737	010420
33	173022	012737	004414
34	173030	105737	172414
35	173034	100375	
36	173036	105037	172414
37	173042	012737	004540
38	173050	105737	172414
39	173054	100375	
40	173056	105037	172414
41	173062	013703	174652
42	173066	005005	

BAR= 172410
WCR= 172412
CSR= 172414
IORUF= 172416
OPREG= 172416
BIT15= 100000
BIT14= 40000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1

;RESET BUS
;PRI=7
;CPU PRI
;RESET STACK

START: RESET MOV #340, R0
 MTS R0
 MOV #160500, SP
; INITIALIZE LINE SWITCHES

;MOD STAT
;SET SWITCHES
;GOOD WD
;LOOP UNTIL

;RESET SWITCHES
;GOOD WD
;LOOP UNTIL

;LC CATION OF STATUS
;CLEAR ERROR COUNT

INITIS: MOV #4368., Q#OPREG
 MOV #4414, Q#OPREG
 TSTB Q#CSR
 BPL -4
 CLRB Q#CSR
 MOV #4540, Q#OPREG
 TSTB Q#CSR
 BPL -4
 CLRB Q#CSR
 MOV Q#STATUS, R3
 CLR R5


```

1 1
2 2
3 3 173070 005004
4 4 173072 012737 172416 CLR R4
5 5 173100 012737 172416 MOV #4354., G#OPREG
6 6 173106 012737 172416 MOV #2304., G#OPREG
7 7 173112 105737 172414 TSTB G#CSR
8 8 173114 105375 172414 BPL -4
9 9 173120 012737 172416 CLRB G#CSR
10 10 173126 012700 172416 MOV #4353., G#OPREG
11 11 173132 012737 172416 MOV #256., R0
12 12 173140 012737 172416 MOV #2311., G#OPREG
13 13 173144 105737 172414 TSTB G#CSR
14 14 173146 105375 172414 BPL -4
15 15 173152 077011 172414 CLRB G#CSR
16 16 173154 012700 172416 SOB R0, 1$
17 17 173160 012737 172416 MOV #256., R0
18 18 173166 105737 172414 MOV #768., G#OPREG
19 19 173172 105375 172414 TSTB G#CSR
20 20 173174 105037 172414 BPL -4
21 21 173200 013701 172416 CLRB G#CSR
22 22 173204 042701 177760 MOV #177760, R1
23 23 173210 022701 000007 BIC #177760, R1
24 24 173214 001401 BEQ #7, R1
25 25 173216 005204 INC R4
26 26 173220 077021 SOB R0, 2$
27 27 173222 012737 172416 MOV #4354., G#OPREG
28 28 173230 013737 174662 STRAM: G#NODE, G#OPREG
29 29 173236 105737 172414 TSTB G#CSR
30 30 173242 105375 172414 BPL -4
31 31 173244 105037 172414 CLRB G#CSR
32 32 173250 012737 172416 MOV #4353., G#OPREG
33 33 173256 012737 172416 MOV #4404, G#OPREG
34 34 173264 105737 172414 TSTB G#CSR
35 35 173270 105375 172414 BPL -4
36 36 173272 105037 172414 CLRB G#CSR
37 37 173276 012700 172416 INBUF0: MOV #10410, R1
38 38 173302 012700 010440 MOV #10440, R0
39 39 173306 004767 000106 JSR PC, ZEROBP
40 40 173312 012737 172416 MOV #1400, G#OPREG
41 41 173320 105737 172414 TSTB G#CSR
42 42 173324 105375 172414 BPL -4
43 43 173326 105037 172414 CLRB G#CSR
44 44 173332 012701 010610 MOV #10610, R1
45 45 173336 012700 010640 MOV #10640, R0
46 46 173342 004767 000052 JSR PC, ZEROBP
47 47 173346 012737 172416 INBUF1: MOV #1400, G#OPREG
48 48 173354 105737 172414 TSTB G#CSR
49 49 173360 105375 172414 BPL -4
50 50 173362 105037 172414 CLRB G#CSR
51 51 173366 012701 010510 MOV #10510, R1
52 52 173372 012700 010540 MOV #10540, R0
53 53 173376 004767 000016 JSR PC, ZEROBP
54 54 173402 012701 010710 MOV #10710, R1
55 55 173406 012700 010740 MOV #10740, R0
56 56 173412 004767 000002 JSR PC, ZEROBP
57 57 173416 000462 BR STAT

```

:INITIALIZE LIU

```

: CLEAR LOOP COUNT
: LDADR
: ADDRESS=0
: GOOD WD
: NO RETRY
: SEL ACRAM
: COUNTER
: WRITE A NULL
: GOOD WD
: NO LOOP UNTIL READY
: LOOP UNTIL ZERO
: COUNTER
: RD
: GOOD RD
: NO RETRY
: FETCH DATA
: CLEAR BITS
: DATA=NULL
: YES
: NO REPORT IT
: LOOP UNTIL ZERO
: LDCTADR
: SEND ADDRESS
: GOOD WRITE
: LOOP UNTIL
: SEL ACRAM
: LOAD A 4
: GOOD WRITE
: NO LOOP UNTIL
: RDBUFADR CMD
: SEL INBUF0
: SET POINTER=0
: FALSE READ DATA
: GOOD READ
: NO RETRY
: CLEAR DONE BIT
: RDBUFADR COMMAND
: SEL INBUF1 COMMAND
: POINTER=0
: FALSE READ DATA
: GOOD RD
: NO RETRY
: CLEAR DONE BIT
: RDBUFADR COMMAND
: SEL OUTBUF0 COMMAND
: RDBUFADR COMMAND
: SEL OUTBUF0 COMMAND
: ZERO BUFFER POINTER
: GO CLEAR STATUS

```


PROM NODE 28 MACRO V03.01 27-MAR-79 18:59:38 PAGE 3

[illegible]

```

1 2
3 173664 005077 000764
4 173670 005077 000762
5 173674 005277 000756
6 173700 105037 172414
7 173704 005737 172414
8 173710 100375
9 173712 013700 174650
10 173716 005010
11 173720 012706
12 173724 012737 172416
13 173732 012737 002400 172416
14 173740 013701 172416
15 173744 042701 177400
16
17
18
19 173750 132701 000004
20 173754 001410
21 173756 012702 010410
22 173762 012703 010440
23 173766 004767 000144
24 173772 004767 000530
25 173776 132701 000010
26 174002 001410
27 174004 012702 010610
28 174010 012703 010640
29 174014 004767 000116
30 174020 004767 000502
31
32
33
34 174024 132701 000001
35 174030 001410
36 174032 012702 010410
37 174036 012703 010440
38 174042 012704 000001
39 174046 004767 000026
40 174052 132701 000002
41 174056 001712
42 174060 012702 010610
43 174064 012703 010640
44 174070 012704 000002
45 174074 004767 000000

;LOADER START
SRTLDR: CLR QNEMPT
          CLR QPKCNT
          INC QPKCNT
          CLR QWCSR
          TST QWCSR
          BPL -4
          MOV QWDATA, R0
          CLR (R0)
          MOV #160500, SP
          MOV #4352., QWOPREG
          MOV #1280., QWOPREG
          MOV QWIOBUF, R1
          BIC #177400, R1

;BUFFER OVERFLOW
B2$: BITB #BIT02, R1
    BEQ B3$
    MOV #4360., R2
    MOV #4384., R3
    JSR PC, EMBF
    JSR PC, NAK
    BITB #BIT03, R1
    BEQ B01$
    MOV #4488., R2
    MOV #4512., R3
    JSR PC, EMBF
    JSR PC, NAK

B3$: BITB #BIT03, R1
    BEQ B01$
    MOV #4488., R2
    MOV #4512., R3
    JSR PC, EMBF
    JSR PC, NAK

;BUFFERS FULL
B01$: BITB #BIT00, R1
    BEQ 2$
    MOV #4360., R2
    MOV #4384., R3
    MOV #BIT00, R4
    JSR PC, PACK
    BITB #BIT01, R1
    BEQ L10
    MOV #4488., R2
    MOV #4512., R3
    MOV #BIT01, R4
    JSR PC, PACK

```

```

;MEMORY POINTER
;PACKET COUNTER
;START'S AT 1
;CLEAR CSR
;REQUEST YET
;NO LOOP ON LIO
;ADDRESS OF DATA
;CLEAR DATA
;RESET SP
;WCR : RS(0)
;RD
;CLEAR UNUSED BITS

;OV-FL
;COM=SEL INBUF 0
;COM=RDBUFFNT
;EMPTY BUFFER
;SEND NAK
;OV-FL
;COM=SEL INBUF 1
;COM=RDBUFFNT
;SEND NAK

;BUFFER 0 FULL
;NO CK 1
;YES, COM=RDBUFFADR
;COM=SEL INBUF0
;WHICH CRC BIT
;GO FIND PACKET
;BUFFER 1 FULL
;NO LOOP BACK
;YES, COM=RDBUFFADR
;COM=SEL INBUF1
;WHICH CRC BIT
;GO FIND PACKET

```


FROM NODE 28 MACRO V03.01 27-MAR-79 18:59:38 PAGE 6

```

1
2
3 174352 016702 000302      ;PACKET PARSE
4 174356 122712 000000      MOV    BUFFER, R2
5 174362 001426      CMFB   #0, (R2)
6 174364 016703 000266      BEQ
7 174370 022713 000144      MOV    PKCNT, R3
8 174374 001002      CMP    #100., (R3)
9 174376 005013      BNE    1$
10 174400 005213      CLR    (R3)
11 174402 121312      INC    (R3)
12 174404 001401      CMFB   (R3), (R2)
13 174406 000207      BEQ    2$
14 174410 002702      RTS     PC
15 174414 005213      ADD    #2, R2
16 174416 017703 000232      INC    (R3)
17 174422 012704 000100      MOV    QMEMPT, R3
18 174426 012223      MOV    #64., R4
19 174430 077402      MOV    (R2)+, (R3)+
20 174432 010377 000216      SOB    R4, 3$
21 174436 000207      MOV    R3, QMEMPT
22 174440 122762 000001      RTS     PC
23 174446 001004      CMFB   #1, 1(R2)
24 174450 013703 174652      BNE    5$
25 174454 011310      MOV    Q#STATUS, R3
26 174456 000207      MOV    (R3), (R0)
27 174460 122762 000005      RTS     PC
28 174466 001005      CMFB   #5, 1(R2)
29 174470 013700 000040      BNE    6$
30 174474 012706 001000      MOV    Q#40, R0
31 174500 000110      MOV    #1000, SP
32 174502 122762 000006      JMP    (R0)
33 174510 001002      CMPB   #6, 1(R2)
34 174512 000167 177146      BNE    7$
35 174516 000207      JMP    SRTIDR
36                                PC

```

```

;BUFFER AREA
;MEMORY OR CONTROL
;CONTROL PACKET
;PACKET COUNTER
;LIMIT
;NO CONTINUE
;RESET POINTER
;NEW PACKET
;RETURN
;OFFSET HEADER
;COUNT + 1
;BYTE COUNT
;LOAD MEMORY
;LOOP UNTIL ZERO
;IS IT A REPORT
;NO
;LOCATION OF STATUS
;TRANSFER STATUS
;IS IT A START
;NO
;FETCH START ADDRESS
;RESET USER STACK
;LEAVE FROM TO RAM
;WAS IT A LOAD COMMAND
;NO
;YES GOTO LOAD START

```


FROM NODE 28 MACRO V03.01 27-MAR-79 18:59:38 PAGE 7

```

1 2
3 174520 012705 174630      ACK:      MOV  #ACKPK, R5
4 174524 000402      BR  8$
5 174526 012705      NAK:      MOV  #NAKPK, R5
6 174532 012737 172416      MOV  #444B., @NOPREG
7 174540 012504      8$:      MOV  (R5)+, R4
8 174542 067704      ADD  @PKCNT, R4
9 174546 010437      MOV  R4, @NOPREG
10 174552 105737 172414      TSTB  @CSR
11 174556 100375      BPL  -4
12 174560 012704      MOV  #3, R4
13 174564 012537      9$:      MOV  (R5)+, @NOPREG
14 174570 105737 172414      TSTB  @CSR
15 174574 100375      BPL  -4
16 174576 077406      SOB  R4, 9$
17 174600 012737      MOV  #4368., @NOPREG
18 174606 012737 172416      MOV  #4421, @NOPREG
19 174614 105737 172414      TSTB  @CSR
20 174620 100375      BPL  -4
21 174622 105037 172414      CLRB  @CSR
22 174626 000207      RTS  PC
23
24 174630 004400      ACKPK:  .WORD  4400
25 174632 004402      .WORD  4402
26 174634 004403      .WORD  4403
27 174636 004405      .WORD  4405
28 174638 004400      NAKPK:  .WORD  4400
29 174640 004403      .WORD  4403
30 174642 004405      .WORD  4405
31 174644 004400      DATA:  .WORD  160400
32 174646 004405      .WORD  160402
33 174650 160400      STATUS: .WORD  160404
34 174652 160402      MEMPT:  .WORD  160406
35 174654 160404      PKCNT:  .WORD  160000
36 174656 160406      BUFFER: .WORD  4402
37 174660 160000      NODE:
38 174662 004402      .END
39 173000
40

```

```

;ADDRESS OF ACK
;ADDRESS OF NAK
;SEL OUTBFO
;FETCH HEADER
;ADD PACKET COUNT
;WRITE IT
;GOOD WD
;NO LOOP
;BYTE COUNT
;WD
;GOOD WD
;NO LOOP
;YES LOOP TIL 0
;MODSTAT
;SET BITS
;GOOD WD
;LOOP UNTIL

```

ACK	174520	BIT06 = 000100	CMEQ	174440	LIO	173704	PACK	174100
ACKPK	174630	BIT07 = 000200	CSR	= 172414	LOAD	174352	PKCNT	174656
BAR	172410	BIT08 = 000400	DATA	174650	MEMPT	174654	SRTIDR	173664
BIT00 = 000001	BIT14 = 040000	EMBP	174136	NAK	174526	START	173000	173664
BIT01 = 000002	BIT15 = 100000	INBUF0	173276	INBUF0	174640	STAT	173564	
BIT02 = 000004	BUFFER	174660	INBUF1	173332	NODE	-174662	STATUS	174652
BIT03 = 000010	B011	174024	INIT	173070	OPREG	= 172416	STRAM	173222
BIT04 = 000020	B21	173750	INTLS	173014	OTBUF0	173366	WCR	172412
BIT05 = 000040	B31	173776	IOBUF	= 172416	OTBUF1	173402	ZEROEP	173420

. ABS. 174664 000
000000 001
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
; DK: PROM28-DK: PROM28

SIG LOADING PROM

MACRO V03.01 27-MAR-79 19:00:35 PAGE 1

```

1  .TITLE SIG LOADING PROM
2  .IDENT /V1.0/
3  .ASECT
4  .=-173000

```

```

5  173000
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26

```

```

L.CKSM= X0
R0= X0
L.ADR= X1
R1= X1
L.BC= X2
R2= X2
L.BYT= X3
R3= X3
R4= X4
L.PTR= X5
R5= X5
SP= X6
PC= X7

```

```

TKS= 175610
TKB= 175612
TPS= 175614
TPB= 175616

```

```

SIGLDR: RESET
MOV #340, R0
MTPS R0
MOV #160500, SP
MOV #R, Q#TPB
TSTB Q#TKS
BPL 1$
MOV Q#TKB, R0
BIC #177400, R0
CMP #L, R0
BNE 1$
RESET

```

```

27 173000 000005
28 173002 012700 000340
29 173006 106400
30 173010 012706 160500
31 173014 012737 000122 175616 1$
32 173022 105737 175610
33 173026 100372
34 173030 013700 175612
35 173034 042700 177400
36 173040 022700 000114
37 173044 001363
38 173046 000005
39
40

```

```

;CPU PRI=7
;RESET STACK
;SIGNAL READY
;FEED BACK
;FETCH BYTE
;CLEAR MST
;LOAD MODE
;NO STAY READY
;RESET BUS FOR ABSLDR

```

```

1 2 173050 012716 175610
3
4 173054 011046
5 173056 012705 173170
6 173062 005001
7 173064 005004
8 173066 010446
9
10 173070 006016
11 173072 103402
12 173074 005016
13 173076 000403
14 173100 006316
15 173102 001001
16 173104 010116
17
18
19
20 173106 005000
21 173110 004715
22 173112 105303
23 173114 001374
24 173116 004715
25
26
27
28
29
30
31 173120 004767 000074
32 173124 010402
33 173126 062702 177774
34 173132 022702 000002
35 173136 001436
36 173140 004767 000054
37 173144 061604
38 173146 010401
39
40
41
42
43
44
45
46
47 173150 004715
48 173152 002004
49 173154 105700
50 173156 001753
51 173160 000000
52 173162 000751
53 173164 110321
54 173166 000770
55

ABSL: MOV #TKS,(SP) ;--> READER CSR
L.LOAD: MOV (SP),-(SP) ;MAKE 2 COPIES FOR L.READ
L.LDI: MOV #L.READ,L.PTR ;--> READ ROUTINE
CLR L.ACR ;CLEAR THE ROAD ADDRESS
CLR R4 ;CLEAR RELOCATION
MOV R4,-(SP) ;PICK UP THE CONTENT OF
;THE SOFTWARE SWITCH REGISTER
ROR GSP ;CHECK RELOCATION FACTOR
BCS L.LL1C ;JUMP IF SOME RELOCATION NEEDED
CLR GSP ;GO DO LOAD
BR L.LL2 ;GO DO LOAD
L.LDI: ASL GSP ;CHECK FOR NON-ZERO
BNE L.LD2 ;JUMP IF LOAD ADDRESS SPECIFIED
MOV L.ACR,GSP ;OTHERWISE CONTINUE LOADING FROM LAST LOAD

;* LOOK FOR THE BEGINNING OF A BLOCK
L.LD2: CLR L.CKSM ;INITIALIZE CHECKSUM
JSR PC,QL.PTR ;READ A FRAME
DECB L.BYT ;LOOP UNTIL +1 (START OF A BLOCK)
BNE L.LD2 ;LOOP UNTIL +1 IS FOUND
JSR PC,QL.PTR ;READ ANOTHER FRAME

;* INPUT AND SAVE BYTE COUNT, IF BYTE COUNT IS EQUAL TO 6
;* GO TO PROCEED JUMP
;*
JSR PC,L.GWRD ;GET FULL BYTE COUNT
MOV R4,L.BC ;
ADD #-4,L.BC ;SUBTRACT 4 TO MAKE BYTE COUNT CORRECT
CMP #2,L.BC ;WAS BYTE COUNT EQUAL TO 6?
BEQ L.JMP ;JUMP IF NO DATA (E.G. - JUMP BLOCK)
JSR PC,L.GWRD ;GET LOAD ADDRESS
ADD GSP,R4 ;GENERATE ACTUAL ADDRESS
MOV R4,L.ADR ;AND PUT IT INTO THE PROPER CELL

;* READ IN REMAINDER OF DATA
;* IF THE LOADER HALTS AT L.BAD, A CHECKSUM ERROR
;* HAS OCCURRED, R3 WILL CONTAIN THE EXPECTED CHECKSUM,
;* AND R0 WILL CONTAIN THE DEVIATION FROM THE EXPECTED
;* CHECKSUM.
;*
L.LD3: JSR PC,QL.PTR ;READ A FRAME
BGE L.LD4 ;BRANCH IF MORE DATA REMAINS
TSTB L.CKSM ;IF CHECKSUM IS
BEQ L.LD2 ;CORRECT, THEN CONTINUE
L.BAD: HALT ;CHECKSUM ERROR
MOV R3,L.LD2 ;PRESS CONTINUE TO IGNORE CHECKSUM
L.LD4: MOV R0,L.BYT,(L.ADR)+ ;STORE 8 BITS AT A TIME
BR L.LD3 ; THE RE-LOOP

```



```

SIG LOADING FROM          MACRO V03.01 27-MAR-79 19:00:35 PAGE 3

1 2
3
4 173170 016003 000006
5 173174 105213
6 173176 105713
7 173200 100376
8 173202 116303
9 173206 042703 177400
10 173212 060300
11 173214 005302
12 173216 000207
13
14
15
16
17
18 173220 004715
19 173222 010304
20 173224 004715
21 173226 000303
22 173230 050304
23 173232 000207
24
25
26
27
28
29 173234 004767 177760
30 173240 004715
31 173242 105700
32 173244 001345
33 173246 006204
34 173250 103002
35 173252 000000
36 173254 000704
37 173256 006304
38 173260 061604
39 173262 000114
40
41
42

; * INPUT A FRAME, DECREMENT BYTE COUNT, AND ACCUMULATE CHECKSUM
; *
; *
L.READ: MOV      G(SP),L.BYT      ;DEVICE ADDRESS TO L.BYT
          INCB     QL.BYT         ;SELECT READER
          TSTB     QL.BYT         ;DONE?
          BPL      L.R1           ;NO
          MOV      2(L.BYT),L.BYT ;GET CHARACTER
          BIC      #177400,L.BYT  ;CLEAR GARBAGE BITS
          ADD      L.BYT,L.CKSM   ;ADD TO CHECKSUM
          DEC      L.BC           ;DECREMENT BYTE COUNT BY ONE
          RTS      PC

; * ASSEMBLE ONE FULL WORD OF DATA
; *
; *
L.GWRD:  MOV      PC,QL.PTR       ;GET ONE CHAR
          MOV      L.BYT,R4       ;SAVE R3 IN TEMP
          JSR      PC,QL.PTR     ;GET ANOTHER FRAME
          SWAB     L.BYT          ;PLACE A FRAME
          BIS      L.BYT,R4       ;ASSEMBLE INTO ONE WORD
          RTS      PC

; * CHECK CORRECTNESS OF JUMP ADDRESS
; *
; * HALT IF ADDRESS IS ODD, JUMP TO PROGRAM IF ADDRESS IS EVEN
; *
; *
L.JMP:   JSR      PC,L.GWRD      ;GET POSSIBLE TRANSFER ADDRESS
          JSR      PC,QL.PTR     ;GET CHECKSUM
          TSTB     L.CKSM        ;IF INCORRECT
          BNE      L.BAD         ;GO TO CHECKSUM HALT ADDRESS
          ASR      R4            ;GET LOW ORDER BIT
          BCC      L.JMP1        ;SKIP IF ADDRESS IS EVEN
          HALT                ;OTHERWISE HALT
          BR       L.LD1B        ;RETURN TO START OF LOADING LOOP
          ASL      R4            ;RESTORE REGISTER
          ADD      QSP,R4
          JMP      GR4           ;JUMP TO USER

          .END      SIGLDR
173000

```

SIG LOADING PROM
SYMBOL TABLE

MACRO V03.01 27-MAR-79 19:00:35 PAGE 3-1

ABS. 173050	L.CKSM=2000000	L.LD1B 173066	L.LOAD 173054	TKB = 175612
L.ADR =2000001	L.GWRD 173220	L.LD1C 173100	L.PTR =2000005	TKS = 175610
L.BAD 173160	L.JMP 173234	L.LD2 173106	L.READ 173170	TPB = 175616
L.BC =2000002	L.JMP1 173256	L.LD3 173150	L.R1 173176	TPS = 175614
L.BYT =2000003	L.LD1 173056	L.LD4 173164	SIGLDR 173000	
. ABS. 173264 000				
000000 001				
ERRORS DETECTED: 0				

VIRTUAL MEMORY USED: 288 WORDS (2 PAGES)
 DYNAMIC MEMORY AVAILABLE FOR 51 PAGES
 ,DK:SIGLDR-DK:SIGLDR

4.0 MODIFICATION TO ESMD SOFTWARE**Page****4.1 GAT13 STACK MACHINE****186****4.2 GAT13****207****4.3 ESMD USER LANGUAGE****219****4.4 ESMD LOADER****231-3**

FRIDAY 05/25/79

FILE-NAME	USER CODE	DATE	CREATE TIME	LAST ACCESS TIME	RECORD SIZE	BLOCK SIZE	RECORD COUNT	PAGE SIZE	FILE TYPE	DISK ADDR	TOTAL SECTORS
ANV1-4HOUR.	AN	08/12/77	7:56	08/12/77	4	1	1	17	LN	1396	8
TEST-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1397	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1398	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1399	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1400	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1401	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1402	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1403	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1404	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1405	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1406	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1407	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1408	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1409	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1410	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1411	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1412	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1413	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1414	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1415	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1416	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1417	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1418	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1419	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1420	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1421	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1422	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1423	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1424	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1425	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1426	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1427	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1428	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1429	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1430	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1431	175
ALPHABET-4HOUR.	SS	09/03/77	1:15	09/03/77	4	1	1	17	LN	1432	175</

[illegible][illegible][illegible]

[illegible][illegible][illegible][illegible][illegible][illegible][illegible]

0	1	2	3	4	5	6	7	8	9
10	11	12	13	14	15	16	17	18	19
20	21	22	23	24	25	26	27	28	29
30	31	32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47	48	49
50	51	52	53	54	55	56	57	58	59
60	61	62	63	64	65	66	67	68	69
70	71	72	73	74	75	76	77	78	79
80	81	82	83	84	85	86	87	88	89
90	91	92	93	94	95	96	97	98	99

[illegible][illegible][illegible]

812	***	ESMD/POP-BDS-STAC.	17
112	***	OLD/TCC14.	1
182	***	MEL/BDS-STAC.	12
832	***	ESMD/TCC15.	20
932	***	LPFT.	14
982	***	OLD/GAT17.	1
992	***	DIAGLDR.	16
002	***	SYSTEM/DISC-PRINT.	13
002	***	SYSTEM/MAP-OUT.	9
002	***	OLD/ESMLDR.	5
002	***	DIAG/POP.	126
002	***	OLD/GAT12.	
002	***	OLD/CRT18.	7
002	***	SOURCE/TABLE.	23
002	***	R/USERS.	36
002	***	ESMLDR.	5
002	***	ESMDIAG/GAT13.	36
002	***	USRLNG.	46
002	***	CODEF.	42
002	***	DIAG/08J-RCV.	50
002	***	DIAG/08J-SEND.	8
002	***	TEST48.	12
002	***	ESMD/LPFT-FOR.	24
002	***	GAMES/STAR-TREK.	24
002	***	LETTERS/FIN-ESMDI.	22
002	***	ESMD/GAT13-ESMD-S.	8
002	***	USER/TKND.	52
002	***	USER/SOURCE/TCCF-SEND.	232
002	***	LOGOFF.	1
002	***	ESMDIAG.	6
002	***	ESMDIAG/RTC.	30
002	***	NEG.	74
002	***	ESMDIAG/SDLC-SND.	
002	***	DIAG/RCV.	
002	***	PROM.	
002	***	ESMDIAG/DNEX.	
002	***	ESMDIAG/08J-DNEX.	
002	***	MEL/BDS-LOADER.	
002	***	ESMDIAG/SDLC-RCV.	
002	***	SYSTEM/CURRENTSTATU.	
002	***	OLD/USRLNG.	
002	***	08J-LPSINT.	
002	***	R.	
002	***	GAMES/NIM.	
002	***	DIAG/ACH.	
002	***	SYSTEM/DUMP.	
002	***	SYSTEM/MT-PRINT.	
002	***	R/USERS.	


```

4052 ***CRC-CALC.
4081 ***
4093 MDMPL-TABLE.
4103 OPERATOR.
4207 ***
4223 ***
4287 DEVICE.
4302 SYSTEM/DIR-LIST.
4319 ***
4329 MGB-TABLE0.
4357 ***
4417 SYSTEM/MDMPL.
4480 ***
4504 LEARN.
4516 OLD/SOL14.
4536 OLD/SOL15.
4556 LENFILE.
4594 ***
4626 HST16.
4657 SOURCE/USRLNG-PATCH.
4657 GAMES/OTHELLO.
4695 PCMAIN.
4749 JUNK.
4829 ***
4884 8776-SEC.
4964 ***
4976 USER/INFO.
5056 SOURCE/NODE-CNTRL.
5216 ***
5224 SOURCE/FILE-FIX.
5244 SOURCE/NODE-CNTRL.
5404 LPSINT.
5496 ***
5576 DIAG/BUF.
5576 ***
5676 OLD/ESMD-GAT13-8.
5708 ***
5774 OLD/GAT13.
5774 GAT12.
5799 USER/TNKD.
5803 GAMES.
5835 ***
5889 SL7-NANO.
5923 ***
5977 EE.
6023 ***
6094 SOURCE/MTDS.
6174 ***
6358 RC.
6358 ***
6421 OPERATOR/DEFINES.
6421 ***
6429 B03-OUTTBL.
6513 ICC15.
6519 ICC15.
6559 SLD15-ST16.
6579 OLD/ST16.
6602 GAT18.
6644 SEC19.
6644 ***
6670 SOURCE/LOADER-PATCH.
6803 SOURCE/TEST-48HOUR.
6823 ***
6977 DIAG/SEND.
6977 ***
7057 SYSTEM/DISK-COMPARE.
7058 SOURCE/LIST.
7149 SOURCE/STACK-LIST.
7229 ***
7233 SYSTEM/COPY-TOP-BOT.
7243 ***
7270 ESMIDIAS/GAT.
7338 ESMID/ESMLDR.
7498 ***
7526 USRLN/MSG.
7606 USER/P0000.
7666 ***
7940 USRLN.

```

```

29
10
64
10
60
16
62
2
55
12
8
12
100
40
54
54
71
154
87
84
133
154
1
4
27
28
174

```

7956	ESMD/MEM.	70
8076	***	
8148	OLD	
8338	USER/P0000.	34
8411	MSG.	
8453	***	
8533	USRLN/MSG.	106
8611	PROM/LDR18.	
8713	***	
8766	LETTERS.	102
8868	***	
8967	BDSIM.	
9127	USER/P0000.	22
9287	ESMD/ESMLDR.	
9335	LOOP.	
9357	SYSTEM/TRANSLANG.	
9455	LOOP/UTILITY.	38
9496	***	
9534	SL9-NANO.	101
9603	***	
9704	EE-TABLE.	39
9730	***	
9767	SYSTEM/DECK-COMPARE.	8
9797	***	
9845	SYSTEM/DSIM.	23
9902	***	
9942	ESMD/B776-STACK.	180
10126	***	
10259	SOURCE/BDS-STACK.	368
10674	SOURCE/DUMMY.	
10700	GAT13	
10739	SOURCE/STACK-PATCH.	41
10777	***	
10937	SOURCE/TAPE-OP.	113
11097	SOURCE/TAPE-OP.	
11257	***	
11370	CHECK-ATTACH.	225
11601	***	
11681	SOURCE/MTDR.	38
11719	***	
11810	TRANSLANG-TA.	919
12729	***	
12737	TAPE-OP.	63
12800	***	
16384	GAP	500
16884	***	
16888	PROM/0BJ-LDR16.	
16892	PROM/0BJ-LDR12.	260
16896	PROM/0BJ-LDR13.	71
16900	PROM/0BJ-LDR14.	18
16904	PROM/0BJ-LDR15.	25
16908	PROM/0BJ-LDR17.	
16912	PROM/0BJ-LDR19.	
17112	***	
17179	DIAG/TCCF-SEND.	64
17251	0BJ-LEARN.	186
17254	***	
17259	SOURCE/NODE-CNTRL.	157
17457	***	
17473	OLD-GAT13.	41
17497	ESMDIAG/0BJ-SDLC-RCV.	11
17561	***	
17565	DIAG/TCCF-RCV.	1067
17757	***	
17757	MEL/CRT-READ.	100
17757	***	
17911	SOURCE/CRT-READ.	
17919	***	
17960	SOURCE/BDS-STACK.	
18120	***	
18131	PROM/LDR16.	
18251	SOURCE/BDS-STACK.	
18411	***	
19448	ESMDIAG/0BJRTC.	
19482	***	

```

124 AVAILABLE AREAS. 14548 DISK SECTORS AVAILABLE.
108
DIAG/0BJ-ACH.
***
169
DIAG/0BJ-CRT.
***
82
SOURCE/BDS-STACK.
***
29
BDS-LOADER.
***
320
DIAG.
***
65
SOURCE/BDS-STACK.
***
480
SOURCE/BDS-STACK.
***
98
END.
***
65
SOURCE/BDS-STACK.
***
75
ESMD/LPFT.
***
65
ESMD/LPFT.
***
74
MSG.
***
618
SOURCE/BDS-STACK.
***
479
ESMDIAG/CRT.
***
818
ESMDIAG/CRT.
***
171
MEL/TEST-48HOUR.
***
3301
SOURCE/BDS-STACK.
***
2
MGB.
***
MGB-TABLE.
***
818
MGB-TABLE.
***
171
ESMDI.
***
3301
USER/DIR.
***
3301
SEC/SEC19.
***
2
TRM.
***
6AP
***
124 AVAILABLE AREAS. 14548 DISK SECTORS AVAILABLE.

```

4.1 GAT13 Stack Machine

The GAT13 Stack Machine includes an operator to handle the asynchronous interface between loop 4 and loop 5. This operator is a modification of the operator used to handle the CRT on loop 4. The differences in the operators are in the areas of character conversion and protocol. In the GAT13 operator the first six characters of a transfer are not converted since they are comprised of the header for the packet and are treated as binary data. In the protocol area, the CRT operator reads and discards a garbage character from the terminal. This function was eliminated in GAT13 stack machine.

AD-A078 392

BURROUGHS CORP PAOLI PA FEDERAL AND SPECIAL SYSTEMS GROUP F/6 9/2
SOFTWARE MAINTENANCE MANUAL FOR THE MODULAR SYSTEM CONTROL DEVE--ETC(U)
NOV 79 DCA100-76-C-0083
66158 SBIE-AD-E100 314 NL

UNCLASSIFIED

3 OF 4
ADA
078392




```

0F0D 2F ** **      WR = IOINTERRUPT
0F10 87 ** **      I1 = WR
0F11 03 5F 00      M1 = INTERPF + OFF
0F14 9B ** **      I1 = 0(1)
0F15 2F ** **      WR = IOINTERRUPTF
0F18 87 ** **      I1 = WR
0F19 2D 04          MASK = #04

INTEXIT:
0F1B 03 ** **      M1 = INTTEMP
0F1E 64 ** **      B0 = I1
0F1F 57 ** **      B1 = I1
0F20 5A ** **      WR = I1

$OMIT = NOT PIO
$POP OMIT
0F21 61 ** **      AD = I1
0F22 68 ** **      J = I1
0F23 66 ** **      UMR = I1
0F24 D5 ** **      M1 = UMR
0F25 B6 ** **      ENABLE RETURN

RTCINT:
0F26 38 04          AD = #04
0F28 F2 ** **      B0 = STAT
0F29 03 ** **      M1 = TIMEWORD
0F2C 5A ** **      WR = I1
0F2D 04 01 00      WR = WR + 1
0F30 AC AC          M1 = M1 - 2
0F32 87 ** **      I1 = WR
0F33 B0 ** **      Z = WR - 1
0F34 25 1B 0F      IF KS GOTO INTEXIT
0F37 5A ** **      WR = I1
0F38 04 01 00      WR = WR + 1
0F3B AC AC          M1 = M1 - 2
0F3D 87 ** **      I1 = WR
0F3E 00 1B 0F      GOTO INTEXIT

$OMIT = NOT PIO
$POP OMIT + OMIT = NOT SDLC
$POP OMIT
TIMEWORD:
0F41 00 00          PLANTD1 0;
0F43 00 00          PLANTD1 0;
0F45 00 00          PLANT1 0;
0F46 00 00          INTLOC: PLANT1 0;
0F47 00 00          INTLOC: PLANT1 0;
0F48 00 00 00 00    INTTEMP:ZER0;ZER04; PLANT1 0;
0F54 00 00 00 00    IOINTERRUPTF:
0F55 82 ** **      I2 = WR
IOINTERRUPT:
0F56 34 00 00          L = M2 + 0
0F59 AC ** **      M1 = M1 - 1
0F5A 07 54 00          M2 = INTERPE + OFF
0F5D 3A 1D ** **      B0 = #1D
0F5F 9A ** **      I2 = B0
0F60 2F BD 00          WR = OPXE
0F63 82 ** **      I2 = WR
0F64 07 5F 00          M2 = INTERPF + OFF
0F67 3A 1D ** **      B0 = #1D
0F69 94 ** **      I2 = B0
0F6A 2F CB 00          WR = OPXF
0F6D 82 ** **      I2 = WR

00314200 D          ** INTERRUPT
00314300 D          ** OVERWRITE INTERPF + 1 WITH ...
00314400 D          ** GOTO ...
00314500 D          ** INTERRUPTF
00314600 D          ** SET MASK TO ALLOW RTC ONLY
00314700 D          ** RESTORE REGISTERS
00314800 D          **
00314900 D          **
00315000 D          **
00315100 D          **
00315200 D          **
00315300 D          **
00315400 D          **
00315700 D          **
00315800 D          **
00315900 D          **
00316000 D          **
00316100 D          **
00316200 D          **
00316300 D          **
00316400 D          **
00316500 D          **
00316600 D          **
00316700 D          **
00316800 D          **
00316900 D          **
00317000 D          **
00317100 D          **
00317200 D          **
00317300 D          **
00317400 D          **
00317500 D          **
00317600 D          **
00317700 D          **
00317800 D          **
00317900 D          **
00318700 D          **
00322100 D          **
00322200 D          **
00322300 D          **
00322400 D          **
00322500 D          **
00322600 D          **
00322700 D          **
00322800 D          **
00322900 D          **
00323000 D          **
00323100 D          **
00323200 D          **
00323300 D          **
00323400 D          **
00323500 D          **
00323600 D          **
00323700 D          **
00323800 D          **
00323900 D          **
00324000 D          **
00324100 D          **
00324200 D          **

```



```

00FA 3A 88      LIO1:      B0 = ADIN1
00FB EB        CIO = B0
00FC F0 ** **  B0 = IO
00FE 24 ** **  IF 1R GOTO J + LIO10
00FC1 35 01    B0 = B0 + 1
          LIO10:      B1FL FLIP
00FC3 C5      B1 = B0
00FC4 E6      Z = WRL + #FF
00FC5 01 FF ** IF KR GOTO LIO4
00FC7 26 ** ** B0 = B1 \ #FF
00FCA 10 FF    B0 = B0 + 1
00FCC 35 01    B0 = B0 + WRL
00FCE D0 ** ** IF KR GOTO LIO8
00FCF 26 ** ** WRL = B1
00FD2 D3      B1FL FLIP
00FD3 C5      B1 = B1 V #40
00FD4 0E 40    SKIP1
00FD6 02      LIO8:      B1FL FLIP
00FD7 C5      B0 = DING
00FD8 3A 20 ** IF 0R GOTO J + LIO3
00FDA 20 ** ** B0 = DIN1
00FDD 3A A0    LIO3:      CIO = B0
00FDF EB ** ** IF 1R GOTO J + LIO9
00FE0 24 ** ** SKIP1
00FE3 02      LIO9:      B0 = IO
00FE4 F0 ** ** IF 7R GOTO J + LIO6
00FE5 1A ** ** TX = WRL
00FE8 C6      I1 = IO
00FE9 90      LIO7:      CIO = RDSTAT1
00FEA 0C 80    B0 = STAT
00FEC F2      IF 0R GOTO J + LIO2
00FED 20 ** ** B0 = B0 & CRCMSK1
00FEF 15 02    SKIP2
00FF0 15 02    LIO2:      B0 = B0 & CRCMSK0
00FF2 40      WR FLIP
00FF3 15 01    Z = B0 = #00
00FF5 AD      IF KR GOTO CRCOK
00FF6 31 00    WRL = #00
00FF8 26 ** ** SKIP2
00FFB 3B 55    CRCOK:      WRL = #01
00FFD 40      IF 6R GOTO J + LIOEXIT
00FFE 3B 54    B0 = WRL
1000 1B ** ** B0 = B0 V 2
1003 DB      WRL = B0
1004 2B 02    LIOEXIT:  WR FLIP
1006 A5      M2 = L - 10
1007 AD      I2 = 0(2)
1008 12 F6 FF  M1 = K
100B 84      GOTO INTERP
100C C2      LIO6:
100D 00 5E 00

```

```

00330300 D
00330400 D
00330500 D
00330600 D
00330700 D
00330800 D
00330900 D
00331000 D
00331100 D
00331200 D
00331300 D
00331400 D
00331500 D
00331600 D
00331700 D
00331800 D
00331900 D
00332000 D
00332100 D
00332200 D
00332300 D
00332400 D
00332500 D
00332600 D
00332700 D
00332800 D
00332900 D
00333000 D
00333100 D
00333200 D
00333300 D
00333400 D
00333500 D
00333600 D
00333700 D
00333800 D
00333900 D
00334000 D
00334100 D
00334200 D
00334300 D
00334400 D
00334500 D
00334600 D
00334700 D
00334800 D
00334900 D
00335000 D
00335100 D
00335200 D
00335300 D
00335400 D
00335500 D
00335600 D
00335700 D
00335800 D
00335900 D
00336000 D
00336100 D
00336200 D

```

```

** INPUT BUFFER 1 ADDRESS COMMAND
**
** READ BUFFER ADDRESS
** IF CONTINUATION, READ
** ...1 MORE CHAR OF DATA
**
** SAVE BYTE FLAG
**
** TEST FOR SIZE = 0
**
** B0 = ~ PTR - 1
** B0 = ~PTR
** B0 = WRL - PTR
** KR => USE SIZE
**
**
** SET EMPTY FLAG
**
**
** READ BUFFER 0 COMMAND
**
** RAAD BUFFER 1 COMMAND
**
** IF CONTINUATION READ
** DON T DC DUMMY READ
**
** DUMMY READ
** 7R => IGNORE DATA
** SIZE
**
** READ STATUS
**
** ISOLATE CRC10K
**
** SET RESULT DESCRIPTOR FALSE
**
** SET RESULT DESCRIPTOR TRUE
** 6R => BUFFER NOT EMPTY
**
**
**
** RESET PC

```

```

1010 C6      TX = WRL      ;*
1011 F0      B0 = IO      ;*
1012 00 EA 0F  GOTO L107  ;*
*
1015 B6      INTENABLE:   ;*
      ENABLE RETURN      ;*
      00336900 D
*
1016 0C 60    WRITE8:    ;*
1018 C6      CIO = DOUT0  ;*
1019 5F      TX = WRL     ;* SIZE
101A A0      IO = I1      ;*
101B 24      WRU = 0       ;*
101E 00 FE 0F IF 1R GOTO J + WRITE8A ;*
      GOTO CRCOK
WRITE8A:
1021 0C E0    CIO = DOUT1  ;*
1023 17 00   IO = 0       ;* CONTROL CHAR
1025 17 FF   IO = #FF     ;* WRITE TOKEN ADDRESS
1027 0C 10   CIO = MODSTAT ;*
1029 17 03   IO = OBSFULL ;*
102B 00 FE 0F GOTO CRCOK  ;* EXIT
*
* * * * *
      WRITE ADDRESS COMPARISON RAM RAM (TYPE/ADDR)
*
RAM:
102E 38 08    AD = #08    ;*
1030 ED      B1 = B2     ;* TYPE
1031 39 FF   B1 = B1 \ #FF ;* COMPLEMENT TYPE
1033 P5      B32 FLIP    ;*
1034 EC      B0 = B2     ;* ADDR RAM ADDRESS
1035 0C 02   CIO = ACRAD  ;* LOAD ADDRESS COMMAND
1037 EA      IO = B0     ;* ADDR1
1038 3A 06   B0 = READND  ;* RAAD NON-DESTRUCTIVE VALUE
103A 20 **   IF 0R GOTO J + RAM1 ;*
103D 3A 04   B0 = READ    ;* READ VALUE
103F 24 **   IF 1R GOTO J + RAM1 ;*
1042 3A 00   B0 = WTKEN   ;* WRITE TOKEN DETECT VALUE
1044 2A **   IF 2R GOTO J + RAM1 ;*
1047 3A 07   B0 = NULLRAM ;* N LL VALUE
1049 1F **   IF 3S GOTO J + RAM1 ;*
104C 00 **   GOTO RAM2
RAM1:
104F 0C 01    CIO = ACRAM  ;*
1051 EA      IO = B0     ;*
1052 12 00 00 M2 = L + 0   ;*
1055 00 5E 00 GOTO INTERP ;*
RAM2:
1058 F0      B0 = IO      ;* READ RAM
1059 A0      WRU = 0      ;*
105A A5      WRL = B0     ;*
105B AD      WR FLIP     ;*
105C 12 00 00 M2 = L + 0   ;*
105F 00 5E 00 GOTO INTERP ;*
*
* * * * *
      READ STATUS STAT (CHAN)
*
STATUS:
1062 F5      B32 FLIP    ;*
1063 EC      B0 = B2     ;* CHAN

```



```

115A 0C 00      CIC = #00
115C C2        M1 = K
115D 2F 00 01  WR = #0100
1160 C9        WR = WR + B32
1161 00 1B 11  GOTO GIOEXIT

GIOWRITE:
1164 F2        B0 = STAT
1165 D7        NOOP
1166 B5        B1 = STAT
1167 39 FF     B1 = B1 \ #FF
1169 0C 00     CIO = #00
116B 20 ** **  IF 0R GOTO J + GIOWRITE1
116E 00 1B 11  GOTO GIOEXITF

GIOWRITE1:
1171 BB        K = M1
1172 A4        M1 = WR
1173 3B AA     WRL = #FF
1175 37 A5     B1 = #FA

WRITELOOP:
1177 64        B0 = I1
1178 E5        B0 = B0 \ WRL
1179 AC        M1 = M1 - 1
117A 89        I1 = B0
117B 42 01     B1 = B1 + 1
117D 26 77 11  IF KR GOTO WRITELOOP
1180 11 FA FF   B32 = B32 - 6

WRITEDATA:
1183 B1        B32 = B32 - 1
1184 19        IF KR GOTO J + TRANSMIT
*
*
WRITE1:
1187 64        Z = B0 - #60
1188 02 A0     IF KS GOTO J + WRITE3
118A 18 ** **  Z = B0 - #40
118D 02 C0     IF KS GOTO J + WRITE2
118F 18 ** **  WRL = B0
1192 A5        WRU = 0
1193 A0        M2 = WRR + INTASC
1194 06 F5 0D  B0 = I2
1197 53        SKIP2
1198 40

WRITE2:
1199 35 C0     B0 = B0 - #40

WRITE3:
119B 3B AA     WRL = #FF
119D E5        B0 = B0 \ WRL
119E AC        M1 = M1 - 1
119F 89        I1 = B0
11A0 00 83 11  GOTO WRITEDATA

TRANSMIT:
11A3 12 FC FF  M2 = I - 4
11A6 56        WR = I2
11A7 AD        WR FLIP
11A8 A1        WR = WR I 2
11A9 A1        WR = WR I 2
11AA A4        M1 = WR
11AB 0C 02     CIO = #02
11AD 05 FF     TX = 255
11AF 5F        IO = I1

```

** RESTORE PC
 ** SET FLAG FOR TRUE
 ** ADD IN SIZE OF TRANSFER
 **
 ** DISCARD OB STATUS
 ** READ IB STATUS
 **
 ** SAVE PC
 ** MASK FOR COMPLEMENTING CHAR
 ** LOOP COUNTER
 ** READ CHAR
 ** COMPLEMENT CHAR
 ** WHITE CHAR TO OB
 ** ADJUST COUNT
 **
 ** NO CHANGE
 ** RELOC
 **
 ** COMPLEMENT CHARACTER
 **
 ** POINTER BUFFER
 **
 ** MAKE BYTE ADDRESS
 ** MAKE BYTE ADDRESS
 ** PUT GIB INTO LOA MODE
 **

00361800 D
 00361900 D
 00362000 D
 00362100 D
 00362200 D
 00362300 D
 00362400 D
 00362500 D
 00362600 D
 00362700 D
 00362800 D
 00362900 D
 00363000 D
 00363100 D
 00363200 D
 00363300 D
 00363400 D
 00363500 D
 00363600 D
 00363700 D
 00363800 D
 00363900 D
 00364000 D
 00364100 D
 00364200 D
 00364300 D
 00364400 D
 00364500 D
 00364600 D
 00364700 D
 00364800 D
 00364900 D
 00365000 D
 00365100 D
 00365200 D
 00365300 D
 00365400 D
 00365500 D
 00365600 D
 00365700 D
 00365800 D
 00365900 D
 00366000 D
 00366100 D
 00366200 D
 00366300 D
 00366400 D
 00366500 D
 00366600 D
 00366700 D
 00366800 D
 00366900 D
 00367000 D
 00367100 D
 00367200 D
 00367300 D
 00367400 D
 00367500 D
 00367600 D
 00367700 D


```

00373800 D      ** READ SIZE
00373900 D      **
00374000 D      ** CHANGE WORD COUNT TO HALF WORD COUNT
00374100 D      ** TIME LIMIT
00374200 D      **
00374300 D      **
00374400 D      **
00374500 D      **
00374600 D      **
00374700 D      **
00374800 D      **
00374900 D      **
00375000 D      **
00375100 D      ** SUCCESSFUL EXIT FOR NAK
00375200 D      ** POINTER TO BUFFER ADDRESS
00375300 D      ** READ BUFFER ADDRESS
00375400 D      **
00375500 D      ** CHANGE WORD ADDRESS TO BYTE ADDRESS
00375600 D      **
00375700 D      ** ADJUST COUNT
00375800 D      **
00375900 D      **
00376000 D      **
00376100 D      **
00376200 D      **
00376300 D      ** READ STATUS
00376400 D      ** GR => HOST READY
00376500 D      **
00376600 D      ** READ DATA
00376700 D      ** READ DATA
00376800 D      ** DECREMENT HALF WORD COUNT
00376900 D      ** KS=>MORE DATA
00377000 D      ** SET TRUE FLAG
00377100 D      **
00377200 D      **
00377300 D      **
00377400 D      **
00377500 D      **
00377600 D      **
00377700 D      **
00377800 D      **
00377900 D      **
00378000 D      ** READ STATUS
00378100 D      ** GR=>HOST NOT READY
00378200 D      **
00378300 D      ** SEND FIRST BYTE
00378400 D      ** SEND SECOND BYTE
00378500 D      ** DECREMENT HALFWORD COUNT
00378600 D      ** KS=>MORE DATA
00378700 D      **
00378800 D      **
00378900 D      ** SET TIMEOUT COUNT
00379000 D      **
00379100 D      **
00379200 D      ** READ STATUS
00379300 D      ** GS=>HOST READY
00379400 D      ** LECEMENT COUNTER
00379500 D      **
00379600 D      **
00379700 D      **

```



```

128E 19 ** **      IF KR GOTO J + EXITFALSE ;*
$ OMIT = G13
$ POP OMIT
READDATA: CALL J + CRTREAD
$ OMIT = NOT G13
WRL = B0
B1FL FLIP
B0 = B0 \ WRL
B1 = B0
B1FL FLIP
B0 = XU
Z = B0 = #00
IF KR GOTO READ4
B0 = WRL
$ POP OMIT
Z = B0 - #20
IF KR GOTO J + READRELOC
Z = B0 - #60
IF KS GOTO J + READ3
B0 = B0 - #20
WRL = B0
WRU = 0
M2 = WRR + ASCINT
B0 = I2
SKIP2
READRELOC:
B0 = B0 + #40
READ3: B32 = B32 - 1
IF KS GOTO J + READ2
B32 = 0
SKIP1
READ2: I1 = B0
Z = B0 = #43
IF KR GOTO J + READDATA
CALL CRTREAD
$ OMIT = NOT G13
GOTO EXITTRUE
B1FL FLIP
B0 = B1
B0 = B0 \ WRL
$ POP OMIT
CALL TURNAROUND
B1 = ACK
$ OMIT = NOT G13
Z = B0 = #00
GOTO SNDRESP
B1 = NAK
SNDRESP:
$ POP OMIT
CALL CRTWRITE
$ OMIT = G13
$ POP OMIT
EXITTRUE: WRL = #01
SKIP2
EXITFALSE:
WRL = #00

1291 1E ** **      00384700 D
1294 A5             00384710
1295 C5             00385610
1296 E5             00385700 D
1297 E6             00385800 D
1298 C5             00385805
1299 CD             00385810
129A 31 00          00385815
129C 26 ** **      00385820
129F DB             00385825
12A0 02 E0          00385830
12A2 19 ** **      00385835
12A5 02 A0          00385840
12A7 18 ** **      00385845
12AA 35 E0          00385850
12AC A5             00385855
12AD A0             00385900 D
12AE 06 35 0E      00386000 D
12B1 53             00386100 D
12B2 40             00386200 D
12B3 35 40          00386300 D
12B5 B1             00386400 D
12B6 1E ** **      00386500 D
12B9 3C 00 00      00386600 D
12BC 02             00386700 D
12BD 89             00386800 D
12BE 31 43          00386900 D
12C0 19 91 12      00387000 D
12C3 2E ** **      00387100 D
12C6 00 ** **      00387200 D
12C9 C5             00387300 D
12CA DC             00387400 D
12CB E5             00387500 D
12CC 2E ** **      00387600 D
12CF 37 B1          00387700 D
12D1 31 00          00387800 D
12D3 00 ** **      00387900 D
12D6 37 C0          00388000 D
12D8 2E ** **      00388010
12DB 3E 54          00388020
12DD 40             00388030
12DE 3B 55          00388040
12DF 37 C0          00388050
12E0 31 00          00388060
12E3 00 ** **      00388100 D
12E6 37 C0          00388200 D
12E9 2E ** **      00388210
12EC 37 B1          00388220
12EF 37 C0          00388230
12F0 37 C0          00388240
12F3 00 ** **      00388250
12F6 37 C0          00388260
12F9 2E ** **      00388300 D
12FB 3E 54          00388310
12FD 40             00388310
12FE 3B 55          00388510
12FF 37 C0          00388600 D
1300 31 00          00388700 D
1303 00 ** **      00388800 D
1306 37 C0          00388900 D
1309 2E ** **      00389000 D
1312 37 C0          00389000 D

```



```

SENDECC:
$ OMIT = G13
$ POP OMIT
$ OMIT = G13
$ POP OMIT
$ OMIT = G13
$ POP OMIT
$ POP OMIT
GOTO J + EXITTRUE
WRITE4:
$ OMIT = G13
$ POP OMIT + OMIT = NOT G13
B0 = B0 - 1
XU = B0
B0 = B1
$ POP OMIT
GOTO WRITE3
TURNAROUND:
B1 = 0
B1 = B1 + 1
IF KR GOTO TURNAROUND + 2
$ OMIT = G13
$ POP OMIT
RETURN
CRTREAD:
CIO = ACIACMND
IO = ENABLEREAD
WR = #8000
CRTREAD1:
B0 = REQ
Z = B0 V ASYNCHMASK
IF KS GOTO J + CRTREAD2
WR = WR + #FFFF
IF KS GOTO CRTREAD1
GOTO EXITFALSE
CRTREAD2:
CIO = ACIAREAD
NOOP
B0 = IO
RETURN
CRTWRITE:
CIO = ACIACMND
IO = ENABLEREAD
WR = #0100
CRTWRITE1:
B0 = REQ
B0 = B0 V ASYNCHMASK
IF KS GOTO J + CRTWRITE2
WR = WR + #FFFF
IF KS GOTO CRTWRITE1
GOTO EXITFALSE
CRTWRITE2:
CIO = ACIAREAD
IO = B1
RETURN
DELAY:
CALL TURNAROUND
GOTO EXITFALSE
CIOFLAG: PLANT1 0

```

132B 1C DB 12

132E 35 FF

1330 CC

1331 DC

1332 00 1B 13

1335 37 AB

1337 42 01

1339 26 37 13

133C A6

133D 0C 81

133F 17 92

1341 2F 00 80

1344 F3

1345 3E FD

1347 18 ** **

134A 04 FF FF

134D 25 44 13

1350 00 DE 12

1353 0C C0

1355 D7

1356 F0

1357 A6

1358 0C 81

135A 17 32

135C 2F 00 01

135F F3

1360 2B FD

1362 18 ** **

1365 04 FF FF

1368 25 5F 13

136B 00 DE 12

136E 0C 80

1370 DC

1371 EA

1372 A6

1373 2E 35 13

1376 00 DE 12

1379 00

```

00394400 D
00394410
00394805
00394810
00395110
00395120
00395510
00395600 D
00395700 D
00395710
00396610
00396620
00396630
00396640
00396650
00396700 D
00396800 D
00396900 D
00397000 D
00397100 D
00397110
00397410
00397500 D
00397600 D
00397700 D
00397800 D
00397900 D
00398000 D
00398100 D
00398200 D
00398300 D
00398400 D
00398500 D
00398600 D
00398700 D
00398800 D
00398900 D
00399000 D
00399100 D
00399200 D
00399300 D
00399400 D
00399500 D
00399600 D
00399700 D
00399800 D
00399900 D
00400000 D
00400100 D
00400200 D
00400300 D
00400400 D
00400500 D
00400600 D
00400700 D
00400800 D
00400900 D
00401000 D
00401100 D
00401200 D

```



```

137A 38 02      CIO1:      AD = #02
137C 0C 00      CIO = #00
137E 0C 81      CIO = #81
1380 17 03      IO = #03
1382 0C 81      CIO = ACIACMND
1384 17 92      IO = ENABLEREAD
1386 A2 3A FF      M2 = M2 - 1
1387 3A FF      B0 = #FF
1389 94      I2 = B0
138A BB      K = M1
138B 00 DB 12      GOTO EXITTRUE
END
$ POP OMIT
$ OMIT = NOT PIO
$ POP OMIT + OMIT = NOT TCCF
** TCCF INTERFACE OPERATOR (BDS SYNCHRONOUS DC CONTROLLER)
**
BEGIN
LABEL SYNCHAR = #22, FILLER = #55, USRTREAD = #00, USRTSTATUS = #01,
USRTRESET = #02, USRTRESTART = #03, USRTWRITE = #04,
USRTLSYNCH = #05, USRTLDIFILL = #06, USRTLDIFORM = #07, NOMODEM = #80,
SLUSRTDA = #C0, SLUSRTTMT = #C1, SLUSRTSCR = #C2, RECEIVE,
TRANSMIT, TRANSMIT1, EXITTRUE, EXITFALSE, READLOOP, WRITELoop,
CHECKSTATUS, CHECKSTATUS1, TCCFLAG, TCCF1, MENDATA;
*
*
*
TCCF:
M2 = L + 0
I2=B32
M2 = TCCFLAG
B0 = I2
IF KR GOTO TCCF1
AD = #02
M2 = L - 4
WR = I2
WR FLIP
WR = WR ! 2
WR = WR ! 2
B1 = I2
B32 FLIP
K = M1
M1 = WR
B0 = #01
B32 = B32 - 1
IF KR GOTO EXITTRUE
IF 6S GOTO J + TRANSMIT
CIO = SLUSRTDA
*
RECEIVE:
WR = #300
READLOOP:
B0 = REQ
Z = B0 V #FD
IF KS GOTO CHECKSTATUS
WR = WR - 1
IF KS GOTO READLOOP
EXITFALSE:
138B 12 00 00
1391 83
1392 07 ** **
1395 53
1396 26 ** **
1399 38 02
139B 12 FC FF
139E 56
139F AD
13A0 A1
13A1 A1
13A2 54
13A3 F5
13A4 BB
13A5 A4
13A6 3A 01
13A8 B1
13A9 26 ** **
13AC 2C ** **
13AF 0C C0
13B1 2F 00 03
13B4 F3
13B5 3E FD
13B7 25 ** **
13BA 04 FF FF
13BD 25 B4 13

```

```

00401300 D
00401400 D
00401500 D
00401600 D
00401700 D
00401800 D
00401900 D
00402000 D
00402100 D
00402200 D
00402300 D
00402400 D
00402500 D
00402600 D
00402700 D
00402800 D
00402900 D
00403000 D
00403100 D
00403200 D
00403300 D
00403400 D
00403500 D
00403600 D
00403700 D
00403800 D
00403900 D
00404000 D
00404100 D
00404200 D
00404300 D
00404400 D
00404500 D
00404600 D
00404700 D
00404800 D
00404900 D
00405000 D
00405100 D
00405200 D
00405300 D
00405400 D
00405500 D
00405600 D
00405700 D
00405800 D
00405900 D
00406000 D
00406100 D
00406200 D
00406300 D
00406400 D
00406500 D
00406600 D
00406700 D
00406800 D
00406900 D
00407000 D
00407100 D
00407200 D
00407300 D
00407400 D
00407500 D
00407600 D
00407700 D
00407800 D
00407900 D
00408000 D
00408100 D
00408200 D
00408300 D
00408400 D
00408500 D
00408600 D
00408700 D
00408800 D
00408900 D
00409000 D
00409100 D
00409200 D
00409300 D
00409400 D

```



```

1427 25 F9 13
142A 0C 03
142C EA 0C 02
142D 0C C2
142F 3A 01
1431 00 C7 13

*
TCCF1:
1434 38 02
1436 0C 02
1438 EA
1439 D7
143A D7
143B D7
143C 0C 07
143E 17 0B
1440 0C 05
1442 17 22
1444 0C 06
1446 17 55
1448 0C 03
144A B7
144B 0C C2
144D A2
144E 3A FF
1450 94 01
1451 3A 01
1453 00 C7 13
1456 00

IF KS GOTO TRANSMIT1
C10 = USRTRESET
IO = B0
C10 = SLUSRTSCR
B0 = #01
GOTO EXITTRUE

AD = #02
C10 = USRTRESET
IO = B0
NOOP
NOOP
NOOP
C10 = USRTLDIFORM
IO = #0B
C10 = USRTLDISYNCH
IO = SYNCHCHAR
C10 = USRTLDIFILL
IO = FILLER
C10 = USRTRESET
B1 = IO
C10 = SLUSRTSCR
M2 = M2 - 1
B0 = #FF
I2 = B0
B0 = #01
GOTO EXITTRUE

TCCFLAG: PLANT1 0
END
$POP OMIT + OMIT = NOT SDLC
$POP OMIT
OPTABLE: OPB EROP, ADD, SUB, MUL, EROP, MOD, DIV, EROP ;
OPB EROP, EQVL, NEQV, GRTR, LEQV, GEQV, LSTR, BAND ;
OPB BOR, BEQV, SCN, DLT, XCH, NDY, STN, STD ;
OPB CBP, CBB, CIO, CADD, CSUB, CHR, XINT, TBL ;
OPB BNOT, CHS, SSP,ENABLE, LOT, STS, GTS, SETP ;
OPB EROP, LOD, DUP, ENT, RND, BRB, BRF, EVAL ;
OPB ADD1, SUB1, ZFOX, NEG1, PORV, UNLK, LOCK, RDI ;
00435500 D
00435600 D
00435700 D
00435800 D
00435900 D
00436000 D
00436100 D
00436200 D
00436300 D
00436400 D
00436500 D
00436600 D
00436700 D
00436800 D
00436900 D
00437000 D
00437100 D
00437200 D
00437300 D
00437400 D
00437500 D
00437600 D
00437700 D
00437800 D
00437900 D
00438000 D
00438100 D
00438200 D
00438300 D
00438400 D
00438500 D
00452600 D
00452700 D
00452800 D
00452900 D
00453000 D
00453100 D
00453200 D
00453300 D
00453400 D

```



```

14C3 9B 06 97 06
14C7 9C 0E E5 07
14CB E9 07 96 07
14CF DE 00 C3 01
14D3 A4 06 75 0E
14D7 53 0D 0D 09
14DB 09 09 D4 08
14DF 82 09 BC 0C
14E3 74 0A 9C 0E
14E7 F9 0E 9C 0E
14EB 9C 0E 9C 0E
14EF 9C 0E 9C 0E
14F3 9C 0E 9C 0E
14F7 9C 0E 9C 0E
14FB 9C 0E 9C 0E
14FF 9C 0E 35 07
1503 22 07 9C 0E
1507 9C 0E 9C 0E
150B 35 08 FF 0A
150F 41 08 92 0B
1513 9C 0E 9C 0E
1517 8C 0F 2E 10
151B 62 10 F3 10
151F 90 10 A2 10
1523 D9 11 9C 0E
1527 9C 0E 83 10
152B 8E 13 9C 0E
152F 9C 0E 9C 0E
1533 9C 0E 9C 0E
1537 9C 0E 9C 0E
153B 9C 0E 9C 0E
153F 9C 0E 9C 0E
1543 9C 0E 9C 0E
1547 9C 0E 9C 0E
154B 9C 0E 9C 0E
154F 9C 0E 9C 0E
1553 9C 0E 9C 0E

OPB RTR, RTN, XIT, NTR, L16, DEL, NOP, HALT ; 00453500 D

OPE XTR, TRC, TRW, TRS, TRD, TRB, TRH, DIO ; 00453600 D

OPB CEQ, EROP, EROP, EROP, EROP, EROP, EROP ; 00453700 D

OPB EROP, EROP, SMX, ZIP, EROP, PUT, GET, LDM ; 00453800 D

OPE WRT, RED, HASH, TRX, TRX1, SCAN1, EROP, PCH ; 00453900 D

OPB LIO, RAM, STATUS, GIO, TIME, WTKN, HIO, PANEL ; 00454000 D

OPB PIO, SWITCH, TCCF, SDLC, EROP, EROP, EROP ; 00454100 D

OPE EROP, EROP, EROP, EROP, EROP, EROP, EROP ; 00454200 D

OPE EROP, EROP, EROP, EROP, EROP, EROP, EROP ; 00454300 D

$ OMIT = NOT TRACE
$ OMIT = NOT TRACE
$ OMIT = NOT HIO
$ POP OMIT
$ POP OMIT
$ POP OMIT
$ LIST CLIST CODE

STACKBASE:4: END
$ CODE
; * ALIGN TO 4-BYTE BOUNDARY

0 ERRORS 4133 CARDS. 10597 TOKENS. 20294 RULES. 474 SECONDS.

1558 A5 A5 A5 A5
31 DISK SECTORS
1414 14 1413 22 1401 14 1400 0B 1389 13 1388 DD 13AE 13 13AD F4 13AB 13 139A C7 1398 14 1397 34 1394 14
1393 56 1364 13 1363 68 1349 13 1348 53 1324 13 1323 58 1311 13 1310 19 130C 13 130B 1B 1306 13 1305 2E
12FB 12 12FA FF 12DA 13 12D9 58 12D5 12 12D4 D8 12CE 13 12CD 35 12C8 12 12C7 DB 12C5 13 12C4 3D 12B8 12 12B7 12
12B7 BD 12A9 12 12A8 B5 12A4 12 12A3 B3 129F 12 129D EE 1293 13 1292 3D 1290 12 128F DE 128A 12 1289 DB 1288 12
1286 12 1285 F5 126F 13 126E 7A 126B 13 126A 79 123F 12 123E 59 122B 12 122A 49 1225 12 1224 3A 1219 12
1218 32 1215 12 1214 35 120C 12 120B 16 11FB 11 11FA FE 11E7 11 11E6 ED 11E4 11 11E3 F5 11E1 11 11E0 E8 1136 11
1191 11 1190 99 118C 11 118B 9B 1186 11 1185 A3 116D 11 116C 71 1142 11 1141 4E 113D 11 113C 4C 1136 11
1135 4E 1117 11 1116 22 110E 11 110D 64 10F9 11 10F8 ED 10F5 11 10F4 D8 10EF F2 10D7 10 10D6 E2

```


4.2 GAT13

The ALGOL program GAT13 was originally a default procedure loaded to insure well defined behavior in loop 4. The modification to GAT13 implements the gateway function between loop 4 and loop 5. GAT13 is identical to GAT12 except for differences in loop addresses, logical ID's, etc. and the interface to the gateway being interrupt driven in GAT13.

START OF SEGMENT

2

IF N = 4 THEN

BEGIN

REPLACE POINTER (PACK ((T1 := GETSPACE), 0)) BY

(MESSSEQ := (IF MESSSEQ=254 THEN 0 ELSE MESSSEQ + 1)) FOR 1,

0 FOR 1, %D2

0 FOR 2, %D3, D4

MONITOR FOR 1, %D5

SLID FOR 1, %D6

HOME FOR 1, IF FOR 23, %HOME, LINEFEEDS

NO RESPONSE FROM "

\$OMIT = NOT HST

\$POP OMIT + OMIT = NOT CRT

\$POP OMIT + OMIT = NOT SEC

\$POP OMIT

HOME FOR 1, ETX FOR 1;

REPLACE POINTER (PACK (T1, 0)) + 254 BY 59 FOR 1;

ENQUEUE (ACKQ, T1);

END ELSE RESTART (1);

END; %ERRORREC

30 LONG.

END; %ERRORREC

PROCEDURE GETSPACE;

BEGIN

IF DELTA (POINTER (FREE), PF) LSS 0 THEN RESTART (2);

GETSPACE := FETCH (PF, 1);

IF DELTA (POINTER (FREE), (PF := PF - 1)) LEQ 5 THEN

FLWCNT := 1 ELSE FLWCNT := 0;

END; %GETSPACE

END; %GETSPACE

PROCEDURE ENQUEUE (A, N); ARRAY A[0]; NEW N;

BEGIN

NEW QHEAD, QTAIL;

3

START OF SEGMENT

3

QHEAD := FETCH (POINTER (A), 1);

QTAIL := FETCH (POINTER (A) + 1, 1);

IF QTAIL GTR 2 THEN

BEGIN

REPLACE POINTER (A) + (QTAIL := QTAIL - 1) BY N FOR 1;

REPLACE POINTER (A) + 1 BY QTAIL FOR 1;

END ELSE

BEGIN

IF QHEAD LSS QLENGTH - 1 THEN

BEGIN

FOR I := 0 STEP 1 UNTIL QHEAD - QTAIL DO

REPLACE POINTER (A) + (QLENGTH - 1 - I) BY POINTER (A)

+ (QHEAD - I) FOR 1;

REPLACE POINTER (A) BY QLENGTH - 1 FOR 1, QLENGTH - 1 -

(QHEAD - QTAIL) FOR 1;

ENQUEUE (A, N);

END ELSE

ERRORREC (4); %QUEUE FULL

END;

38 LONG.

END;

3

3

3

3

3

3

3

3

3

3

3

3

3

3

3

00005400 D

00005500 D

00005600 D

00005700 D

00005800 D

00005900 D

00006000 D

00006100 D

00006200 D

00006300 D

00006400 D

00006500 D

00006600 D

00006700 D

00006800 D

00006900 D

00007000 D

00007100 D

00007200 D

00007300 D

00007400 D

00007500 D

00007600 D

00007700 D

00007800 D

00007900 D

00008000 D

00008100 D

00008200 D

00008300 D

00008400 D

00008500 D

00008600 D

00008700 D

00008800 D

00008900 D

00009000 D

00009100 D

00009200 D

00009300 D

00009400 D

00009500 D

00009600 D

00009700 D

00009800 D

00009900 D

00010000 D

00010100 D

00010200 D

00010300 D

00010400 D

00010500 D

00010600 D

00010700 D

00010800 D

00010900 D

00011000 D

00011100 D

00011200 D

00011300 D

00011400 D

00011500 D

00011600 D

00011700 D

00011800 D

00011900 D

00012000 D

00012100 D

00012200 D

2:0000:0

2:0001:0

2:0002:0

2:0003:0

2:0004:0

2:0005:0

2:0006:0

2:0007:0

2:0008:0

2:0009:0

2:0010:0

2:0011:0

2:0012:0

2:0013:0

2:0014:0

2:0015:0

2:0016:0

2:0017:0

2:0018:0

2:0019:0

2:0020:0

2:0021:0

2:0022:0

2:0023:0

2:0024:0

2:0025:0

2:0026:0

2:0027:0

2:0028:0

2:0029:0

2:0030:0

2:0031:0

2:0032:0

2:0033:0

2:0034:0

2:0035:0

2:0036:0

2:0037:0

2:0038:0

2:0039:0

2:0040:0

2:0041:0

2:0042:0

2:0043:0

2:0044:0

2:0045:0

2:0046:0

2:0047:0

2:0048:0

2:0049:0

2:0050:0

2:0051:0

2:0052:0

2:0053:0

2:0054:0

2:0055:0

2:0056:0

2:0057:0

2:0058:0

2:0059:0

2:0060:0

2:0061:0

2:0062:0

2:0063:0

2:0064:0

2:0065:0

2:0066:0

2:0067:0

2:0068:0

2:0000:0

2:0001:0

2:0002:0

2:0003:0

2:0004:0

2:0005:0

2:0006:0

2:0007:0

2:0008:0

2:0009:0

2:0010:0

2:0011:0

2:0012:0

2:0013:0

2:0014:0

2:0015:0

2:0016:0

2:0017:0

2:0018:0

2:0019:0

2:0020:0

2:0021:0

2:0022:0

2:0023:0

2:0024:0

2:0025:0

2:0026:0

2:0027:0

2:0028:0

2:0029:0

2:0030:0

2:0031:0

2:0032:0

2:0033:0

2:0034:0

2:0035:0

2:0036:0

2:0037:0

2:0038:0

2:0039:0

2:0040:0

2:0041:0

2:0042:0

2:0043:0

2:0044:0

2:0045:0

2:0046:0

2:0047:0

2:0048:0

2:0049:0

2:0050:0

2:0051:0

2:0052:0

2:0053:0

2:0054:0

2:0055:0

2:0056:0

2:0057:0

2:0058:0

2:0059:0

2:0060:0

2:0061:0

2:0062:0

2:0063:0

2:0064:0

2:0065:0

2:0066:0

2:0067:0

2:0068:0

2:0000:0

2:0001:0

2:0002:0

2:0003:0

2:0004:0

2:0005:0

2:0006:0

2:0007:0

2:0008:0

2:0009:0

2:0010:0

2:0011:0

2:0012:0

2:0013:0

2:0014:0

2:0015:0

2:0016:0

2:0017:0

2:0018:0

2:0019:0

2:0020:0

2:0021:0

2:0022:0

2:0023:0

2:0024:0

2:0025:0

2:0026:0

2:0027:0

2:0028:0

2:0029:0

2:0030:0

2:0031:0

2:0032:0

2:0033:0

2:0034:0

2:0035:0

2:0036:0

2:0037:0

2:0038:0

2:0039:0

2:0040:0

2:0041:0

2:0042:0

2:0043:0

2:0044:0


```

SEGMENT 4 IS 16 LONG.
*****
QHEAD := FETCH (POINTER (A), 1);
DEQUEUE := FETCH (POINTER (A) + QHEAD, 1);
IF D THEN
  IF QHEAD = 2 THEN REPLACE POINTER (A) BY QLENGTH FOR 1,
    QLENGTH + 1 FOR 1 ELSE
    REPLACE POINTER (A) BY QHEAD - 1 FOR 1;
END;

*****
START OF SEGMENT 5
POINTER P;
PROCEDURE FINDPACK (J); NEW J;
BEGIN
  FINDPACK := OUTQ;
  IF J = 1 THEN
    BEGIN
      PACK [OUTQ, 63] := 0; %MARK PACKET EMPTY
      FULL := SENT := 0;
    END;
  END;
  %
  PROCEDURE NOTSENT (M1); NEW M1;
  BEGIN
    REPLACE POINTER (PACK [(T1 := GETSPACE), 0]) BY
      0 FOR 1, %D1
      (MESSEQ := (IF MESSEQ=254 THEN 0 ELSE MESSEQ + 1)) FOR 1, %D2
      0 FOR 2, %D3, %D4
      MONITOR FOR 1, %D5
      SLID FOR 1, %D6
      HOME FOR 1, LF FOR 23, %HOME, LINEFEEDS
      PACKET, FETCH (P, 1) FOR 3 DIGITS, " OF MESSAGE ",
      FETCH (P + 1, 1) FOR 3 DIGITS, " NOT SENT FROM NODE ",
      DIGITS, " TO NODE ", FETCH (P + 4, 1) FOR 3 DIGITS, " P
      FOR 18, ETX FOR 1;
    REPLACE POINTER (PACK [T1, 0]) + 254 BY 110 FOR 1;
    ENQUEUE (ACKQ, T1);
    PACK [OUTQ, 63] := SENT := FULL := WRT := 0;
    REPLACE (PF := PF + 1) BY OUTQ FOR 1;
  END; %NOTSENT
  IF N THEN REPLACE (PF := PF + 1) BY FINDPACK (1) FOR 1 ELSE %ACK
  BEGIN
    T1 := FINDPACK (0);
    T2 := FETCH ((P := POINTER (PACK [T1, 0])) + 255, 1);
    IF (T2 := T2 + 1) GTR RESENDLIM THEN
      IF FETCH (P + 2, 1) [5:1] THEN
        NOTSENT (T1) ELSE
        BEGIN
          REPLACE P + 2 BY (FETCH (P + 2, 1) & 1[5:1]) FOR 1; %ALTRT
          $OMIT = NOT SEC
          $POP OMIT
          $OMIT = HST
          $POP OMIT
          REPLACE P + 4 BY ALT FOR 1;
          REPLACE P + 255 BY 0 FOR 1; %RESET RESEND COUNT
          SENT := 0;
          END ELSE
          BEGIN

```

```

00011200 D
00011300 D
00011400 D
00011500 D
00011600 D
00011700 D
00011800 D
00011900 D
00012000 D
00012100 D
00012200 I
00012300 D
00012400 D
00012500 D
00012600 D
00012700 D
00012800 D
00012900 D
00013000 D
00013100 D
00013200 D
00013300 D
00013400 D
00013500 D
00013600 D
00013700 D
00013800 D
00013900 D
00014000 D
00014100 D
00014200 D
00014300 D
00014400 D
00014500 D
00014600 D
00014700 D
00014800 D
00014900 D
00015000 D
00015100 D
00015200 D
00015300 D
00015400 D
00015500 D
00015600 D
00015700 D
00015800 D
00015900 D
00016000 D
00016100 D
00016300 D
00016400 D
00016500 D
00016600 D
00016700 D
00016800 D
00016900 D
00017000 D

```

```

4:0000:0
4:0002:0
4:0004:3
4:0005:1
4:0010:0
4:0012:2
4:0014:3
0:0054:1
0:0054:1
0:0056:0
0:0056:0
5:0000:2
5:0000:3
5:0002:3
5:0002:3
5:0003:3
5:0004:2
5:0005:2
5:0008:3
5:0010:2
5:0010:2
5:0010:3
5:0012:3
5:0012:3
5:0016:0
5:0016:3
5:0022:2
5:0023:1
5:0024:1
5:0025:1
5:0027:3
5:0032:3
5:0037:0
5:0042:0
5:0044:3
5:0049:2
5:0051:3
5:0057:1
5:0060:2
5:0060:3
5:0066:3
5:0066:3
5:0068:0
5:0072:3
5:0074:2
5:0077:2
5:0080:2
5:0080:2
5:0084:1
5:0084:1
5:0084:1
5:0084:1
5:0086:1
5:0086:1
5:0088:2
5:0089:2
5:0090:2

```

```

REPLACE P + 255 BY T2 FOR 1;
SENT := 0;
END;
END;
SEGMENT 5 IS 95 LONG.
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
PROCEDURE CNTRLMESS (CC); NEW CC;
BEGIN
  IF CC.[9:1] THEN RAM (FETCH (PI + 6, 1) & 1[8:8]) ELSE %ADD RDND D7
  IF CC.[10:1] THEN RAM (FETCH (PI + 6, 1) & 2[8:8]) ELSE %ADD RDA D7
  IF CC.[11:1] THEN RAM (FETCH (PI + 6, 1) & 4[8:8]) ELSE %ADD WTA D7
  IF CC.[12:1] THEN
    REPLACE POINTER (LIDFAD) + (FETCH (PI + 6, 1) - 1) % CHANGE PAD AT
    BY PI + 7 FOR 1 ELSE % ... TO DB
  $OMIT = NOT CRT
  $POP OMIT + OMIT = CRT
  IF CC.[14:1] THEN BEGIN END ELSE
  $POP OMIT
  IF CC.[0:1] THEN OUTFLWCT := NOT (OUTFLWCT) ELSE
  IF CC.[1:1] THEN RAM (FETCH (PI + 7, 1)) ELSE %DELETE RDND DB
  IF CC.[2:1] THEN RAM (FETCH (PI + 7, 1)) ELSE %DELETE RDA DB
  IF CC.[3:1] THEN RAM (FETCH (PI + 7, 1)) ELSE %DELETE WTA DB
  IF CC.[4:1] THEN SWITCH (SETPRIM) ELSE
  IF CC.[5:1] THEN
    BEGIN
      SWITCH (RSTPRIM);
      PRIMARYSET := FALSE;
    END ELSE
    IF CC.[6:1] THEN SWITCH (SETBKUP) ELSE
    IF CC.[7:1] THEN
      BEGIN
        SWITCH (RSTBKUP);
        BACKUPSET := FALSE;
      END;
    END;
  $OMIT := NOT GAT
  $POP OMIT
  BEGIN
    NEW T1, LI;
    START OF SEGMENT 6
    PROCEDURE BUILDACK (N); NEW N;
    $OMIT = NOT GAT
    IF FETCH (PI + 4, 1) = SLID THEN
      $POP OMIT
      BEGIN
        T1 := GETSPACE;
        REPLACE POINTER (PACK [T1, 0]) BY
        POINTER (INBUF) FOR 2, %D1 PACKET SEQ NO
        (IF N = 1 THEN 1 ELSE 128) FOR 1, %D2 MESSAGE SEQ NO
        0 FOR 1, %D3 CONTROL ACK OR NAK
        %D4 SECOND CONTROL CHAR
        POINTER (INBUF) + 5 FOR 1,
        SLID FOR 1;
        REPLACE POINTER (PACK [T1, 0]) + 254 BY 6 FOR 1;
        ENQUEUE (ACKQ, T1);
      END;
      LI := L.[8:8] - 2;
      IF L THEN
        BEGIN

```

5:0090:2 00017100 D
5:0092:3 00017200 D
5:0093:3 00017300 D
5:0093:3 00017400 D
5:0093:3 00017500 D
0:0057:1 0000017600 D
0:0057:1 00017700 D
0:0059:0 00017800 D
0:0059:0 0000017900 D
0:0055:0 0000018000 D
0:0071:0 0000018100 D
0:0077:0 0000018200 D
0:0078:0 0000018300 D
0:0081:3 0000018400 D
0:0085:0 0000018500 D
0:0085:0 0000018700 D
0:0085:0 0000018800 D
0:0088:0 0000018900 D
0:0088:0 0000019000 D
0:0092:2 0000019100 D
0:0097:2 0000019200 D
0:0102:2 0000019300 D
0:0107:2 0000019400 D
0:0111:2 0000019500 D
0:0112:2 0000019510 D
0:0113:2 0000019520 D
0:0114:2 0000019530 D
0:0115:2 0000019540 D
0:0116:2 0000019600 D
0:0120:2 0000019700 D
0:0121:2 0000019710 D
0:0122:2 0000019720 D
0:0123:2 0000019730 D
0:0124:2 0000019740 D
0:0124:2 0000019800 D
0:0124:3 0000019900 D
0:0124:3 0000020000 D
0:0126:2 0000020100 D
0:0126:2 0000020200 D
6:0000:1 0000020300 D
6:0002:1 0000020400 D
6:0002:1 0000020500 D
6:0004:2 0000020600 D
6:0004:2 0000020700 D
6:0005:2 0000020800 D
6:0007:0 0000020900 D
6:0009:1 0000021000 D
6:0011:0 0000021100 D
6:0011:0 0000021200 D
6:0015:1 0000021300 D
6:0016:0 0000021400 D
6:0018:1 0000021500 D
6:0019:2 0000021600 D
6:0023:3 0000021700 D
6:0026:0 0000021800 D
6:0026:1 0000021900 D
6:0028:2 0000022000 D
6:0029:0 0000022100 D

```

$OMIT = GAT
$OMIT = CRT
$POP OMIT
$POP OMIT

T1 := GETSPACE;
REPLACE POINTER (PACK [T1, 0]) BY POINTER (INBUF) FOR LI;
REPLACE POINTER (PACK [T1, 0]) + 254 BY LI FOR 1;
ENQUEUE (INQ, T1);

$OMIT = GAT
$OMIT = CRT
$POP OMIT
$POP OMIT
END ELSE BUILDACK (0); %BUILD NAK MESSAGE
END;

SEGMENT 6 IS 44 LONG.
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
PROCEDURE LOOPINPUT (IB); NEW IB;
BEGIN
  NEW CC, LI;

  START OF SEGMENT 7
    PI := POINTER(INBUF);
    LI := LIO (IB, INBUF, 0); %LENGTH OF INPUT BUFFER
    IF (CC := FETCH (PI + 2, 2)) = 0 THEN INPUTQUEUE (LI) ELSE
      BEGIN
        $OMIT = NOT GAT
        IF (CC.[8:1] OR CC.[15:1]) AND (FETCH (PI + 4, 1) = SLID) THEN
          $POP OMIT + OMIT = GAT
          $POP OMIT
          BEGIN
            ALTRT := CC.[13:1];
            IF CC.[8:1] THEN ACKNAK (1) ELSE ACKNAK (0); %ACK OR NAK
            END ELSE
              $OMIT = NOT GAT
              IF (FETCH (PI, 2) = 4 "55AA" AND (FETCH (PI + 4, 1) = SLID) THEN
                CNTRLMESS (CC) ELSE INPUTQUEUE (LI);
                $POP OMIT + OMIT = GAT
                $POP OMIT
                END;
              END;
            END;
          END;

          SEGMENT 7 IS 34 LONG.
          XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          PROCEDURE LINELOSS (P); POINTER P;
          BEGIN
            NEW TI;

            START OF SEGMENT 8
              REPLACE POINTER (PACK [(T1 := GETSPACE), 0]) BY
                0 FOR 1, %D1
                (MESSSEQ := (IF MESSSEQ=254 THEN 0 ELSE MESSSEQ + 1)) FOR 1, %D2
                0 FOR 2, %D3, %D4
                %D5
                MONITOR FOR 1,
                SLID FOR 1,
                HOME FOR 1, LF FOR 23, %HOME, LINEFEEDS
                "LOSS OF MODULATION ON " P FOR 7, "LOOP AT NODE ",
                SLID FOR 3 DIGITS, HOME FOR 1, ETX FOR 1;
                REPLACE POINTER (PACK [T1, 0]) + 254 BY 78 FOR 1;
                ENQUEUE (ACKQ, T1);
                % LINE LOSS
              END;

              SEGMENT 8 IS 31 LONG.
              XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
              PROCEDURE INTERRUPT (INT); NEW INT;

```

```

6:0030:0
6:0030:0
6:0030:0
6:0030:0
6:0030:0
6:0030:0
6:0031:1
6:0035:1
6:0039:1
6:0041:1
6:0041:1
6:0041:1
6:0041:1
6:0041:1
6:0043:1

0:0127:3
0:0127:3
0:0129:2
0:0129:2

7:0000:1
7:0002:1
7:0004:2
7:0010:0
7:0010:0
7:0010:0
7:0014:3
7:0014:3
7:0014:3
7:0015:3
7:0017:2
7:0023:0
7:0024:0
7:0024:0
7:0028:2
7:0033:0
7:0033:0
7:0033:0
7:0033:0

0:0130:3
0:0130:3
0:0132:2
0:0132:2

8:0000:0
8:0003:0
8:0003:3
8:0009:2
8:0010:1
8:0011:1
8:0012:1
8:0014:3
8:0019:2
8:0023:1
8:0027:3
8:0029:3

0:0133:3
0:0133:3

```



```

      BEGIN
      BEGIN
      NEW STATUS, CHAN;
      START OF SEGMENT 9
      CHAN := INT.[8:8];
      STATUS := INT.[0:8];
      IF CHAN = 8 THEN
      BEGIN
      DO BEGIN
      IF STATUS.[0:1] THEN %IBOF
      BEGIN
      STATUS := STATUS & 0 [0:1];
      LOOPINPUT (0);
      END ELSE
      IF STATUS.[1:1] THEN %IBIF
      BEGIN
      STATUS := STATUS & 0 [1:1];
      LOOPINPUT (1);
      END ELSE
      IF STATUS.[2:1] THEN %IBOOVL
      BEGIN
      STATUS := STATUS & 0 [2:1];
      LIO (0, INBUF, 0); %EMPTY BUFFER
      END ELSE
      IF STATUS.[3:1] THEN %IBIOVL
      BEGIN
      STATUS := STATUS & 0 [3:1];
      LIO (1, INBUF, 0); %EMPTY BUFFER
      END ELSE
      IF STATUS.[4:1] THEN %LNSWPR
      BEGIN
      STATUS := STATUS & 0 [4:1];
      IF STATUS.[7:1] AND NOT PRIMARISSET THEN
      BEGIN
      REPLACE POINTER (LINE) BY "PRIMARY";
      PRIMARISSET:=TRUE;
      LINELOSS (POINTER (LINE));
      END;
      END ELSE
      IF STATUS.[5:1] THEN %LNSWBK
      BEGIN
      STATUS := STATUS & 0 [5:1];
      IF NOT (STATUS.[7:1] OR BACKUPSET) THEN
      BEGIN
      REPLACE POINTER (LINE) BY "BACK UP";
      LINELOSS (POINTER (LINE));
      BACKUPSET:=TRUE;
      END;
      END ELSE
      IF STATUS.[6:1] THEN %WTD
      BEGIN
      STATUS := STATUS & 0 [6:1];
      WRTIME := 0; WRT := 0;
      END ELSE
      IF STATUS.[7:1] THEN % LINESWITCH
      BEGIN
      STATUS := 0;
      END UNTIL STATUS = 0;
      END ELSE
      $OMIT = NOT HST
      $POP OMIT + OMIT = NOT CRT

```

```

0028500 D
0028600 D
0028700 D
0028800 D
0028900 D
0029000 D
0029100 D
0029200 D
0029300 D
0029400 D
0029500 D
0029600 D
0029700 D
0029800 D
0029900 D
0030000 D
0030100 D
0030200 D
0030300 D
0030400 D
0030500 D
0030600 D
0030700 D
0030800 D
0030900 D
0031000 D
0031100 D
0031200 D
0031300 D
0031400 D
0031500 D
0031600 D
0031700 D
0031800 D
0031900 D
0032000 D
0032100 D
0032200 D
0032300 D
0032400 D
0032500 D
0032600 D
0032700 D
0032800 D
0032900 D
0032910 D
0033000 D
0033100 D
0033200 D
0033300 D
0033400 D
0033500 D
0033600 D
0033700 D
0033800 D
0033900 D
0034000 D
0034100 D
0040200 D
9:0136:1
9:0136:1
9:0136:1
9:0000:1
9:0002:0
9:0003:3
9:0004:2
9:0005:2
9:0006:2
9:0007:2
9:0009:1
9:0010:2
9:0011:2
9:0012:2
9:0013:2
9:0015:1
9:0016:2
9:0017:2
9:0018:2
9:0019:2
9:0021:1
9:0023:0
9:0024:0
9:0025:0
9:0026:0
9:0027:3
9:0029:2
9:0030:2
9:0031:2
9:0032:2
9:0034:1
9:0036:1
9:0037:1
9:0040:1
9:0041:1
9:0043:1
9:0043:1
9:0044:1
9:0045:1
9:0046:1
9:0048:0
9:0050:0
9:0051:0
9:0054:0
9:0056:0
9:0057:0
9:0057:0
9:0058:0
9:0059:0
9:0060:0
9:0061:3
9:0063:3
9:0064:3
9:0065:3
9:0067:2
9:0069:0
9:0070:0
9:0070:0

```



```

$POP OMIT + OMIT = NOT GAT
$ OMIT = G13
$ POP OMIT + OMIT=NOT G13
IF CHAN=2 THEN
BEGIN
IF (Q1 := GIO (1, XINBUF, 64)) THEN
BEGIN
REPLACE POINTER(XINBUF) + 256 BY ETX FOR 1;
SCAN PI:(PI:=POINTER(XINBUF)+6) WHILE SET;
Q1:=DETA(POINTER(XINBUF),PI)+6;
PI := POINTER(XINBUF);
REPLACE FI + (Q1 - 1) BY ETX FOR 1;
IF (M := FETCH (PI + 2, 2)) = 0 THEN
BEGIN
ENQUEUE (XINQ, (Q2 := GETSPACE));
REPLACE POINTER (PACK [Q2, 0]) BY POINTER (XINBUF) FOR Q1;
REPLACE POINTER (PACK [Q2, 0]) + 254 BY Q1 FOR 1;
END ELSE
BEGIN
IF (M.[8:1] OR M.[15:1]) AND (FETCH (PI + 4, 1) = SLID) THEN
BEGIN
ALTRT := M.[13:1];
IF M.[8:1] THEN ACKNAK (1) ELSE ACKNAK (0); %ACK OR NAK
END ELSE
IF FETCH (PI, 2) = 4"55AA" THEN
BEGIN
INTASC (POINTER (XINBUF) + 6, 2);
IF FETCH (PI + 4, 1) = SLID THEN CNTRLMESS (M) ELSE
BEGIN
ENQUEUE (XINQ, (Q2 := GETSPACE));
REPLACE POINTER (PACK [Q2, 0]) BY POINTER (XINBUF) FOR Q1;
REPLACE POINTER (PACK [Q2, 0]) + 254 BY Q1 FOR 1;
END;
END ELSE
IF M.[13:1] OR M.[8:1] OR M.{
9:0152:1
ENQUEUE (XINQ, (Q2 := GETSPACE));
REPLACE POINTER (PACK [Q2, 0]) BY POINTER (XINBUF) FOR Q1;
REPLACE POINTER (PACK [Q2, 0]) + 254 BY Q1 FOR 1;
END;
END;
END;
END;
$ POP OMIT + OMIT = NOT D13
$POP OMIT + OMIT = NOT D17
$POP OMIT + OMIT = NOT SEC
$POP OMIT + OMIT = NOT N14
$OMIT = NOT SDLC
$POP OMIT + OMIT = NOT TCCF
$POP OMIT
$POP OMIT + OMIT = NOT N15
$POP OMIT + OMIT = NOT SDLC
$POP OMIT + OMIT = NOT TCCF
$POP OMIT
$POP OMIT
END;
Q58 ;
END;
SEGMENT 9 IS 166 LONG.
00043268
00043269
00043270
00043271
00043272
00043273
00043274
00043275
00043276
00043277
00043278
00043279
00043280
00043281
00043282
00043283
00043284
00043285
00043286
00043287
00043288
00043289
00043290
00043291
00043292
00043293
00043294
00043295
00043296
00043297
00043298
00043299
00043300
00043301
00043302
00043303
00043304
00043305
00043306
00043307
00043308
00043309
00043310
00043311
00043312
00043313
00043314
00043315
00043316
00043317
00043318
00043319
00043320
00043321
00043322
00043323
00043324
00043325
00043326
00043327
00043328
00043329
00043330
00043331
00043332
00043333
00043334
00043335
00043336
00043337
00043338
00043339
00043340
00043341
00043342
00043343
00043344
00043345
00043346
00043347
00043348
00043349
00043350
00043351
00043352
00043353
00043354
00043355
00043356
00043357
00043358
00043359
00043360
00043361
00043362
00043363
00043364
00043365
00043366
00043367
00043368
00043369
00043370
00043371
00043372
00043373
00043374
00043375
00043376
00043377
00043378
00043379
00043380
00043381
00043382
00043383
00043384
00043385
00043386
00043387
00043388
00043389
00043390
00043391
00043392
00043393
00043394
00043395
00043396
00043397
00043398
00043399
00043400
00043401
00043402
00043403
00043404
00043405
00043406
00043407
00043408
00043409
00043410
00043411
00043412
00043413
00043414
00043415
00043416
00043417
00043418
00043419
00043420
00043421
00043422
00043423
00043424
00043425
00043426
00043427
00043428
00043429
00043430
00043431
00043432
00043433
00043434
00043435
00043436
00043437
00043438
00043439
00043440
00043441
00043442
00043443
00043444
00043445
00043446
00043447
00043448
00043449
00043450
00043451
00043452
00043453
00043454
00043455
00043456
00043457
00043458
00043459
00043460
00043461
00043462
00043463
00043464
00043465
00043466
00043467
00043468
00043469
00043470
00043471
00043472
00043473
00043474
00043475
00043476
00043477
00043478
00043479
00043480
00043481
00043482
00043483
00043484
00043485
00043486
00043487
00043488
00043489
00043490
00043491
00043492
00043493
00043494
00043495
00043496
00043497
00043498
00043499
00043500
00043501
00043502
00043503
00043504
00043505
00043506
00043507
00043508
00043509
00043510
00043511
00043512
00043513
00043514
00043515
00043516
00043517
00043518
00043519
00043520
00043521
00043522
00043523
00043524
00043525
00043526
00043527
00043528
00043529
00043530
00043531
00043532
00043533
00043534
00043535
00043536
00043537
00043538
00043539
00043540
00043541
00043542
00043543
00043544
00043545
00043546
00043547
00043548
00043549
00043550
00043551
00043552
00043553
00043554
00043555
00043556
00043557
00043558
00043559
00043560
00043561
00043562
00043563
00043564
00043565
00043566
00043567
00043568
00043569
00043570
00043571
00043572
00043573
00043574
00043575
00043576
00043577
00043578
00043579
00043580
00043581
00043582
00043583
00043584
00043585
00043586
00043587
00043588
00043589
00043590
00043591
00043592
00043593
00043594
00043595
00043596
00043597
00043598
00043599
00043600
00043601
00043602
00043603
00043604
00043605
00043606
00043607
00043608
00043609
00043610
00043611
00043612
00043613
00043614
00043615
00043616
00043617
00043618
00043619
00043620
00043621
00043622
00043623
00043624
00043625
00043626
00043627
00043628
00043629
00043630
00043631
00043632
00043633
00043634
00043635
00043636
00043637
00043638
00043639
00043640
00043641
00043642
00043643
00043644
00043645
00043646
00043647
0
```



```

PF := POINTER (FREE);
FOR G1 := 0 STEP 1 UNTIL FREESIZE - 1 DO PACK [G1, 63] := 0;
FOR G1 := 0 STEP 1 UNTIL FREESIZE - 2 DO REPLACE PF:PF BY G1 FOR 1;
FOR G1 := 0 STEP 1 UNTIL 255 DO ACKSEQ [G1] := 255;
PF := PF - 1;
REPLACE POINTER (LIDPAD) BY 3 FOR 12, XLID/PAD TABLE
5 FOR 1, 1 FOR 1, 2 FOR 1, 4 FOR 1, 6 FOR 1, 7 FOR 1, 8 FOR 1,
5 FOR 9, 0 FOR 12, 1 FOR 15, 2 FOR 5, 0 FOR 15,
4 FOR 5, 0 FOR 15, 5 FOR 5, 0 FOR 150;
FOR G1 := 0 STEP 1 UNTIL 255 DO RAM (G1 & 8[8]);
RAM (RDA & 2[8]);
RAM (WTA & 4[8]);
ENABLE (MASK);
NEWTIME := TIMER (0);
END;
$OMIT = NOT GAT
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
PROCEDURE ASCINT (PC, N); POINTER PC; NEW N;
BEGIN
  NEW CHAR, OFFSET;
  FOR OFFSET := 0 STEP 1 UNTIL N - 1 DO
    BEGIN
      CHAR := FETCH (PC + OFFSET, 1);
      IF CHAR LSS 4"20" THEN REPLACE PC + OFFSET BY CHAR + 4"40" FOR 1
      ELSE IF CHAR LSS 4"60" THEN
        REPLACE PC + OFFSET BY POINTER(TASCINT) + (CHAR - 4"20") FOR 1;
    END;
  END;
  $ASCINT
END;
SEGMENT 12 IS 21 LONG.
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
PROCEDURE INTASC (PC, N); POINTER PC; NEW N;
BEGIN
  NEW CHAR, OFFSET;
  FOR OFFSET := 0 STEP 1 UNTIL N - 1 DO
    BEGIN
      CHAR := FETCH (PC + OFFSET, 1);
      IF CHAR LSS 4"40" THEN
        REPLACE PC + OFFSET BY POINTER (TINTASC) + CHAR FOR 1 ELSE
        IF CHAR LSS 4"60" THEN REPLACE PC + OFFSET BY CHAR - 4"40" FOR 1;
    END;
  END;
  $INTASC
END;
SEGMENT 13 IS 21 LONG.
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
$POP OMIT
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
INIT;
WHILE TRUE DO
  BEGIN
    ENABLE (4);
    IF FETCH (POINTER (INQ), 1) GEQ FETCH (POINTER (INQ) + 1, 1) THEN
      BEGIN
        ENQUEUE (XOUTQ, (C2 := DEQUEUE (INQ, 1)));
        $OMIT = NOT HST
        $POP OMIT + OMIT = NOT CRT
        $POP OMIT + OMIT = NOT SEC
        $POP OMIT + OMIT = NOT N14
        $POP OMIT + OMIT = NOT N15
        $POP OMIT
      END
    END
  END

```



```

ENABLE (MASK);
END ELSE ENABLE (MASK);
IF NOT (STAT (8).{2:1}) THEN
BEGIN
  ENABLE (4);
  IF FETCH (POINTER (ACKQ), 1) GEQ FETCH (POINTER (ACKQ) + 1, 1) THEN
  BEGIN
    LQ := DEQUEUE (ACKQ, 1);
    REPLACE POINTER (OUTBUF) BY POINTER (PACK [LQ, 0]) FOR
    (Q1 := FETCH (POINTER (PACK [LQ, 0]) + 254, 1)),
    0 FOR 1, %DLC CNTRL CHAR
    POINTER (LIDFAD) + (FETCH (POINTER (OUTBUF) + 4, 1) - 1) FOR 1;
    REPLACE (PF := PF + 1) BY LQ FOR 1;
    PACK [LQ, 63] := 0; %MARK PACKET EMPTY
    LIO (0, OUTBUF, -(Q1 + 2));
    WRTIME := 0;
    WRT := 1;
  END ELSE
  IF NOT (OUTPLWNT) THEN
  IF FULL AND NOT SENT THEN
  BEGIN
    ENABLE (MASK);
    REPLACE POINTER (OUTBUF) BY POINTER (PACK [OUTQ, 0]) FOR
    (Q2 := FETCH (POINTER (PACK [OUTQ, 0]) + 254, 1)),
    0 FOR 1, %BLC CNTRL CHAR
    POINTER (LIDFAD) + (FETCH (POINTER (OUTBUF) + 4, 1) - 1)
    FOR 1;
    SENT := 1;
  $OMIT = NOT GAT
    SENT := FULL := 0;
    ENABLE (4);
    REPLACE (PF := PF + 1) BY OUTQ FOR 1;
  $VOID
  $POP OMIT
    PACK [OUTQ, 63] := 0;
    LIO (0, OUTBUF, -(Q2 + 2));
    ACTIME := 0;
    WRTIME := 0;
    WRT := 1;
  END ELSE ENABLE (MASK);
  END;
  ENABLE (4);
  IF FETCH (POINTER (XING), 1) GEQ FETCH (POINTER (XING) + 1, 1) AND
  NOT FULL THEN
  BEGIN
    OUTQ := DEQUEUE (XING, 1);
    FULL := 1;
    ENABLE (MASK);
  END ELSE ENABLE (MASK);
  $OMIT = NOT CRT
  $POP OMIT + OMIT = NOT GAT
  $OMIT = G13
  $POP OMIT
  IF FETCH (POINTER (XOUTQ), 1) GEQ FETCH (POINTER (XOUTQ) + 1, 1) THEN
  BEGIN
    Q1 := DEQUEUE (XOUTQ, 0);
    REPLACE POINTER (XOUTBUF) BY POINTER (PACK [Q1, 0]) FOR
    FETCH (POINTER (PACK [Q1, 0]) + 254, 1), LEOP FOR 1;

```

0:0313:1
 0:0314:1
 0:0316:1
 0:0317:3
 0:0318:3
 0:0319:2
 0:0323:1
 0:0324:1
 0:0326:3
 0:0330:0
 0:0333:0
 0:0335:3
 0:0340:1
 0:0343:2
 0:0346:3
 0:0349:2
 0:0350:2
 0:0351:2
 0:0352:2
 0:0353:2
 0:0354:1
 0:0356:3
 0:0357:3
 0:0358:3
 0:0362:0
 0:0367:0
 0:0367:3
 0:0371:0
 0:0372:1
 0:0373:1
 0:0373:1
 0:0375:0
 0:0375:3
 0:0379:0
 0:0379:0
 0:0382:1
 0:0382:1
 0:0385:0
 0:0386:0
 0:0387:0
 0:0388:0
 0:0389:0
 0:0391:0
 0:0391:0
 0:0391:3
 0:0395:2
 0:0396:2
 0:0397:2
 0:0400:0
 0:0401:0
 0:0402:0
 0:0404:0
 0:0404:0
 0:0404:0
 0:0404:0
 0:0404:0
 0:0404:0
 0:0407:3
 0:0408:3
 0:0411:1
 0:0414:2


```

SEGMENT 11 IS 28 LONG.
SEGMENT 0 IS 454 LONG.
0 ERRORS 609 CARDS. 4142 TOKENS. 7507 RULES. 158 SECONDS.
PROGRAM SIZE = 1043 WORDS 26 DISK SECTORS.
END

```

4.3 ESMD User Language

Additives were made to the User Language to send forms control characters to node 25 which is listed as a CRT but is in fact a DECWriter. A modification was also made to increment the packet numbers in USRLNG messages.

These changes have no effect on the operation of loop 4.


```

NOREC:= 11;
ST(IND,1):= 1003;
END;
BEGIN
  NRCNO:= 11;
  NOREC:= 11;
  ST(IND,1):= 1000;
END;

END;
ELSE IF ST(IND,1)= 1004 THEN
  BEGIN
    I:= INT(PIC,1);
    IF I=1 THEN
      BEGIN
        NRCNO:=14;
        NOREC:= 11;
        ST(IND,1):= 1003;
      END;
    ELSE IF I=2 THEN
      BEGIN
        NRCNO:= 12;
        NOREC:= 11;
        ST(IND,1):= 1000;
      END;
    ELSE IF I=3 THEN
      BEGIN
        NRCNO:= 21;
        NOREC:= 11;
        ST(IND,1):= 9999;
      END;
    ELSE IF I= 4 THEN
      BEGIN
        NRCNO:= 5;
        NOREC:= 11;
        ST(IND,1):= 0004;
      END;
    ELSE BEGIN
      NRCNO:= 11;
      NOREC:= 11;
      ST(IND,1):= 1004;
    END;
  END;
ELSE BEGIN % CONNECTOR M(P,1-108)
  REPLACE PX:PCN*8 BY LEOP,SEQNO,ZER;
  TR(DELTA(PCN*PX));
  IF ST(IND,2)=4 THEN
    BEGIN
      REPLACE PCN*6 BY IMC4;
      REPLACE PMT BY "MSG TO CRT ND= 4",NULX(63);
    END;
    ELSE
      BEGIN
        ST(IND,2)=18 THEN
          REPLACE PCN*6 BY IMC18;
          REPLACE PMT BY "MSG TO CRT ND= 18",NULX(62);
        END;
        ELSE
          BEGIN
            ST(IND,2)=25 THEN
              REPLACE PX:PCN*8 BY CR,LF,PIC WHILE SET,CR,LF,ETX;
              REPLACE PCN*6 BY IMC25;
              REPLACE PMT BY "MSG TO CRT ND=25",NULX(62);
            END;
            ELSE BEGIN
              REPLACE PCN*6 BY IMC8;
              REPLACE PMT BY "MSG TO CRT ND=8",NULX(63);
            END;
          WRITE (MSG(33),MOUT);
          FOR J:=1 STEP 1 UNTIL 5 DO
            BEGIN
              READ(MSG(J+13),MOUT);
              ENTA( MSG(J+33),MOUT);
            END;
            NRCNO:= 34;
            NOREC:= 6;
            ICFLL:= 1;
            ST(IND,1) := 1004;
          END;
        END;
      4 IS 160 LONG.
      %
      SYSTEM INQUIRY MODE OF OPERATION -MODE 2
    
```


[illegible]

```

%
PROCEDURE P2000;
BEGIN
  WHEN SYSTEM INQUIRY MODE OF OPERATION;
  NEW ND,NR,JJ,NMR,MNR;

  5
  START OF SEGMENT

  ARRAY OFIL(20);
  POINTER PX,POF;
  POF:=POINTER(OFIL);
  IF ST(IND,1) = 2000 THEN
    BEGIN
      I:=INT(PIC,1);
      IF I<=0 OR I GE3 5 THEN
        BEGIN
          NRCNO := 11;
          NOREC := 11;
        END
      ELSE IF I=1 THEN
        BEGIN
          NRCNO := 0 STEP 1 UNTIL 11 DO
            BEGIN
              READ(INFO(J),MOUT);
              WRITE(MSG(J+33),MOUT);
            END;
          NRCNO:= 34;
          NOREC := 12;
          ST(IND,1) := 2004;
        END
      ELSE BEGIN
        ST(IND,3) :=1;
        NRCNO := 27;
        NOREC := 3;
        ST(IND,1) := 2003;
      END;
    END
  ELSE IF ST(IND,1) = 2004 THEN
    BEGIN
      NRCNO:=30;
      NOREC:=4;
      ST(IND,1):= 2005;
    END
  ELSE IF ST(IND,1)=2005 THEN
    BEGIN
      I:=INT(PIC,1);
      IF I=1 THEN
        BEGIN
          NRCNO:= 22;
          NOREC:= 5;
          ST(IND,1):=2000;
        END
      ELSE IF I=2 THEN
        BEGIN
          NRCNO := 21;
          NOREC := 1;
          ST(IND,1) := 9999;
        END
      ELSE IF I=3 THEN
        BEGIN
          NRCNO := 5;
          NOREC:= 5;
          ST(IND,1):= 0004;
        END
      ELSE BEGIN
        NRCNO := 11;
        NOREC := 11;
        NND;
      END;
    END
  ELSE BEGIN
    ST(1,1) NE 2000,2004,2005
  END
END BEGIN
  TR(PIC);
  IF PIC = "NDI" THEN
    FOR J:=0 STEP 1 UNTIL 11 DO
      BEGIN
        READ(INFO(J),MOUT);
        WRITE (MSG(J+33),MOUT);
      END;
      NRCNO := 34;
      NOREC:= 12;
    END
  END
END

```

```

SI(IND,1)):= 2004;
END

ELSE BEGIN
TR(5);
SCAN PX:PIC WHILE "0123456789";
NR:= JJ:=INT(PX-DELTA(PIC,PX));
IF JJ=0 OR JJ GEQ 29 THEN
BEGIN
NRCNO:= 11;
MOREC := 1;
END
ELSE BEGIN
% ND = 1-28
IF JJ LEQ 3 THEN BEGIN MNR:=12; MNR:=47; END
ELSE MNR:=19; MNR:=57;
END
ELSE IF JJ GEQ 8 AND JJ LEQ 11 THEN
BEGIN MNR:=26; MNR:=67;
END
ELSE IF JJ GEQ 12 AND JJ LEQ 19 THEN
BEGIN MNR:=33; MNR:=77;
END
ELSE IF JJ GEQ 20 THEN
BEGIN MNR:=40; MNR:=87;
END;
END;

TR(6); IF SI(IND,3)=2 THEN
BEGIN
FOR J:=0 STEP 1 UNTIL 1 DO
BEGIN
MR:=MNR+J;
READ (INFO(NR),MOUT);
WRITE(MSG(J+44),MOUT);
END;
REPLACE PMT BY HEAD1,HEAD2;
WRITE(MSG(46),MOUT);
FOR J:=2 STEP 1 UNTIL 6 DO
BEGIN
NR:=MNR+J;
READ (INFO(NR),MOUT);
WRITE(MSG(J+45),MOUT);
END;
NRCNO:= 45;
MOREC := 8;
SI(IND,1) := 2004;
END
ELSE IF SI(IND,3)=3 THEN
BEGIN
FOR J:=0 STEP 1 UNTIL 1 DO
BEGIN
MR:=MNR+J;
READ (INFO(NR),MOUT);
WRITE (MSG(J+44),MOUT);
END;
REPLACE PMT BY HEAD1,HEAD2;
FOR J:=2 STEP 1 UNTIL 9 DO
BEGIN
NR:=MNR+J;
READ (INFO(NR),MOUT);
WRITE(MSG(J+45),MOUT);
END;
NRCNO:= 45;
MOREC := 11;
SI(IND,1) := 2004;
END
ELSE IF SI(IND,3)=4 THEN
BEGIN
FOR J:=0 STEP 1 UNTIL 9 DO
WRITE (INFO(106+JJ),OFIL);
END;
END;

TR(9);
FOR J:=0 STEP 1 UNTIL 9 DO

```

[illegible]

[illegible][illegible]

[illegible][illegible]

[illegible][illegible]

[illegible]

[illegible]


```

ELSE IF ST(IND,1) GEQ 4000 AND ST(IND,1) LSS 4999 THEN P4000
  BEGIN
  TR(1,1) := 1;
  NRCNO := 1;
  NREC := 2;
  ST(IND,1) := 0002;
  END;

%-----WRITE TO LOOP-----%
IF ST(IND,1) = 9999 THEN ST(1,1) := 1;
REPLACE PIC BY (" ") FOR 256;
IF NRCNO = 10 THEN
  BEGIN
  READ (MSG(NRCNO), MOJT);
  REPLACE PIC BY HOME; 4"4" FOR 12;
  PMT FOR 80; HOME; ETX;
  IF IND=3 THEN REPLACE PIC BY CR,LF,PMT FOR 80,CR,LF,ETX;
  WRITE (CRTX,ICODE);
  ELSE
  BEGIN
  REPLACE PIC BY CLEAR,LF;
  IF IND=3 THEN REPLACE PIC BY LEOP,SEQNO,ZER,INC25,CR,LF;
  FOR J:=1 STEP 1 UNTIL NREC DO
    BEGIN
    NN:=NRCNO+J-1;
    READ (MSG(NN), MOJT);
    WHILE (P3:=P3-2)="" " DO BEGIN END;
    P3:=P3+2;
    IF (DELTA(PIC,P1)+DELTA(PMT,P3)) GEQ 249 THEN
      BEGIN
      WRITE (MSG(PMT,P3), MOJT);
      REPLACE PIC BY STAYREC,ETX;
      WRITE (CRTX,ICODE);
      END;
    LOAD ICODE
    END;
    REPLACE PIC BY PMT FOR DELTA(PMT,P3),CR,LF;
    END;
    LAST PACKET
    IF FORM=1 THEN
      BEGIN
      REPLACE PIC BY SETFORMS,HOME,ETX;
      IF IND=3 THEN REPLACE PIC BY CR,LF,ETX;
      END;
    ELSE
      REPLACE PIC BY HOME,ETX;
    IF IND=3 THEN REPLACE PIC BY CR,LF,ETX;
    WRITE (CRTX,ICODE);
    PMT:=PIC;
    END;
    IF ICLG = 1 THEN
      BEGIN
      REPLACE PIC BY PCN FOR 256;
      WRITE (CRTX,ICODE);
      ICLG:=0;
      END;
    END;
  END;

%-----
3 IS 409 LONG;
CLOSE (CRD,1);
CLOSE (CRD,1);
CLOSE (CRD,1);
CLOSE (CRD,1);
END;
61 LONG;
64 LONG;
1018 CARDS. 6061 TOKENS. 11602 RULES. 187 SECONDS.

```


4.4 ESMD Loader

The loader was modified to load ESMD/GAT13-BDS-STACK instead of ESMD/BDS-STACK into GAT13. This change has no effect on the operation of the loader on the TTY output.

ALGOL COMPILER (02/16/78) FRIDAY 05/25/79 15:37:48 ESMLDR

```

$ MERGE ESMD/ESMLDR DISK
% ESMD LOOP 4 LOADER
%
% SOURCE ESMD/ESMLDR
% OBJECT ESMLDR
%SET ABS LIST CLIST
BEGIN
  FILE CARD(CARDS);
  START OF SEGMENT
  1
  FILE TTY(CRT, POINTER(" ").320);
  NEW NORM, ND, RESP, PLPIN, STAT0, STAT1, J, NDPRT ALL, BAD;
  ARRAY FILENAME[20], INP[20], OUTP[20], LPOUT[46];
  POINTER P1, PFIL, PIN, POUT, PLPINT, PLPIN, PND;
  DEFINE CRLEFTH=4 4D FOR 1, 4 43 FOR 2, 4 4A FOR 1,
  4 43 FOR 1#,
  TRUE=1#,
  DRUG-BEGIN
  % FOR DEBUGGING ONLY ***
  % REPLACE POUT BY "STAT0=", STAT0 FOR 3 DIGITS,
  "STAT1=", STAT1 FOR 3 DIGITS,
  "NDPRT=", NDPRT FOR 3 DIGITS, CRLEFTH;
  % WRITE(TTY, POUT);
  % FOR DEBUGGING ONLY ***
  END#;
  WRLOOP=G26(2, PLPINT, -46)#,
  RLLOOP=FOR J=0 STEP 1 UNTIL VALUE DO BEGIN END;
  G26(2, PLPIN, 1); J:=FETCH(PLPIN, 3);
  IF J=16 THEN RESP:=0 ELSE RESP:=1#;
  ARRAY PRINT [33], DISKBUFFER [46];
  FILE LINE (PRINT);
  FILE NODE (DISK, POINTER (" ").45, 180, 182);
  FILE MCP (DISK);
  FILE STACK (DISK, POINTER (" ").45, 180, 40);
  POINTER PDSK, STACKNAME, MCPNAME;
  PROCEDURE MIN FORWARD;
  PROCEDURE SEND (CODE, CODELENGTH); POINTER CODE; NEW CODELENGTH;
  BEGIN
    NEW LENGTH;
    START OF SEGMENT
    3
    POINTER PC;
    LENGTH := MIN (180 - DELTA (POINTER (DISKBUFFER)+4, PDSK), CODELENGTH);
    * 4);
    REPLACE PDSK:PDSK BY PC:CODE FOR LENGTH;
    IF DELTA (POINTER (DISKBUFFER) + 4, PDSK) = 180 THEN
      BEGIN
        BAD:=0;
        REPLACE PLPINT BY PND FOR 4;
        WRLOOP;
        RLLOOP;
        IF RESP=0 THEN BEGIN
          *TIMEOUT WITH NO RESPONSE
          REPLACE POUT BY "NO RESPONSE FROM NODE",
          ND FOR 2 DIGITS, CRLEFTH;
          BAD:=1;
        END
      END
    ELSE BEGIN
      NDPRT:=FETCH(PLPIN+2, 1);
      STAT0:=FETCH(PLPIN, 1);
    END
  END

```



```

NEW
  RELOCATIONOFFSET,
  SEGMENTDICTIONARYLENGTH,
  CODESEGMENTLENGTH,
  LOADADDRESS;

  IF SEARCH (MCP) LEQ 0 THEN
    RETURN (- NONEXISTANTMCPFILE);
  READ (MCP, BUFFER);
  IF (SEGMENTDICTIONARYLENGTH := - BUFFER [0]) LSS 0 THEN
    RETURN (- CLDSTYLMCP);
  CODESEGMENTLENGTH := BUFFER [1];

  READ (MCP, POINTER (BUFFER [HEADERSIZE]));
  RELOCATIONOFFSET := MEMORYSIZE - CODESEGMENTLENGTH;

  BEGIN NEW INDEX; FOR INDEX := 0 STEP 1 UNTIL SEGMENTDICTIONARYLENGTH - 1 DO
    DO
      BUFFER [HEADERSIZE + INDEX] := * &
        (BUFFER [HEADERSIZE + INDEX] * DESCRIPTORBASE +
          RELOCATIONOFFSET) DESCRIPTORBASE;
    END;
  END;

SEGMENT 8 IS 12 LONG.
  $OMIT = ABS
  $POP OMIT

  IF NOT SEND (POINTER (BUFFER), SEGMENTDICTIONARYLENGTH) THEN
    RETURN (- TRANSMISSIONFAILURE);
  $OMIT = NOT ABS

  IF NOT SENDZEROS (MEMORYSIZE -
    (STACKBASE + SEGMENTDICTIONARYLENGTH + CODESEGMENTLENGTH)) THEN
    RETURN (- TRANSMISSIONFAILURE);
  $POP OMIT

  LOADADDRESS := RELOCATIONOFFSET;
  WHILE READ (MCP, POINTER (BUFFER [HEADERSIZE])) AND
    CODESEGMENTLENGTH GTR 0 DO
    BEGIN
      $OMIT = ABS
      $POP OMIT

      IF NOT SEND (POINTER (BUFFER), MIN (MCPRECORDSIZE,
        CODESEGMENTLENGTH)) THEN
        RETURN (- TRANSMISSIONFAILURE);
      $OMIT = ABS
      $POP OMIT

      CODESEGMENTLENGTH := CODESEGMENTLENGTH - MCPRECORDSIZE;
    END;
  END;

  RETURN (TRUE);
END;

```



```

SEGMENT 9 IS
$LIST
  BEGIN
  REPLACE POUT BY "INVALID ND-RERUN ESMLDR",CRLFETX;
  WRITE(TTY,POUT);
  RETURN; %STOP
  END;

  END;
  REPLACE POUT BY "ENTER CEJ FILE TO BE LOADED",CRLFETX;
  WRITE(TTY,POUT);
  READ(CARD,PFIL);
  SCAN PI:PFIL UNTIL " ";
  REPLACE P1 BY " ";
  ENL;
  WHILE TRUE DO
  BEGIN
    CLOSE(STACK,1);
    IF PFIL="SDL14." OR PFIL="SDL15." THEN
      FILEATTRIBUTE(STACK,1,POINTER("ESMD/SDLC-BDS-STACK."))
    ELSE IF PFIL="SEC19." THEN
      FILEATTRIBUTE(STACK,1,POINTER("ESMD/PDP-BDS-STACK."))
    ELSE IF PFIL="GAT13." THEN
      FILEATTRIBUTE(STACK,1,POINTER("ESMD/GAT13-BDS-STACK."))
    ELSE
      FILEATTRIBUTE(STACK,1,POINTER("% NORMAL STACK MACHINE
      % ESMD/BDS-STACK."));
  END;
  STACKNAME := (PDSK := POINTER (DISKBUFFER) + 4);
  $LIST
  REPLACE STACKNAME BY STACK," ";
  IF STACKLOAD (STACKNAME, PFIL, 8192) GEQ 0 AND BAD=0 THEN
  BEGIN
    REPLACE POUT BY "SUCCESSFUL LOAD-NODE ",ND FOR 2 DIGITS,CRLFETX;
    WRITE (TTY, POUT);
  END;
  IF NORM = 0 AND ALL = 0 THEN RETURN; %CHECK FOR END OF PROGRAM
  ND := ND + 1;
  IF ND=16 THEN ND:=17;
  IF ND=20 THEN ND:=16;
  IF ALL=1 THEN BEGIN END
  ELSE BEGIN
    IF ND=13 THEN REPLACE PFIL BY "GAT13."
    ELSE IF ND=14 THEN REPLACE PFIL BY "SDL14."
    ELSE IF ND=15 THEN REPLACE PFIL BY "SDL15."
    ELSE IF ND=17 THEN REPLACE PFIL BY "GAT17."
    ELSE IF ND=18 THEN REPLACE PFIL BY "CRT18."
    ELSE IF ND=19 THEN REPLACE PFIL BY "SEC19."
    ELSE IF ND=16 THEN REPLACE PFIL BY "HST16."
  END;
  ENL;
  IF ND=16 THEN BEGIN NORM:=0; ALL:=0; END;
  END ?
END ?

SEGMENT 1 IS 264 LONG.
SEGMENT 11 IS 24 LONG.
SEGMENT 0 IS 1 LONG.
0 ERRORS 390 CARDS. 1919 TOKENS. 3579 RULES. 100 SECONDS.
PROGRAM SIZE = 683 WORDS 18 DISK SECTORS.

```

```

00035900 D 1:0126:1
00036000 D 1:0127:1
00036010 D 1:0134:3
00036100 D 1:0134:3
00036200 D 1:0137:2
00036300 D 1:0137:3
00036400 D 1:0137:3
00036500 D 1:0137:3
00036600 D 1:0145:1
00036700 D 1:0148:0
00036800 D 1:0150:3
00036900 D 1:0153:2
00037000 D 1:0155:0
00037100 D 1:0155:0
00037200 D 1:0155:1
00037300 D 1:0156:1
00037400 D 1:0159:0
00037500 D 1:0163:1
00037600 D 1:0167:2
00037700 D 1:0171:0
00037720 D 1:0175:1
00037730 D 1:0178:3
00037800 D 1:0183:0
00037900 D 1:0184:2

00038000 D 1:0188:1
00038010 D 1:0191:3
00038100 D 1:0191:3
00038200 D 1:0195:3
00038300 D 1:0199:2
00038400 D 1:0200:2
00038500 D 1:0208:3
00038600 D 1:0211:2
00038700 D 1:0211:2
00038800 D 1:0214:0
00038900 D 1:0215:0
00039000 D 1:0217:2
00039100 D 1:0220:0
00039200 D 1:0221:3
00039300 D 1:0222:3
00039400 D 1:0225:0
00039500 D 1:0230:1
00039600 D 1:0235:2
00039700 D 1:0240:3
00039800 D 1:0246:0
00039900 D 1:0251:1
00040000 D 1:0258:2
00040100 D 1:0258:2
00040200 D 1:0261:3
00040300 D 1:0262:3

```

APPENDIX 1 Systems Drawings

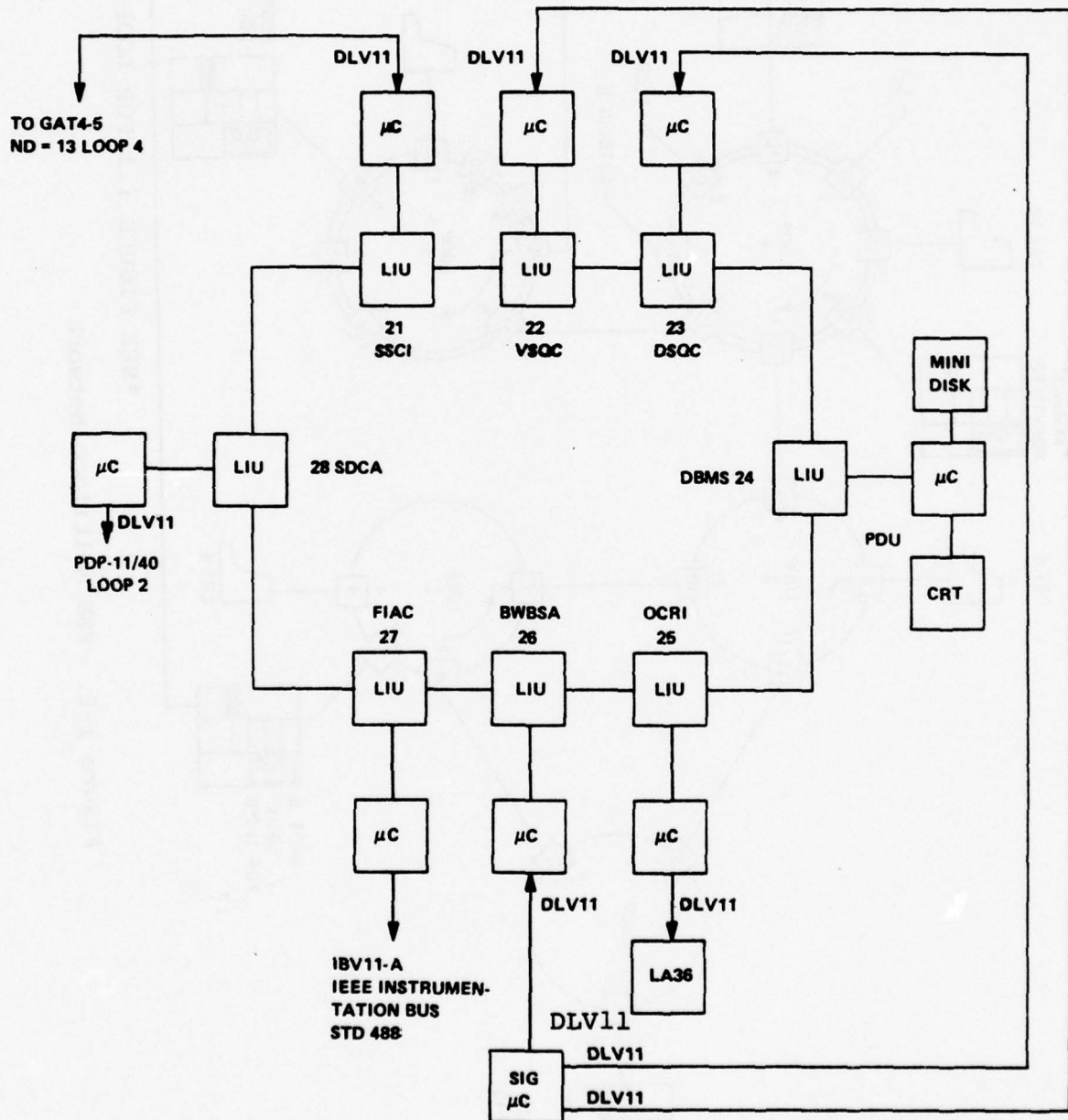
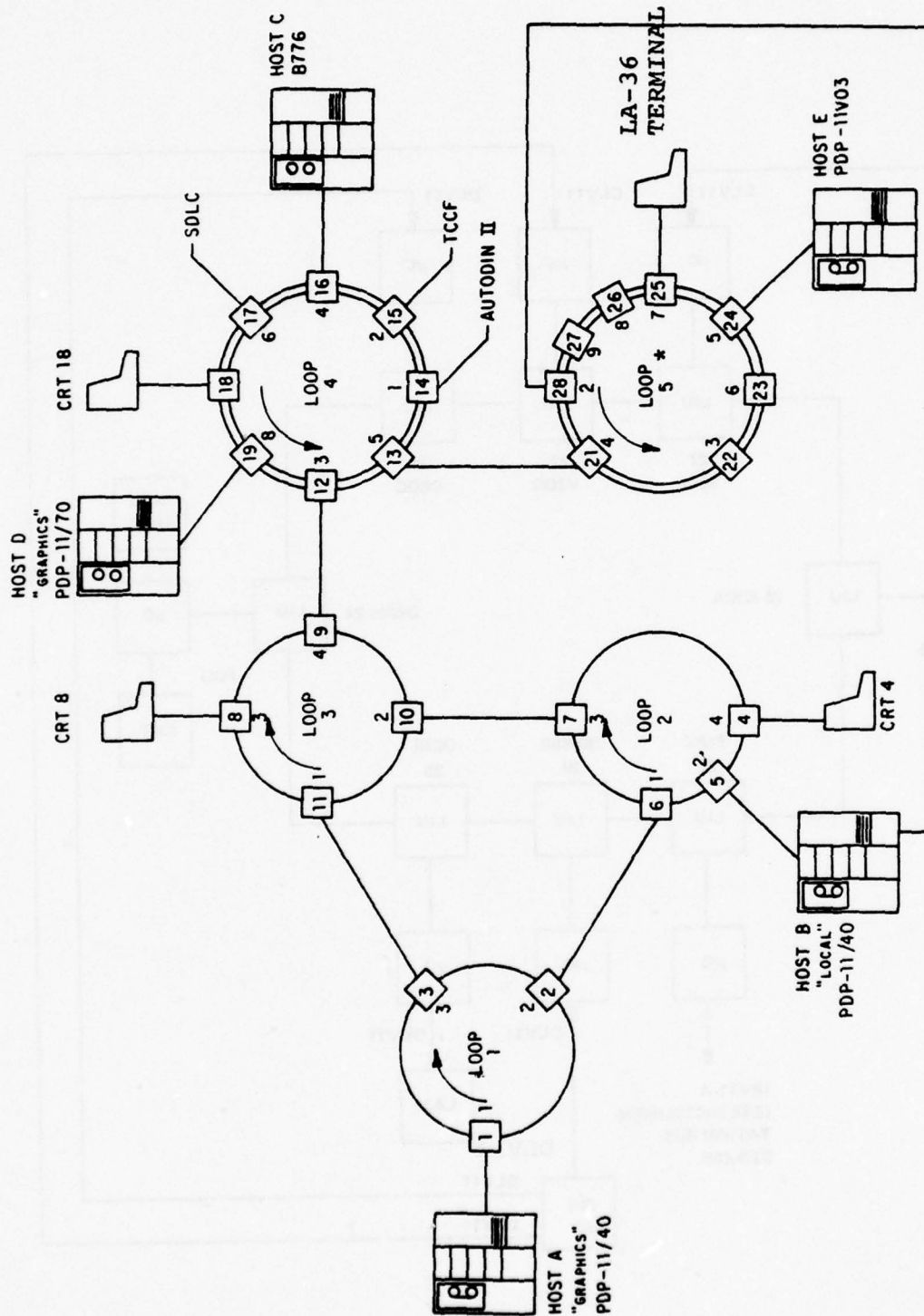


Figure 1-1. MSCDM System Configuration



*SEE FIGURE 1.1 FOR LOOP CONFIGURATION

Figure 1-2. ESM Multiloop Network

APPENDIX 2 System Diskettes

The MSCDM system diskettes are described below.

- Diskette #1: Contains the Loop Loader Utility and the RT-11 operating system. This diskette normally resides in drive 0 (DX0:).
- Diskette #2: Contains the task files for Nodes 21 - 27 (excluding Node 24) and for the SIG
- Diskette #2A: Contains the task files for Node 28.
- Diskette #3: Contains DBMS run file and S/J OS and nodal task file.
- Diskette #3A: Contains the message display file, and status file for Node 24 execution.
- Diskette #4: Used for linking. Contains FORTRAN compiler, MACRO Assembler and libraries. Runs in DX0:
- Diskette #5: Contains the source files for Node 21.
- Diskette #6: Contains the source files for Node 22.
- Diskette #7: Contains the source files for Node 23.
- Diskette #8: Contains the source files for Node 24.
- Diskette #9: Contains the source files for Node 25.
- Diskette #10: Contains the source files for Node 26.
- Diskette #11: Contains the source files for Node 27.
- Diskette #12: Contains the source files for Node 28.
- Diskette #13: Contains the source files for the SIG.
- Diskette #14: Scratch disk.
- Diskette #15: Contains the source files for the Loader Utility and PROM Loader programs.
- Diskette #16: Contains diagnostic programs for the LSI-11 processor and loop hardware.
- Diskette #17: Contains miscellaneous files, IEEE interface and other special programs.
- Diskette #18: Contains DBMS Linker DX0:
- Diskette #18A: Contains DBMS Object files.

Page 001

23-Jul-79						
DXMNF.B.SYS	97	18-Oct-78	TT	.SYS	2	18-Oct-78
DX .SYS	2	18-Oct-78	NL	.SYS	2	18-Oct-78
MACRO .SAV	45	18-Oct-78	STARTF.	COM	1	
PIP .SAV	16	18-Oct-78	DIR	.SAV	17	18-Oct-78
DUP .SAV	17	18-Oct-78	PGLOOP.	SAV	30	
EDIT .SAV	21	18-Oct-78	LINK	.SAV	29	18-Oct-78
DUMP .SAV	7	18-Oct-78	FORTRA.	SAV	128	27-Dec-78
FDMLDR.SAV	32	27-Apr-79	DUMMY	.MAC	1	
16 Files,	447	Blocks				
33 Free Blocks						

decwriter program: DISK2.DIR 23-Jul-79 11:36:02 Page 001

23-Jul-79			
SIGGEN.LDA	36 22-May-79	NODE21.SAV	57 22-May-79
NODE22.SAV	74 22-May-79	NODE23.SAV	73 22-May-79
NODE25.SAV	65 22-May-79	NODE26.SAV	72 22-May-79
NODE27.SAV	75 22-May-79		
7 Files, 452 Blocks			
28 Free Blocks			

decwriter program: DISK2A.DIR 23-Jul-79 11:36:51 Page 001

23-Jul-79
NODE28.SAV 72 22-May-79
1 Files, 72 Blocks
408 Free Blocks

decwriter program: DISK3.DIR 23-Jul-79 11:34:54 Page 001

23-Jul-79			
DXMNSJ.SYS	86	17-May-79	TT .SYS
DX .SYS	2	17-May-79	NL .SYS
PIP .SAV	16	17-May-79	DUP .SAV
DIR .SAV	17	17-May-79	STARTS.COM
NODE24.SAV	93	17-May-79	
9 Files, 236 Blocks			
244 Free Blocks			

Page 001

11:32:13

23-Jul-79

DISK3A.DIR

decwriter program:

23-Jul-79	STAT .COM	1 17-May-79
BDSTAT.FOR	STATUS.MAS	40 17-May-79
BDSTAT.SAV	MSGCON.FOR	3 17-May-79
MASDIR.DAT	MSGCL .DAT	21 17-May-79
MSGCON.SAV	CRASH .COM	1 14-May-79
MSG .DAT	DIR .DAT	9 17-May-79
BACKUP.COM	CKTD .DAT	32 17-May-79
DAT .DAT	TDIR .DAT	9 17-May-79
REPORT.DAT	MASDAT.DAT	2 17-May-79
MASDAT.BAK	CDIR .DAT	9 17-May-79
TNKD .DAT	TNKD .OLD	32 17-May-79
TDIR .OLD	CKTD .OLD	32 17-May-79
CDIR .OLD		
STATUS.DAT		
25 Files, 420 Blocks		
60 Free Blocks		

decwriter program: DISK4.DIR 01-Jul-79 12:00:00 Page 001

```

01-Jul-79
DXMNF.B.SYS      97 08-Feb-79      TT      .SYS      2 21-Feb-79
DX      .SYS      2 21-Feb-79      NL      .SYS      2 21-Feb-79
STARTF.COM      1 21-Feb-79      PIP      .SAV      16 21-Feb-79
DIR      .SAV      17 21-Feb-79      DUMP      .SAV      7 18-Oct-78
LINK      .SAV      29 21-Feb-79      SYSLIB.OBJ  203 27-Dec-78
10 Files, 376 Blocks
104 Free Blocks

```

decwriter program: DISK5.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
NODAL1.OLD	10	23-Jul-79	NODAL2.OLD
NODAL .OLD	6	23-Jul-79	FDM .OLD
NODAL .FOR	8	22-May-79	NODAL1.FOR
FDM .MAC	41	21-May-79	COMP21.COM
LNK21 .COM	1	23-Jul-79	
9 Files, 135 Blocks			
345 Free Blocks			

decwriter program: DISK6.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
NODAL .OLD		NODAL1.OLD	10 04-Apr-79
VSQC .OLD	7 04-Apr-79	FDM .OLD	41 15-Apr-79
NODAL2.OLD	15 16-Apr-79	NODAL .FOR	8 22-May-79
NODAL1.FOR	25 01-May-79	VSQC .FOR	13 22-May-79
FDM .MAC	17 22-May-79	COMP22.COM	1 01-Jul-79
LNK22 .COM	41 21-May-79		
	1 01-Jul-79		
11 Files, 179 Blocks			
301 Free Blocks			

decwriter program: DISK7.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
NODAL .OLD	7 04-Apr-79	NODAL1.OLD	10 04-Apr-79
FDM .OLD	41 12-Apr-79	NODAL2.OLD	25 01-May-79
DSQC .OLD	13 14-May-79	NODAL .FOR	9 21-May-79
NODAL1.FOR	17 22-May-79	DSQC .FOR	12 22-May-79
FDM .MAC	41 21-May-79	COMP23.COM	1 01-Jul-79
LNK23 .COM	1 01-Jul-79		
11 Files, 177 Blocks			
303 Free Blocks			

decwriter program: DISK8.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
M2000 .OLD	6 22-May-79	M4000 .OLD	22 22-May-79
FDM .OLD	40 22-May-79	NODAL .OLD	9 22-May-79
NODALL.OLD	18 22-May-79	M0000 .OLD	19 22-May-79
M1000 .OLD	6 22-May-79	M3000 .OLD	6 22-May-79
M5000 .OLD	21 22-May-79	M6000 .OLD	8 22-May-79
COMP2 .COM	1 17-May-79	COMP2A.COM	1 17-May-79
COMP1 .COM	1 17-May-79	M2000 .FOR	6 17-May-79
M4000 .FOR	22 17-May-79	NODAL .FOR	9 17-May-79
NODALL.FOR	18 17-May-79	M0000 .FOR	19 17-May-79
M1000 .FOR	6 17-May-79	M3000 .FOR	6 17-May-79
M5000 .FOR	21 17-May-79	M6000 .FOR	8 17-May-79
FDM .MAC	40 17-May-79		
23 Files, 313 Blocks			
167 Free Blocks			

decwriter program: DISK9.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
NODAL1.OLD	13 23-Jul-79	NODAL ,OLD	7 08-May-79
FDM .OLD	42 09-May-79	NODAL2,OLD	24 09-May-79
NODAL .FOR	11 21-May-79	NODAL1,FOR	15 22-May-79
FDM .MAC	42 21-May-79	COMP25,COM	1 01-Jul-79
LNK25 .COM	1 01-Jul-79		
9 Files, 156 Blocks			
324 Free Blocks			

decwriter program:

DISK10.DIR

01-Jul-79

12:00:00

Page 001

01-Jul-79			
NODAL1.OLD	10 04-Apr-79	NODAL ,OLD	6 04-Apr-79
FDM .OLD	41 23-Jul-79	NODAL2,OLD	25 01-May-79
BWBSA .OLD	13 01-May-79	NODAL ,FOR	9 21-May-79
NODAL1.FOR	17 22-May-79	BWBSA ,FOR	13 22-May-79
FDM .MAC	41 21-May-79	COMP26.COM	1 01-Jul-79
LNK26 .COM	1 01-Jul-79		
11 Files, 177 Blocks			
303 Free Blocks			

decwriter program: DISK11.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
NODAL1.OLD	10 04-Apr-79	NODAL .OLD	6 25-Apr-79
NODAL2.OLD	25 23-Jul-79	FIAC .OLD	19 06-May-79
FDM .OLD	41 01-May-79	NODAL .FOR	9 21-May-79
NODAL1.FOR	17 21-May-79	FIAC .FOR	17 22-May-79
FDM .MAC	41 21-May-79	COMP27.COM	1 01-Jul-79
LNK27 .COM	1 01-Jul-79		
11 Files, 187 Blocks			
293 Free Blocks			

decwriter program: DISK12.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
NODAL1.OLD	10 04-Apr-79	FDM .OLD	41 23-Jul-79
NODAL .OLD	7 23-Jul-79	NODAL2.OLD	26 23-Jul-79
SDCA .OLD	13 06-May-79	NODAL .FOR	9 21-May-79
NODAL1.FOR	17 21-May-79	SDCA .FOR	14 21-May-79
FDM .MAC	41 21-May-79	COMP28.COM	1 01-Jul-79
LNK28 .COM	1 01-Jul-79		
11 Files, 180 Blocks			
300 Free Blocks			

decwriter program: DISK13,DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
LNK20 .COM	1 07-Mar-79	SIGMAC,OLD	3 12-Apr-79
SIGGEN.OLD	17 01-May-79	SIGGEN,CRT	44 01-May-79
SIGGEN.LDA	36 14-May-79	SIGGEN,FOR	17 21-May-79
SIGMAC.MAC	3 21-May-79	COMP20.COM	1 01-Jul-79
8 Files, 122 Blocks			
358 Free Blocks			

decwriter program: DISK15.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
LDRMAC.OLD	17	01-Feb-79	19
PROM21.MAC	17	01-Feb-79	17
PROM23.MAC	17	01-Feb-79	17
PROM28.MAC	17	01-Feb-79	1
PROM25.MAC	18	08-Feb-79	7
DUMMY .MAC	0	12-Feb-79	16
FDMLDR.FOR	16	22-Apr-79	17
FDMLDR.OBJ	52	27-Apr-79	3
FDMLDR.LST	36	01-Jul-79	
17 Files, 287 Blocks			
193 Free Blocks			

PROM26.MAC
PROM22.MAC
PROM27.MAC
FDMLDR.COM
SIGLDR.MAC
FDMLDR.OLD
LDRMAC.MAC
LDRMAC.OBJ

19 01-Feb-79
17 01-Feb-79
17 01-Feb-79
1 06-Feb-79
7 11-Feb-79
16 27-Apr-79
17 22-May-79
3 27-Apr-79

decwriter program: DISK16.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
PRTCT.MAC	15 01-Feb-79	LSICLK.MAC	4 01-Feb-79
CRTPT.MAC	15 01-Feb-79	LSIMEM.MAC	8 06-Feb-79
LSICPU.MAC	11 06-Feb-79	LIUINT.MAC	10 06-Feb-79
LIUBUF.MAC	12 06-Feb-79	LIURAM.MAC	12 06-Feb-79
DIARCV.MAC	20 17-Mar-79	DIASND.MAC	10 17-Mar-79
EXEML.COM	1 17-Mar-79	LSICPU.OBJ	2 17-Mar-79
LSIMEM.OBJ	1 17-Mar-79	LSICLK.OBJ	1 17-Mar-79
LIUINT.OBJ	2 17-Mar-79	LIUBUF.OBJ	2 17-Mar-79
LIURAM.OBJ	2 17-Mar-79	DIARCV.OBJ	3 17-Mar-79
DIASND.OBJ	2 17-Mar-79	CRTPT.OBJ	3 17-Mar-79
PRTCT.OBJ	3 17-Mar-79	LSICPU.SAV	2 17-Mar-79
LSIMEM.SAV	2 17-Mar-79	LSICLK.SAV	2 17-Mar-79
LSICPU.LDA	1 17-Mar-79	LSIMEM.LDA	1 17-Mar-79
LSICLK.LDA	1 17-Mar-79	LIURAM.SAV	3 17-Mar-79
LIUBUF.SAV	4 17-Mar-79	LIUINT.SAV	3 17-Mar-79
CRTPT.SAV	5 17-Mar-79	PRTCT.SAV	5 17-Mar-79
DIARCV.SAV	4 17-Mar-79	DIASND.SAV	3 17-Mar-79
DIR.DIR	3 17-Mar-79		
35 Files, 178 Blocks			
302 Free Blocks			

decwriter program: DISK17.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
SIEEE .MAC	10 03-Feb-79	RIEFE .MAC	15 03-Feb-79
PGMAC .MAC	17 12-Feb-79	PGLOOP.COM	1 12-Feb-79
PGLOOP.FOR	9 12-Feb-79	WRTLP .FOR	2 12-Feb-79
SIEEE .OBJ	2 26-Apr-79	RIEEE .OBJ	3 26-Apr-79
SIEEE .SAV	3 26-Apr-79	RIEEE .SAV	3 26-Apr-79
PGLOOP.SAV	30	PGLOOP.OBJ	52
WRTLP .OBJ	12	PGMAC .OBJ	3

14 Files, 162 Blocks
318 Free Blocks

decwriter program: DISK13.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79			
DXMNF.B.SYS	97	18-Oct-78	PIP .SAV
DX .SYS	2	18-Oct-78	SYSLIB.OBJ
NL .SYS	2	01-Feb-79	STARTF.COM
LINK .SAV	29	18-Oct-78	
7 Files, 350 Blocks			
130 Free Blocks			

16 18-Oct-78
203 27-Dec-78
1

decwriter program: DSK18A.DIR 01-Jul-79 12:00:00 Page 001

01-Jul-79
 NODAL .OBJ 13 17-May-79 NODALL.OBJ 57 17-May-79
 FDM .OBJ 5 17-May-79 M0000 .OBJ 46 17-May-79
 M1000 .OBJ 17 17-May-79 M2000 .OBJ 16 17-May-79
 M3000 .OBJ 14 17-May-79 M4000 .OBJ 55 17-May-79
 M5000 .OBJ 56 17-May-79 M6000 .OBJ 21 17-May-79
 LNK24 .COM 1 17-May-79 MAP .MAP 20 17-May-79
 DSK18 .DIR 1 01-Jul-79
 13 Files, 340 Blocks
 140 Free blocks

Page 001

[illegible]

decrwiter program: FLP2.DIR 23-Jul-79 13:47:31 Page 001

23-Jul-79			
RKMNSJ.SYS	86 14-Aug-77	RKMNFB.SYS	96 14-Aug-77
RKMNXM.SYS	106 14-Aug-77	RKMNSJ.BL	82 14-Aug-77
DMMNSJ.SYS	87 14-Aug-77	ODT .OBJ	9 14-Aug-77
6 Files, 466 Blocks			
14 Free blocks			

Page 001

[illegible]

ewriter program: FLP4.DIR 23-Jul-79 13:48:25 Page 001

23-Jul-79					
DMNFB.SYS	98	14-Aug-77	DMXXM.SYS	108	14-Aug-77
DXMFB.SYS	97	14-Aug-77	DXMNM.SYS	107	14-Aug-77
SYE.SAV	50	14-Aug-77	NLX.SYS	2	14-Aug-77
PC.SYS	2	14-Aug-77	PCX.SYS	2	14-Aug-77
DEMOFL.FOR	2	28-Jan-77			
9 Files, 468 Blocks					
12 Free blocks					

decwriter program: FLP5.DIR 23-Jul-79 13:48:43 Page 001

23-Jul-79			
DXMNSJ.BL	83 14-Aug-77	DTMNSJ.SYS	86 14-Aug-77
DTMNSJ.SYS	96 14-Aug-77	DTMNSJ.BL	82 14-Aug-77
DSMNSJ.SYS	86 14-Aug-77	RF .MAC	6 14-Aug-77
DM .MAC	19 14-Aug-77	DEMOSP.MAC	11 01-Aug-77
8 Files, 469 blocks			
11 Free blocks			

decwriter program: FLP6.DIR 23-Jul-79 13:49:01 Page 001

23-Jul-79			
DSMNXM.SYS	96	14-Aug-77	DSMNXM.SYS
OPMNSJ.SYS	86	14-Aug-77	DPMNFB.SYS
DP .MAC	9	14-Aug-77	MUBRTE.OBJ
MUBTAB.OBJ	1	04-May-77	MUBZNL.OBJ
CT .MAC	32	14-Aug-77	TM .MAC
MMHD .SYS	4	14-Aug-77	MMHDX .SYS
PAT .SAV	7	14-Aug-77	
13 Files, 468 Blocks			
12 Free blocks			

106	14-Aug-77
97	14-Aug-77
1	04-May-77
1	04-May-77
24	14-Aug-77
4	14-Aug-77

decwriter program: FLP7DIR 23-Jul-79 13:49:23 Page 001

23-Jul-79			
KMON .MAC	118 14-Aug-77	USR .MAC	59 14-Aug-77
RMONSJ.MAC	56 14-Aug-77	RMONFB.MAC	138 14-Aug-77
EL .MAC	18 14-Aug-77	LP .MAC	7 14-Aug-77
RK .MAC	7 14-Aug-77	DT .MAC	7 14-Aug-77
DS .MAC	7 14-Aug-77	PC .MAC	6 14-Aug-77
ERRUTL.SAV	6 14-Aug-77	MACFST.SAV	45 14-Aug-77
12 Files, 474 Blocks			
6 Free blocks			

decwriter program: FLP8.DIR 23-Jul-79 13:49:46 Page 001

[illegible]

decwriter program: FLP9.DIR 23-Jul-79 13:50:13 Page 001

23-Jul-79			
SYSMAC.MAC	36 01-Aug-77	BATCH .SAV	25 14-Aug-77
MACBK .SAV	52 14-Aug-77	PSE .SAV	13 14-Aug-77
SYSGEN.SAV	32 14-Aug-77	SYSGEN.CND	83 08-Aug-77
SYSTBL.CND	26 08-Aug-77	VTMAC .MAC	7 01-Aug-77
VTHDLR.OBJ	8 14-Aug-77	SYSF4 .OBJ	39 14-Aug-77
MDUP .SAV	9 14-Aug-77	MDUP .MM	48 14-Aug-77
MDUP .MT	48 14-Aug-77	MBOOT .BOT	1 14-Aug-77
MSBOOT.BOT	3 14-Aug-77	STARTF.COM	1 10-Jun-77
STARTX.COM	1 10-Jun-77	SJ .MAC	1 01-Aug-77
FB .MAC	1 01-Aug-77	XM .MAC	1 01-Aug-77
SYSDEV.MAC	1 24-Sep-76	CT .SYS	5 14-Aug-77
CTX .SYS	6 14-Aug-77	TECO .SAV	27 14-Aug-77
DEMOX1.MAC	5 01-Aug-77		
25 Files, 479 Blocks			
1 Free Blocks			

APPENDIX 3 Startup Procedures

MSCDM STARTUP PROCEDURES

1. Power up all equipment (User Manual)
2. Insert diskette #1 into drive 0
3. Type DX (Return), system will now boot strap DEC F/B operating system which is used for loading MSCDM
4. Type in Date and Time
DA DD-MON-YY
TI HH:MM:SS
5. Start loader by typing .R FDMLDR
6. Press clear switch
7. Select MODE 1 (Normal Load), the loader will ask you to insert Diskette with File SIGGEN.LDA
- ** So insert Diskette #2 and press return the loader will load nodes SIG, 21, 22, 23, 25, 26, 27 from this diskette. It will ask you to insert diskette with file NODE28.SAV. So now insert Diskette #2A and press return. Loader finishes loading and starting nodes, also clears the CRT screen.
8. Ensure all LED's are on (Redo if not).
9. Turn PDU DC power supply OFF then ON
10. Insert Diskette #3 into Drive 0
11. Insert Diskette #3A into Drive 1
12. Type DX (return)
System Bootstraps DEC S/J Operating system which is used for the applications program (Node 24)
13. Type the following commands:
 - Copy STATUS.MAS STATUS.DAT
 - RESET
 - R NODE 24

14. MSCDM is now running, the CRT will not be used from now on.

Note 1: After you have typed "ABORT" on MSCDM's LA36, you will notice a "STOP--" appear on CRT and the PDU will return to operating system.

** If you made a file updates (Mode 4) you should invoke a backup command file, so to save these changes. You do so by typing.@Backup.

Note 2: Should system crash, you should recover the data bases before restarting. To do so typing. @Crash

Note 3: Remember you are running S/J operating system, so for any reason if you change diskette in drive 0, you must reboot the new operating system...

PAGE 001

11:38:50

23-JUL-79

CRASH.COM

Program:

decwriter

COPY CDR.OLD CDR.DAT
COPY TDR.OLD TDR.DAT
COPY EXT.D.OLD EXT.DAT
COPY INK.D.OLD INK.DAT

PAGE 001

11:39:12

23-JUL-79

BACKUP.COM

decuriter program:

COPY CDR.DAT CDR.OLD
COPY TDR.DAT TDR.OLD
COPY CNTR.DAT CNTR.OLD
COPY INKD.DAT INKD.OLD

USING THE PDU PROGRAM DEVELOPMENT SYSTEM
FOR SOFTWARE GENERATION

Start-Up Procedures

1. Turn on main breaker at rear of PDU cabinet.
2. Turn on CRT switch on (right side of CRT).
3. Insert an operating diskette #1 into drive (0) (left).
4. Turn on all (three) switches on front of PDU.
5. A "\$" should appear on CRT, type "DX" (return).
6. System should then "Boot strap displaying startup commands".
7. Type in date (.DA dd-mon-yy).
8. Type in time (.TI hh:mm:ss).
9. System is now ready to be used as a program develop system.
10. Diskette #1 contains the EDITOR, FORTRAN, MACRO system files
this disk will be used to do all system functions except
linking of FORTRAN object files.
11. Diskette #4 is used as a linking disk. It is inserted into
Drive 0 and invoked by a user command file to link your
FORTRAN object files.

APPENDIX 4 Interface Data

MSCDM REGISTER BITSOPREG/INTERFACE

LST
BIT 0- PRGM I/O
BIT 1- RD
BIT 2- RS
BIT 3- WD
BIT 4- WC
BIT 5- SET INT. REQ

LIU/COMMAND REGISTER

LST
BIT 0- SEL ACRAM
BIT 1- SEL LDACTR
BIT 2-
BIT 3- SEL RDBUFADR
BIT 4- SEL MODSTAT
BIT 5- SEL INOROUTBUF
BIT 6- SEL IN/OUT
BIT 7- SEL 0/1

LIU/STATUS BYTE(0)

LST
BIT 0- IB0 FULL
BIT 1- IB1 FULL
BIT 2- IB0 OV-FL
BIT 3- IB1 OV-FL
BIT 4- PRSW
BIT 5- BKSW
BIT 6- WT DETECT
BIT 7- PRSW LATCH

LIU/STATUS BYTE(1)

LST
BIT 0- CRC 0 OK
BIT 1- CRC 1 OK
BIT 2- OB0 FULL
BIT 3- OB1 FULL
BIT 4- BDSLNSWPR
BIT 5- BDSLNSWBK
BIT 6- WRITE COMMAND

AD-A078 392

BURROUGHS CORP PAOLI PA FEDERAL AND SPECIAL SYSTEMS GROUP F/6 9/2
SOFTWARE MAINTENANCE MANUAL FOR THE MODULAR SYSTEM CONTROL DEVE--ETC(U)
NOV 79 DCA100-76-C-0083

UNCLASSIFIED

66158

SBIE-AD-E100 314

NL

4 OF 4
ADA
078392



END
DATE
FILMED
-80
DDC

MSCDM REGISTER BITS CONT.LIU/MODSTAT BYTE

LST
 BIT 0- SET OB0 FULL
 BIT 1- SET OB1 FULL
 BIT 2- SET BDSLNSW PRIMARY
 BIT 3- SET BDSLNSW BACKUP
 BIT 4- SET WRITE COMMAND
 BIT 5- CLR BDSLNSW PRIMARY
 BIT 6- CLR BDSLNSW BACKUP

BLIUI CSR REGISTER

LST
 BIT 0- LIU CLEAR=1,NORMAL=0
 BIT 5- TIME-OUT OCCURED DURING DMA XFER
 BIT 6- ENABLE DONE BIT INTERRUPT
 BIT 7- STATUS OF CURRENT I/O OPERATION
 BIT14- ENABLE LIU INTERRUPTS
 BIT15- LIU REQUESTS AN INTERRUPT

INTERFACE/LIU COMMANDS

COMMAND	MST	LST	DEC	N/C
WCR: RS(0)	00010001	00000000	4352	RS
WCR: RS(1)	00010001	10000000	4488	RS
WCR: RDBUFADR IN / 0	00010001	00001000	4360	RD
WCR: RDBUFADR IN / 1	00010001	10001000	4488	RD
WCR: RDBUFADR OUT / 0	00010001	01001000	4424	RD
WCR: RDBUFADR OUT / 1	00010001	11001000	4552	RD
WCR: MODSTAT	00010001	00010000	4368	WD
WCR: LDACTR	00010001	00000010	4354	WD
WCR: ACRAM	00010001	00000001	4353	RD/WD
WCR: INBUF 0	00010001	00100000	4384	RD/WD
WCR: INBUF 1	00010001	10100000	4512	RD/WD
WCR: OUTBUF 0	00010001	01100000	4448	RD/WD
WCR: OUTBUF 1	00010001	11100000	4576	RD/WD
RS : PRGM	00000101	00000000	1280	
RD : PRGM	00000011	00000000	768	* CSR
RD : DMA	00100010	00000000	8704	+ CSR
WD : PRGM	00001001	00000000	2304	* CSR
WD : DMA	00101000	00000000	10240	+ CSR
WC : PRGM	00010001	00000000	4352	CMD

WCR: WRITE COMMAND REGISTER

RS : READ STATUS

RD : READ DATA (PROGRAM I/O OR DMA)

WD : WRITE DATA (PROGRAM I/O OR DMA)

WC : WRITE COMMAND REGISTER LIU

MSCDM ACRAM DATA

NREAD=(6) 0110
 DREAD=(4) 0100
 NULL =(7) 0111
 WTKN=(0) 0000

<u>LOCATION</u>	<u>DR</u>	<u>NR</u>	<u>WT</u>
NODE 20: SIG	"012	00	000
NODE 21: SSCI	"004	00	255
NODE 22: VSQC	"003	00	255
NODE 23: DSQC	"006	00	255
NODE 24: DBMS	"005	00	255
NODE 25: OCRI	"007	00	255
NODE 26: BWBSA	"010	00	255
NODE 27: FIAC	"011	00	255
NODE 28: SDCA	"002	00	255

NOTE: sig interlocks to loop though NODE 26(DLV11).

MSCDM INTERFACE BOARD DATA

<u>LABEL</u>	<u>TYPE</u>	<u>ADDRESS</u>	<u>VECTOR</u>	<u>BAUD</u>	<u>TRANS/RECV</u>	<u>NODE</u>	<u>B/P POS.</u>
(A)	DLV11-F	177560	60/62	9600	EIA/EIA	23	3 A-B
(B)	DLV11	177560	60/62	2400	PAS/PAS	28	3 A-B
(C)	DLV11	177560	60/62	9600	ACT/ACT	24	B1 C-D
(D)	DLV11-F	177510	310/312	9600	EIA/EIA	SIG	2 C-D
(E)	DLV11	177560	60/62	300	ACT/ACT	25	3 A-B
(F)	DLV11-F	175610	300/302	9600	EIA/EIA	SIG	2 A-B
(G)	DLV11-F	177410	320/322	9600	EIA/EIA	SIG	3 A-B
(H)	DLV11-F	177560	60/62	9600	EIA/EIA	26	3 A-B
(I)	DLV11-F	177560	60/62	9600	EIA/EIA	22	3 A-B
(J)	DLV11	177560	60/62	300	ACT/ACT	SIG	4 C-D
(K)	DLV11-F	177560	60/62	9600	EIA/EIA	21	3 A-B
(L)	IBV11-A	177560	420	N/A	IEEE-488	27	3 A-B

APPENDIX 5

GLOSSARY OF ACRONYMS

The interdisciplinary nature of the present study is emphasized by the large number of different acronyms, from diverse sources, that appear in the discussion. The following is a partial list of some of the relevant acronyms that have been identified. It also serves as a glossary.

ACAS	AUTOVON Centralized Alarm System
ACOC	Area Communications Operations Center
ADM	Adaptive Delta Modulation
ADO	Burroughs Advanced Development Organization
ASC	Automatic Swtiching Center (AUTODIN):
ASCII	American Standard Code for Information Interchange
ASCC	AUTODIN Station Control Console
ASSC	AUTODIN Station Supervisory Console
ASU	Alarm Scanner Unit
ATEC	Automated Tech Control
AVIE	AUTOVON Information and Evaluation Network
BARS	Buffered Automatic Reporting System
BBSA	Baseband Signal Analysis

BDLC	Burroughs Data Link Control
BLIUI	Bus Loop Interface Unit Interface
BWBSA	Combined functions of BBSA and WBSA
CCI	Command and Control Interpreter
CPU	Central Processor Unit
CRT	Cathode Ray Tube
DCA	Defense Communications Agency
DCAOC	Defense Communications Agency Operations Center
DCEC	Defense Communications Engineering Center
DCS	Defense Communications System
DBMS	Data Base Management Service
DDMS	Digital Distortion Monitoring Subsystem
DMA	Direct Memory Access
DSQC	Digital Service Quality Control
ESM	Exploratory System Control Model
ESMD	Exploratory System Control Model Development
FDM	Feasibility Development Model
FIAC	Fault Isolation and Analysis Coordination
IO	Input/Output
LA-36	DEC Hard Copy Terminal
LIU	Loop Interface Unit
MSCDM	Modular System Control Development Model
OCRI	Operator Control and Report Interface
PDU	Program Development Unit
PROM	Programmable Read Only Memory

RAM	Random Access Memory	RAM
SDCA	Switch Data Collection/Analysis	SDCA
VSQC	Voice Service Quality Control	VSQC
WBSA	Wideband Signal Analysis	WBSA
WT	Write Token	WT



Burroughs Corporation

Federal and Special Systems Group

Paoli, Pennsylvania 19301